

ASM25512C

DATA SHEET

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ASM25512C — *VERY LOW-COST VOICE SYNTHESIZER WITH 4-BIT MICROPROCESSOR*

1.0 General Description

The ASM25512C is very low cost voice synthesizer with 4-bit microprocessor. It has various features including 4-bit ALU, ROM, RAM, I/O ports, timers, clock generator, watchdog timer(WDT), voice synthesizer, etc. It consists of 22 instructions in the device. With CMOS technology and halt function can minimize power dissipation. Its architecture is similar to RISC, with two stages of instruction pipeline. It allows all instructions to be executed in a single cycle, except for program branches and data table read instructions (which need two instruction cycles).

1.1 Feature

- ◆ Single power supply can operate from 2.4V through 5.5V
- ◆ Internal Program ROM: 4K x 10-bit
- ◆ 1 sets of 20-bit DPR can access up to 768K x 10 bits data memory space
- ◆ Data Registers:
 - 96 x 4-bit data RAM (00-1Fh plus 40h-7Fh)
 - Unbanked special function registers (SFR) range: 20h-3Fh
- ◆ I/O Ports:
 - PRA: 4-bit I/O Port A (2Bh)
 - PRB: 4-bit Output Port B (2Dh)
 - PRC: 4-bit Input Port C (2Fh)
- ◆ On-chip clock generator: Resistive Clock Drive(**RM**)
- ◆ Timer: 1
 - Timer0: a 9-bit auto-reload timer/counter
- ◆ Stack: 2-level subroutine nesting
- ◆ HALT and Release from HALT function to reduce power consumption
- ◆ Watch Dog Timer (**WDT**)
- ◆ Instruction: 1-cycle instruction except for table read and program branches which are 2-cycles
- ◆ Number of instruction: 22
- ◆ The Voice function can be implemented by microprocessor instruction
 - One 8-bit COUT output for ASM25512C

FIGURE 1.1 : Block Diagram of ASM25512C

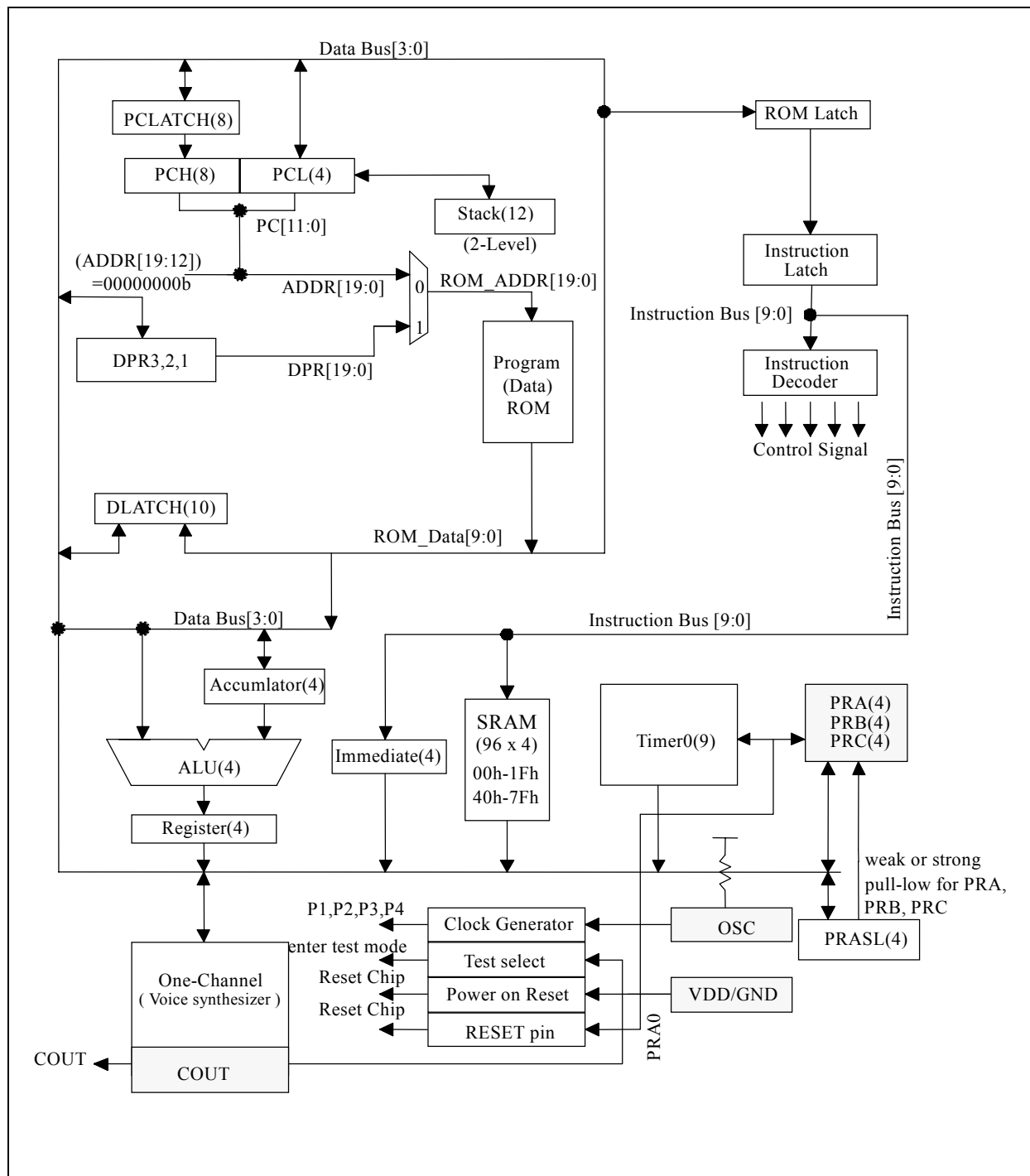
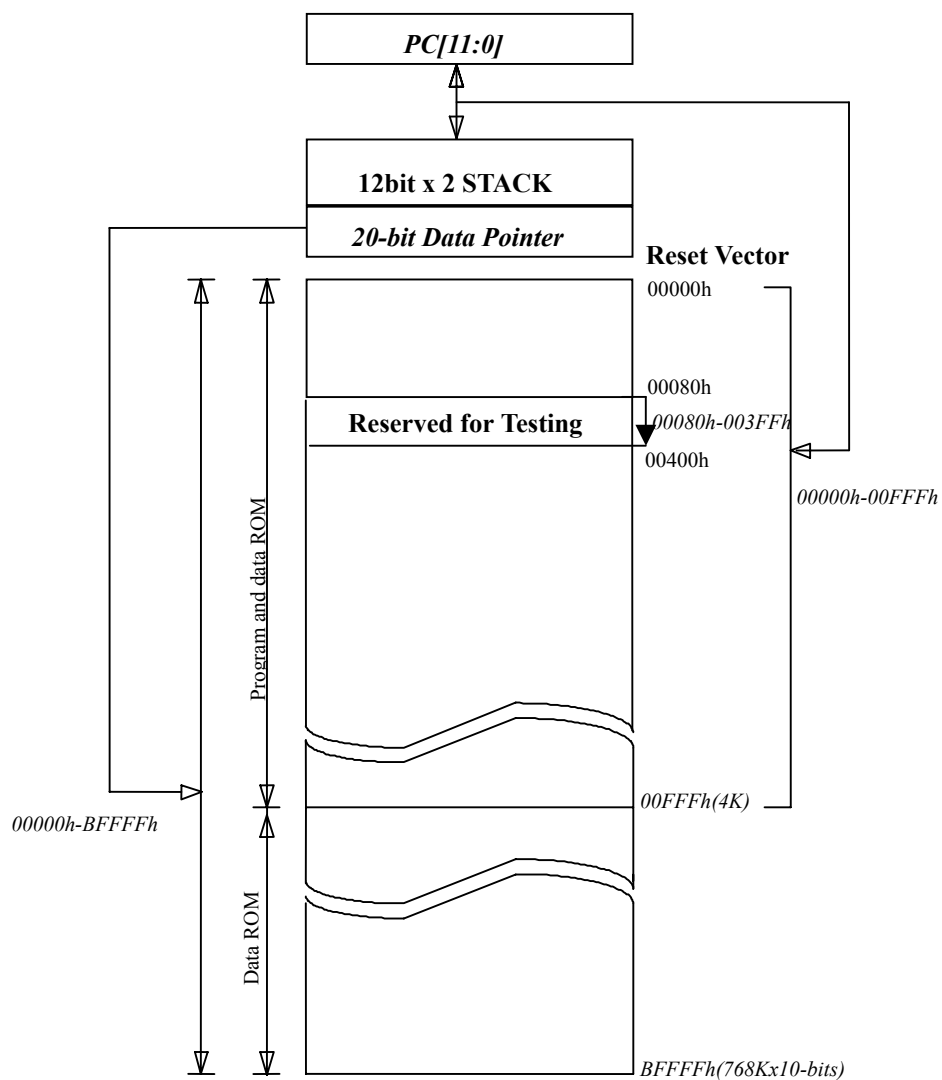
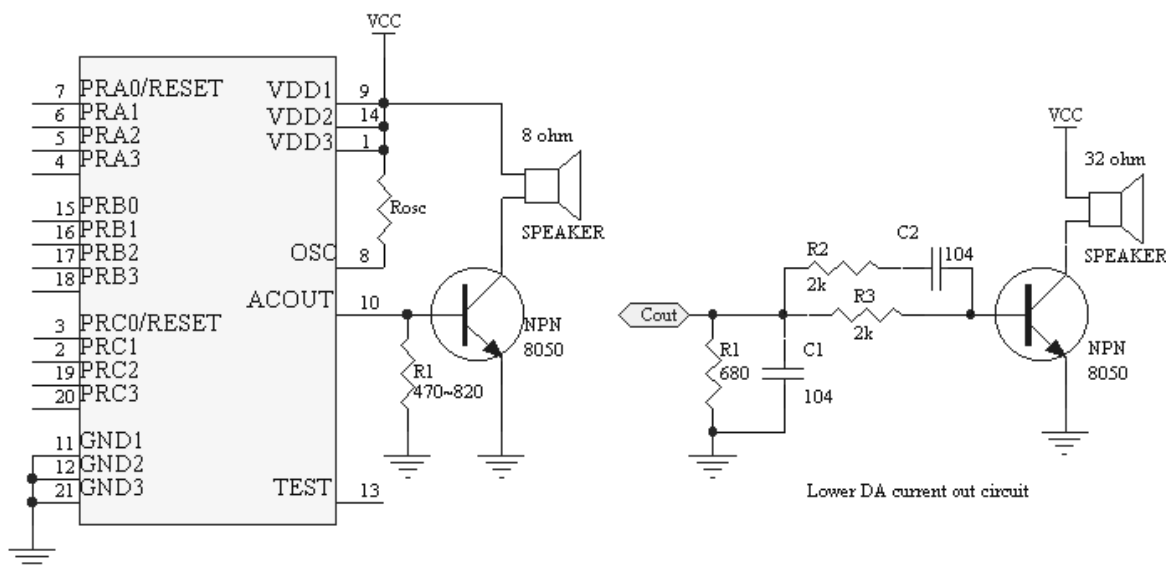


FIGURE 1.2 : External ROM Map of ASM25512C


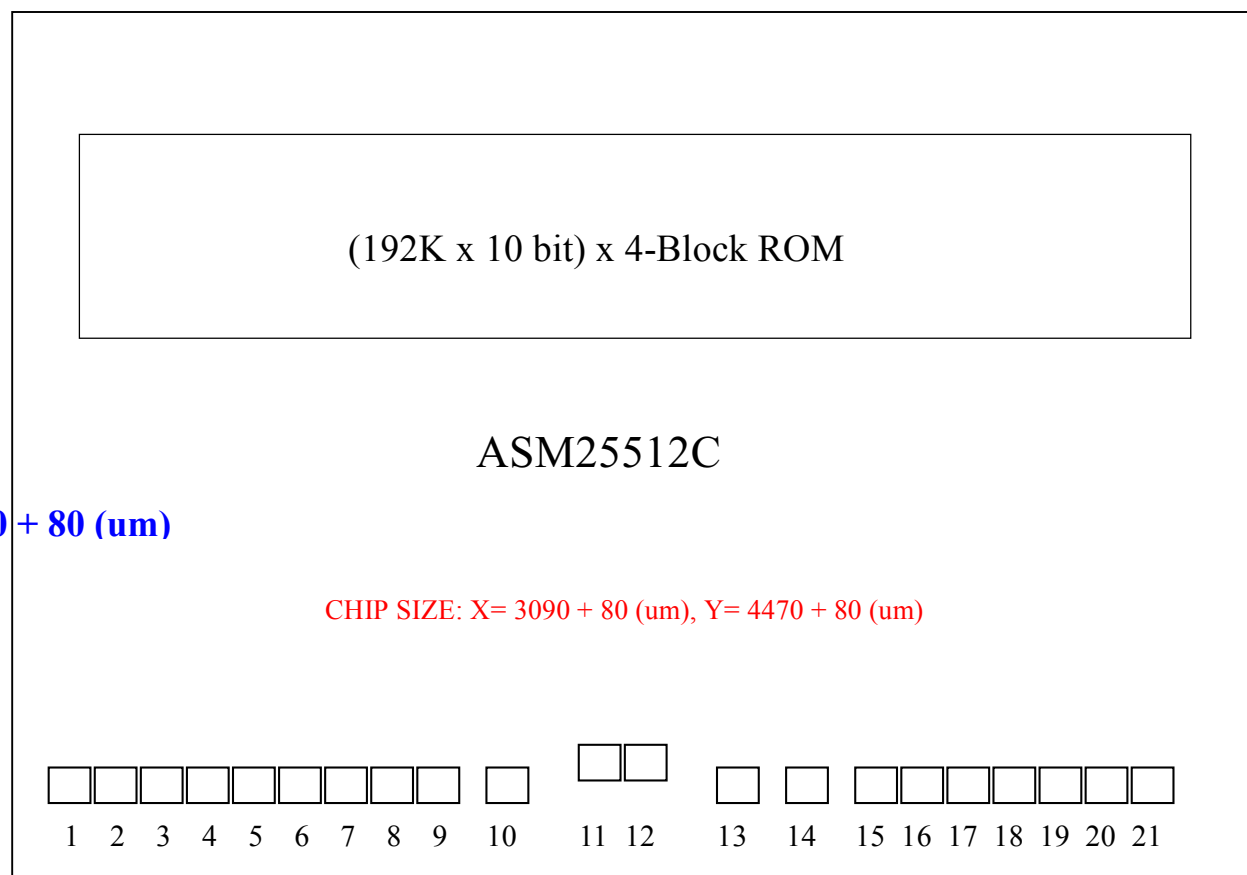
1.2 Pin-Out

ASM25512C Pin-Out			
VDD3	I	-	Third Power supply during operation
PRC1	I	STI Std./O.D.	Input port with programmable strong pull-low or weak pull-low or fix-input-floating capability
PRC0/RESET	I	STI Std./O.D.	Input port with programmable strong pull-low or weak pull-low or fix-input-floating capability <i>Mask option selected as an external RESET pin with weak pull-low capability</i>
PRA3-1	I/O	STI Std./O.D.	I/O port with programmable strong pull-low or weak pull-low or fix-input-floating capability Output type with standard or Open-Drain output
PRA0/RESET	I/O	STI Std./O.D.	I/O port with programmable strong pull-low or weak pull-low or fix-input-floating capability Output type with standard or Open-Drain output <i>Mask option selected as an external RESET pin with weak pull-low capability</i>
OSC	I	-	RM mode Oscillator input
VDD1	I	-	First Power supply during operation
COUT	O	-	Current Output of Audio
GND1	I	-	First Circuit Ground Potential
GND2	I	-	Second Circuit Ground Potential
TEST	O	-	Enter Test Mode. (TEST = High)
VDD2	I	-	Second Power supply during operation
PRB0-3	O	Std./O.D.	Output type with standard or Open-Drain output
PRC2-3	I	STI Std./O.D.	Input port with programmable strong pull-low or weak pull-low or fix-input-floating capability
GND3	I	-	Third Circuit Ground Potential

1.3 Application circuit



1.4 Bonding Diagram



$X = 3090 + 80 \text{ (um)}$

Substrate must be connected to GND.

ASM25512C Pad Location				Chip Size: $X = 3090 + 80 \text{ (um)}$, $Y = 4470 + 80 \text{ (um)}$			
PAD #	PAD Name	X	Y	PAD #	PAD Name	X	Y
1	VDD3	-1445.96	-2123.6	11	GND1	38.72	-2118.08
2	PRC1	-1325.24	-2123.6	12	GND2	118.72	-2118.08
3	PRC0	-1202.92	-2123.6	13	TEST	319.68	-2123.6
4	PRA3	-1080.6	-2123.6	14	VDD2	575.32	-2123.6
5	PRA2	-958.28	-2123.6	15	PRB0	714.48	-2123.6
6	PRA1	-835.96	-2123.6	16	PRB1	836.8	-2123.6
7	PRA0/RESET	-713.64	-2123.6	17	PRB2	959.12	-2123.6
8	OSC	-591.32	-2123.6	18	PRB3	1081.44	-2123.6
9	VDD1	-416.04	-2123.6	19	PRC2	1203.76	-2123.6
10	COUT	-162.24	-2123.6	20	PRC3	1326.08	-2123.6
				21	GND3	1449.36	-2123.6

1.5 DC Characteristics for ASM25512C

SYMBOL	PARAMETER		VDD	MIN.	TYP.	MAX.	UNIT	CONDITION
VDD	OPERATING VOLTAGE			2.4	3	5.5	V	depending on Freq.
I _{sb}	SUPPLY CURRENT	STANDBY	3			1	uA	4MHz, RM in HALT Mode
			5			1		
I _{op}		OPERATING	3		2		mA	4MHz, RM IO Floating
			5		7			
I _{ih}	INPUT CURRENT /Internal pull low		3		3		uA	4MHz, RM in HALT Mode (IO Ports with weak pull-high pull-low)
			5		9			
			5		-5.2			
I _{oh}	OUTPUT HIGH CURRENT		3		-3		mA	4MHz, RM (IO ports)
			5		-8			
I _{ol}	OUTPUT LOW CURRENT		3		7			
			5		20			
C _{out}	DA CURRENT OUT (FULL SCALE)		3		4			
			5		5.2			
dF/F	FREQUENCY STABILITY			-10		10	%	$\frac{F_{osc}(3v- 2.4v)}{F_{osc} (3v)}$
dF/F	Fosc VARIATION			-20		20	%	VDD=3V, Rosc=820k, 4MHz

FIGURE 1.3 : Frequency Range for R_{osc} in RM mode

Resistor(k ohm)	1000	760	560	390
3v Freq.(MHz)	3.18	4.3	6.02	7.89

