M81720FP

HIGH VOLTAGE HALF BRIDGE DRIVER

DESCRIPTION

M81720FP is high voltage Power MOSFET and IGBT module driver for half bridge applications.

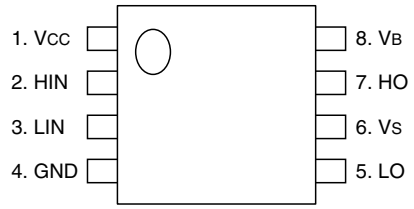
FEATURES

- FLOATING SUPPLY VOLTAGE 600V
- OUTPUT CURRENT ..... +120mA/–250mA (min)
- HALF BRIDGE DRIVER
- SOP-8 PACKAGE

APPLICATIONS

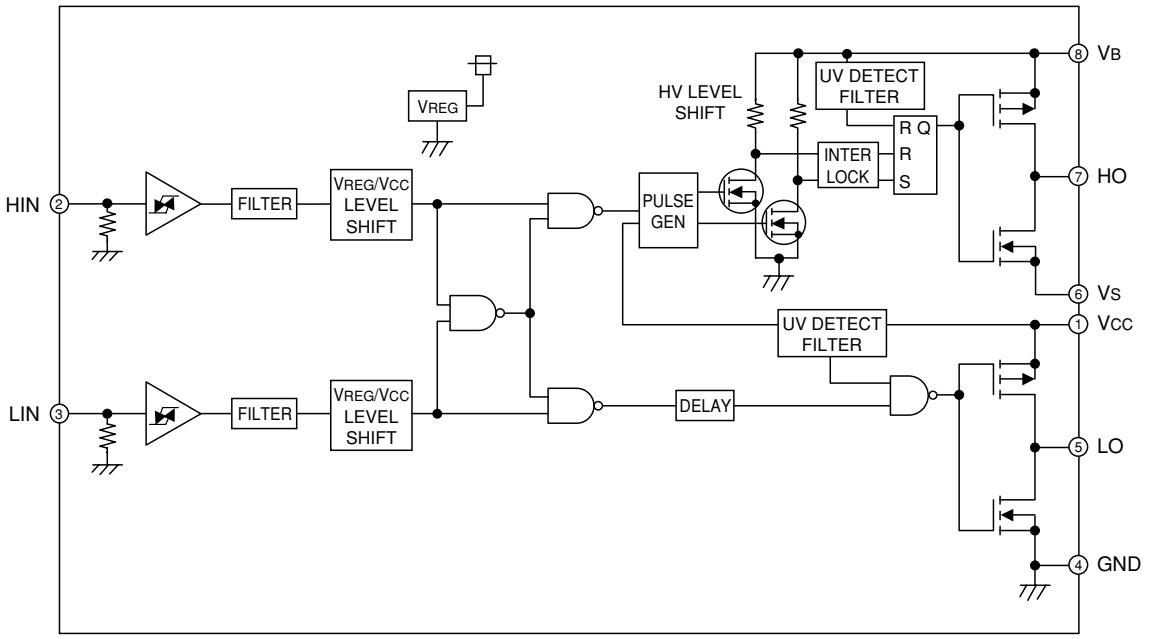
MOSFET and IGBT module inverter driver for Automotive, PDP, HID lamp, refrigerator, air-conditioner, washing machine, AC-servomotor and general purpose.

PIN CONFIGURATION (TOP VIEW)



Outline:8P2S

BLOCK DIAGRAM



MITSUBISHI ELECTRIC

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C unless otherwise specified)

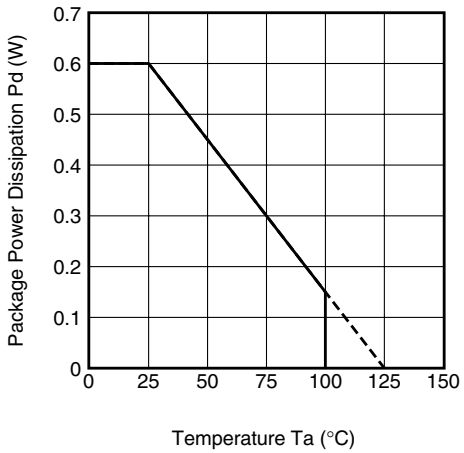
Symbol	Parameter	Test conditions	Ratings	Unit
V_B	High Side Floating Supply Absolute Voltage		–0.5 ~ 624	V
V_S	High Side Floating Supply Offset Voltage		–5 ~ 600	V
V_{BS}	High Side Floating Supply Voltage	$V_{BS} = V_B – V_S$	–0.5 ~ 24	V
V_{HO}	High Side Output Voltage		V_S–0.5 ~ V_B+0.5	V
V_{CC}	Low Side Fixed Supply Voltage		–0.5 ~ 24	V
V_{LO}	Low Side Output Voltage		–0.5 ~ V_{CC}+0.5	V
V_{IN}	Logic Input Voltage	HIN, LIN	–0.5 ~ V_{CC}+0.5	V
Pd	Package Power Dissipation	Ta = 25°C, On Board	0.6	W
$K\theta$	Linear Derating Factor	Ta > 25°C, On Board	6.0	mW/°C
Rth(j-c)	Junction-Case Thermal Resistance		50	°C/W
Tj	Junction Temperature		–20 ~ 125	°C
Topr	Operation Temperature		–20 ~ 100	°C
Tstg	Storage Temperature		–40 ~ 125	°C

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V_B	High Side Floating Supply Absolute Voltage		V_S+10	—	V_S+20	V
V_S	High Side Floating Supply Offset Voltage		0	—	500	V
V_{BS}	High Side Floating Supply Voltage	$V_{BS} = V_B – V_S$	10	—	20	V
V_{HO}	High Side Output Voltage		V_S	—	V_B	V
V_{CC}	Low Side Fixed Supply Voltage		10	—	20	V
V_{LO}	Low Side Output Voltage		0	—	V_{CC}	V
V_{IN}	Logic Input Voltage	HIN, LIN	0	—	7	V

* For proper operation, the device should be used within the recommended conditions.

THERMAL DERATING FACTOR CHARACTERISTIC (MAXIMUM RATING)

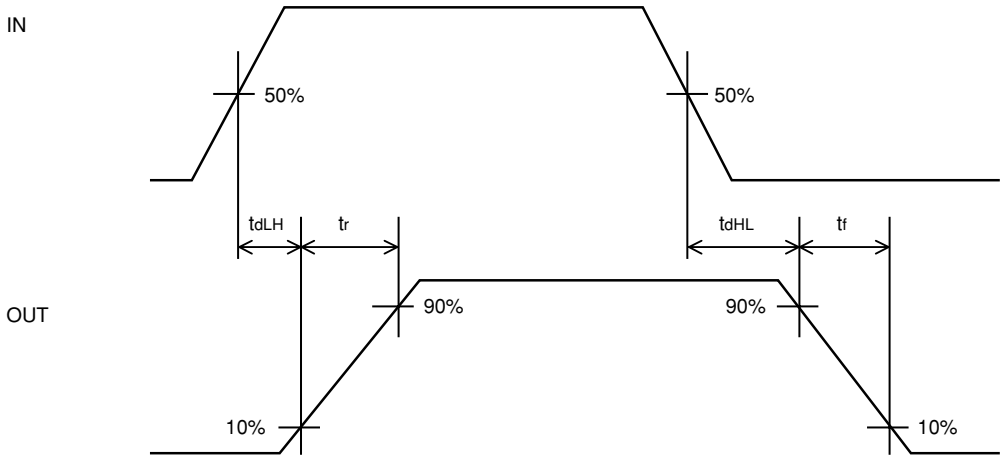


ELECTRICAL CHARACTERISTICS (Ta = 25°C, V_{CC}=V_{BS}(=V_B-V_S)=15V, unless otherwise specified)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.*	Max.	
I_{FS}	Floating Supply Leakage Current	V_B = V_S = 600V	—	—	1.0	μA
I_{BS}	V_{BS} Standby Current	HIN = LIN = 0V	—	0.2	0.5	mA
I_{CC}	V_{CC} Standby Current	HIN = LIN = 0V	0.2	0.6	1.0	mA
V_{OH}	High Level Output Voltage	I_O = –20mA, LO, HO	13.6	14.2	—	V
V_{OL}	Low Level Output Voltage	I_O = 20mA, LO, HO	—	0.3	0.6	V
V_{IH}	High Level Input Threshold Voltage	HIN, LIN	2.7	—	—	V
V_{IL}	Low Level Input Threshold Voltage	HIN, LIN	—	—	0.8	V
I_{IH}	High Level Input Bias Current	V_{IN} = 5V	—	5	20	μA
I_{IL}	Low Level Input Bias Current	V_{IN} = 0V	—	—	2	μA
V_{BSuvr}	V_{BS} Supply UV Reset Voltage		8.0	8.9	9.8	V
V_{BSuvt}	V_{BS} Supply UV Trip Voltage		7.4	8.2	9.0	V
V_{BSuvh}	V_{BS} Supply UV Hysteresis Voltage		0.5	0.7	—	V
t_{VBSuv}	V_{BS} Supply UV Filter Time		—	7.5	—	μs
V_{CCuvr}	V_{CC} Supply UV Reset Voltage		8.0	8.9	9.8	V
V_{CCuvt}	V_{CC} Supply UV Trip Voltage		7.4	8.2	9.0	V
V_{CCuvh}	V_{CC} Supply UV Hysteresis Voltage		0.5	0.7	—	V
t_{VCCuv}	V_{CC} Supply UV Filter Time		—	7.5	—	μs
I_{OH}	Output High Level Short Circuit Pulsed Current	V_O = 0V, V_{IN} = 5V, PW < 10μs	120	200	—	mA
I_{OL}	Output Low Level Short Circuit Pulsed Current	V_O = 15V, V_{IN} = 0V, PW < 10μs	250	350	—	mA
R_{OH}	Output High Level On Resistance	I_O = –20mA, R_{OH} = (V_{CC}–V_O)/I_O	—	40	70	Ω
R_{OL}	Output Low Level On Resistance	I_O = 20mA, R_{OL} = V_O/I_O	—	15	30	Ω
$t_{dLH(HO)}$	High Side Turn-On Propagation Delay	CL = 1000pF between HO-V_S	—	450	650	ns
$t_{dHL(HO)}$	High Side Turn-Off Propagation Delay	CL = 1000pF between HO-V_S	—	450	650	ns
t_{rH}	High Side Turn-On Rise Time	CL = 1000pF between HO-V_S	—	130	220	ns
t_{fH}	High Side Turn-Off Fall Time	CL = 1000pF between HO-V_S	—	50	80	ns
$t_{dLH(LO)}$	Low Side Turn-On Propagation Delay	CL = 1000pF between LO-GND	—	450	650	ns
$t_{dHL(LO)}$	Low Side Turn-Off Propagation Delay	CL = 1000pF between LO-GND	—	450	650	ns
t_{rL}	Low Side Turn-On Rise Time	CL = 1000pF between LO-GND	—	130	220	ns
t_{fL}	Low Side Turn-Off Fall Time	CL = 1000pF between LO-GND	—	50	80	ns
Δt_{dLH}	Delay Matching, High Side and Low Side Turn-On	$\lvert t_{dLH(HO)} - t_{dLH(LO)} \rvert$	—	0	30	ns
Δt_{dHL}	Delay Matching, High Side and Low Side Turn-Off	$\lvert t_{dHL(HO)} - t_{dHL(LO)} \rvert$	—	0	30	ns
Tinon	Input Filter Time (ON)	CONVEX PULSE	150	250	350	ns
		CONCAVE PULSE	250	350	450	ns
Tinoff	Input Filter Time (OFF)	CONVEX PULSE	150	250	350	ns
		CONCAVE PULSE	250	350	450	ns
ΔPwIO	I/O Pulse Width Difference	$\lvert P_{W(IN)} - P_{W(OUT)} \rvert$	–40	0	100	ns

* Typ. is not specified.

TIMING REQUIREMENT (Input Signal Frequency : 200kHz Duty < 50%)



FUNCTION TABLE

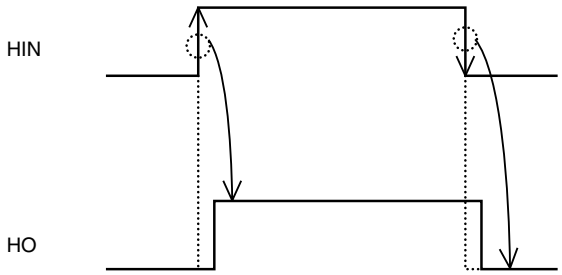
HIN	LIN	VBS UV	VCC UV	HO	LO	Behavioral state
H→L	L	H	H	L	L	LO = HO = Low
H→L	H	H	H	L	H	LO = High
L→H	L	H	H	H	L	HO = High
L→H	H	H	H	L	L	LO = HO = Low
X	L	L	H	L	L	HO = Low, VBS UV tripped
X	H	L	H	L	H	LO = High, VBS UV tripped
H→L	X	H	L	L	L	LO = Low, VCC UV tripped
L→H	X	H	L	L	L	HO = LO = Low, VCC UV tripped

Note1 : “L” state of VBS UV, VCC UV means that UV trip voltage.

2 : In the case of both input signals (HIN and LIN) are “H”, output signals (HO and LO) become “L”.

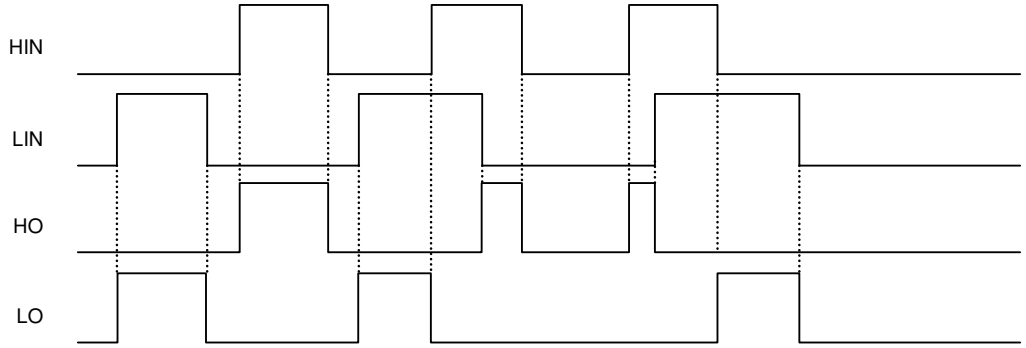
3 : X (HIN) : L→H or H→L.X(LIN) : H or L.

4 : Output signal (HO) is triggered by the edge of input signal.



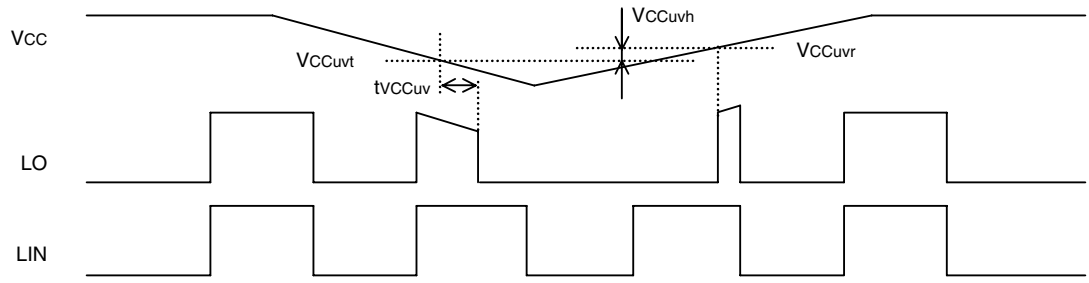
TIMING DIAGRAM

1. Input/Output Timing Diagram
   HIGH ACTIVE (When input signal (HIN or LIN) is “H”, then output signal (HO or LO) is “H”.)
   In the case of both input signals (HIN and LIN) are “H”, output signals (HO and LO) become “L”.

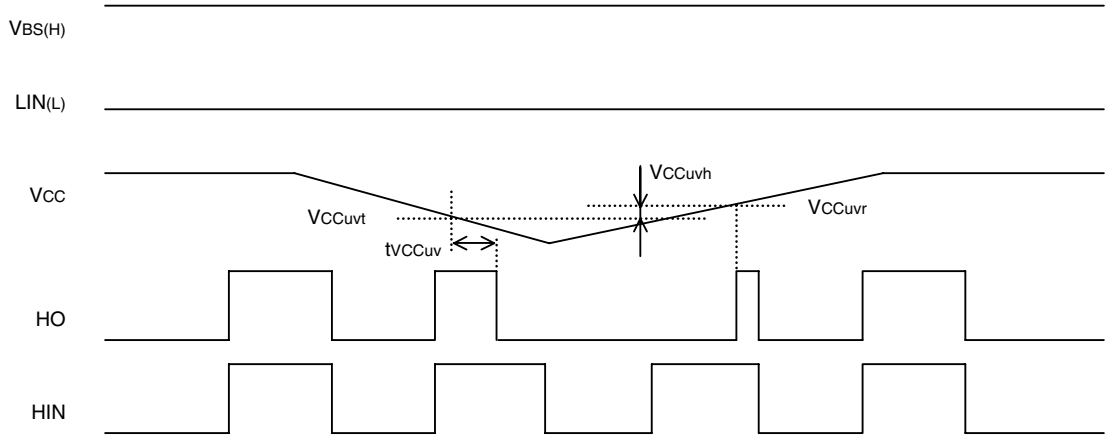


2. V_{CC} (V_{BS}) Supply Under Voltage Lockout Timing Diagram

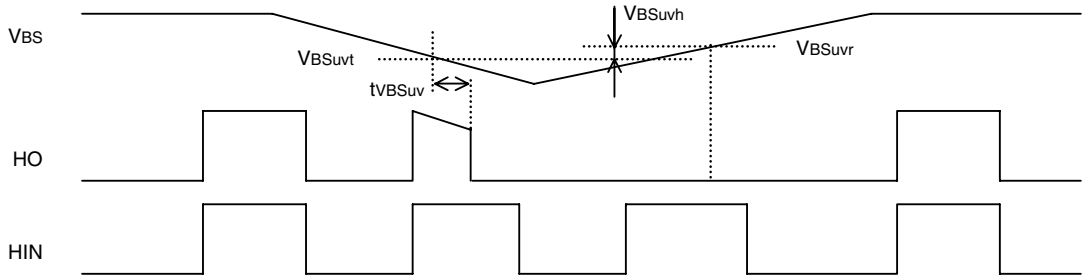
   If V_{CC} supply voltage drops below UV trip voltage (V_{CCuvt}) for V_{CC} Supply UV filter time, output signal is shut down. As soon as V_{CC} supply voltage rises over UV reset voltage, output signal LO becomes “H” it LIN is “H”.



   If V_{CC} supply voltage drops below UV trip voltage (V_{CCuvt}) for V_{CC} supply UV filter time, output signal is shut down. As soon as V_{CC} supply voltage rises over UV reset voltage, output signal HO becomes “H” it HIN is “H”.



If V_{BS} supply voltage drops below UV trip voltage (V_{BSuvt}) for V_{BS} supply UV filter time, output signal is shut down. As soon as V_{BS} supply voltage rises over UV reset voltage, output signal HO becomes “H” at following “H” HIN input signal.



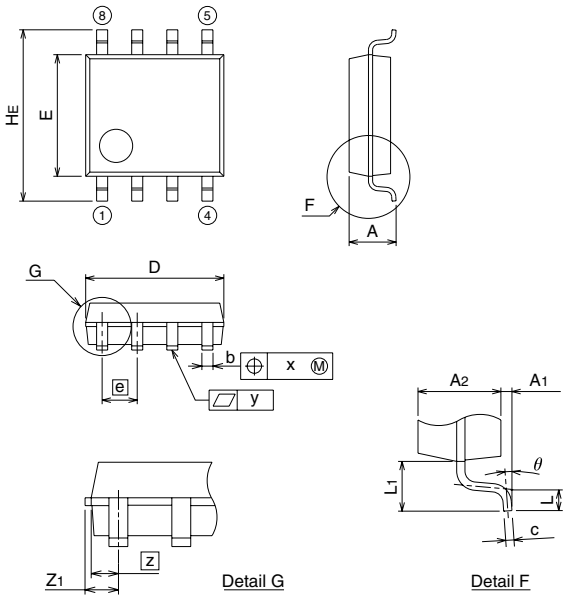
3. Allowable Supply Voltage Transient
It is recommended to supply V_{CC} firstly and supply V_{BS} secondly. In the case of shutting off supply voltage, please shut off V_{BS} firstly and shut off V_{CC} secondly. When applying V_{CC} and V_{BS}, power supply should be applied slowly. If it rises rapidly, output signal (HO or LO) may be malfunction.

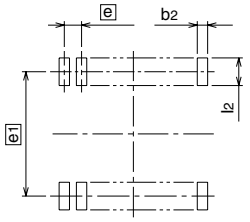
4. About the deadtime between HIN and LIN.
Due to input filter circuit, the pulse width of output maybe longer than the pulse width of input. In that case, please assure that the deadtime between HIN and LIN is over 300ns, or HO and LO maybe on at the same time (shut-through).

Consideration

As for this product, the terminal of low voltage part and high-voltage part is very clear (The Fifth: LO, The Sixth: VS). Therefore, pin insulation space distance should be taken enough.

PACKAGE OUTLINE





Recommended Mount Pad

Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	–	–	1.9
A_1	0.05	–	–
A_2	–	1.5	–
b	0.35	0.4	0.5
c	0.13	0.15	0.2
D	4.8	5.0	5.2
E	4.2	4.4	4.6
e	–	1.27	–
H_E	5.9	6.2	6.5
L	0.2	0.4	0.6
L_1	–	0.9	–
Z	–	0.595	–
Z_1	–	–	0.745
x	–	–	0.25
y	–	–	0.1
θ	0°	–	10°
b_2	–	0.76	–
e1	–	5.72	–
l_2	1.27	–	–