ZENER VOLTAGE REGULATOR DIODES

MMSZ3V3 - 4V3

SOD-123
PLASTIC PACKAGE



For High Density Applications

Polarity: - Cathode indicated by polarity band

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	VALUE	UNIT
Power Dissipation on FR-5 Board at T_L=75ºC (Note 1)	P_D	500	mW
Derated Above 75ºC		6.7	mW/ºC
Thermal Resistance, Junction to Ambient (Note 2)	$R_{th\ (j\text{-}a)}$	340	ºC/W
Thermal Resistance, Junction to Lead (Note 2)	$R_{th\ (j\text{-}L)}$	150	ºC/W
Operating and Storage Junction Temperature Range	T_j, T_{stg}	- 55 to +150	°C

Note1. FR-5=3.5 x 1.5 inches
Note2. Thermal Resistance measured obtained via infrared Scan Method

Forward Voltage at I_F=10mA <0.9V and <1.5V at 200mA

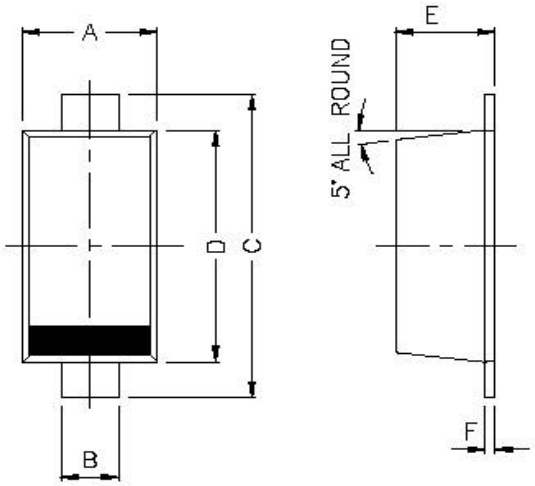
ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

Device	V_{Z1} (V) Notes 3 and 4 at I_{ZT1}=5mA			Z_{ZT1} (Note5)	V_{Z2} (V) Notes 3 and 4 at I_{ZT2}=1mA		Z_{ZT2} (Note 5)	Max Reverse Current		Marking
	min	nom	max	(W) at I_{ZT1}= 5mA max	min	max	(W) at I_{ZT2}= 1mA max	I_R at mA Max	V_R (V)	
MMSZ3V3	3.14	3.3	3.47	95	2.3	2.9	600	5.0	1.0	**T4**
MMSZ3V6	3.42	3.6	3.78	90	2.7	3.3	600	5.0	1.0	**T5**
MMSZ3V9	3.71	3.9	4.10	90	2.9	3.5	600	3.0	1.0	**U1**
MMSZ4V3	4.09	4.3	4.52	90	3.3	4.0	600	3.0	1.0	**U2**

Note3. Tolerance of +/- 5% on the nominal Zener Voltage
Note4. Tolerance and Voltage Designation: Zener Voltage (V_Z) is measured with the Zener Current App;ied for PW=1ms
Note5. Z_{ZT} and Z_{ZK} are measured by dividing the AC Voltage drop across the device by the AC Current Applied
The specified limits are for $I_{Z\ (AC)}$ =0.1 $I_{Z\ (DC)}$ with the AC frequency =1KHz

MMSZ3V3_4V3Rev_1 050506E

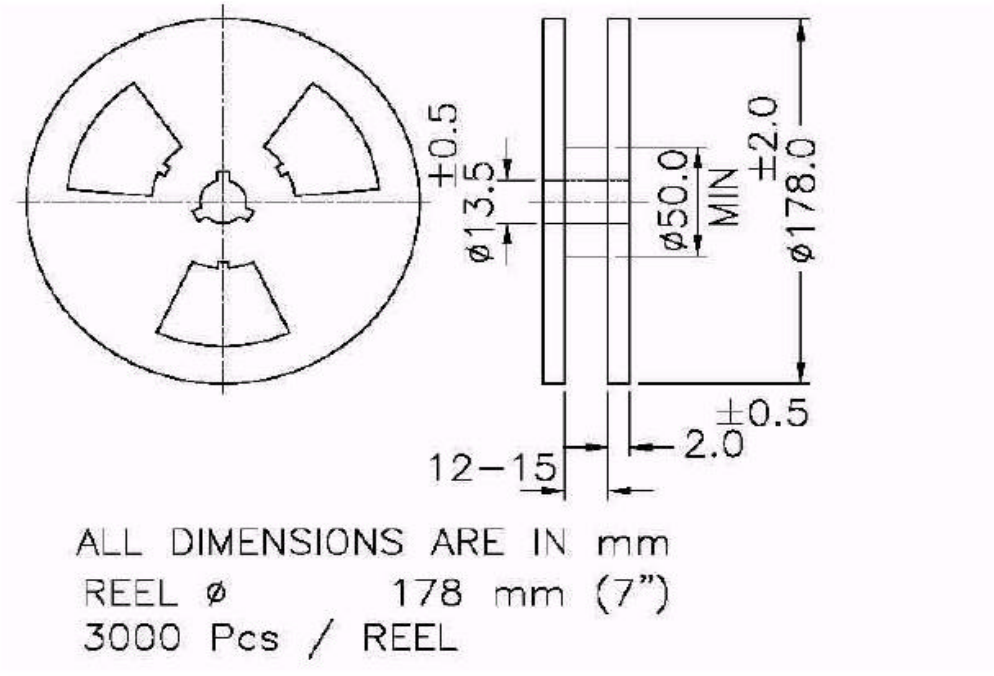
PACKAGE SOD–123 FL



DIM	MIN.	MAX.
A	1.55	1.65
B	0.5	0.6
C	3.7	3.9
D	2.6	2.7
E	1.05	1.15
F	0.127	0.135

All dimensions are in mm

CATHODE IS MARKED BY BAND



Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.

2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

MMSZ3V3_4V3Rev_1 050506E

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.

CDIL is a registered Trademark of
Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150,4141 1112 Fax + 91-11-2579 5290, 4141 1119
email@cdil.com www.cdilsemi.com