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ACT OCXO14

Compatible with Eu Directive 2002/EC - RoHS

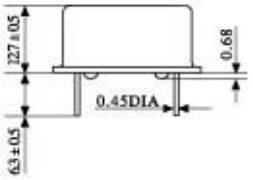


FEATURES

- Stabilities to ±0.05PPM (AT CUT)
- Low Aging
- Voltage Controlled Frequency Adjustment
- Measurement Equipment

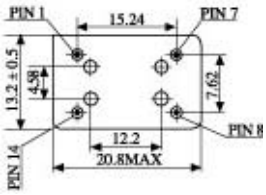
APPLICATIONS

- SDH/SONET
- ATM
- WLL
- **SC Cut OXCO** Please Enquire



SPECIFICATIONS Frequency Range 1.0 ~ 100MHz

Frequency Accuracy (Adjustment 25°C)	±1PPM (Centre control voltage)
Frequency Stability vs Temperature	(See Table 1)
Aging	First year ±0.5PPM, 10 years ±3PPM
Output Type and Load Characteristics	(See Table 2)
Frequency Stability vs Load	±0.05PPM vs ±10% load change
Supply Voltage	+3.3VDC, +5.0VDC
Frequency Stability vs Voltage	±0.05PPM vs ±5% voltage change
Supply Consumption	1.5W (max.)warm-up; 0.5W (max.) static
Warm-up Time	±0.1PPM, <1 min.
Adjustable Frequency Range	±7.0PPM
Control Voltage Range	0~5V
Slope	Positive
Linearity	±10%
Phase Noise(10MHz)	10Hz, -100dBc/Hz 100Hz, -120dBc/Hz 1kHz, -135dBc/Hz 10kHz, -145dBc/Hz



(Unit: mm)

PIN FUNCTION:

PIN1 – Control Voltage
PIN7 – GND/Case
PIN8 – Output
PIN14 – Power Supply

FREQUENCY STABILITY vs TEMPERATURE – TABLE 1

(Applies to frequencies < 20MHz & to 5v0 supply without voltage control. For frequencies > 20MHZ & 3v3 stabilities will be lower –Please enquire)

Frequency Stability vs Temperature	Temperature Range
±0.05PPM	0 - +50°C
±0.30PPM	-20 - +70°C
±0.50PPM	-40 - +75°C

OUTPUT TYPE AND LOAD CHARACTERISTICS – TABLE 2

Output Waveform	Frequency Range	Oscillation State	Output Characteristics
Clipped Sine Wave	8MHz - 30MHz 20MHz - 100MHz	F: Fundamental O: Overtone	Load: 10kΩ/10pF Output level: >1Vp-p
TTL	1MHz - 30MHz 20MHz - 100MHz	F: Fundamental O: Overtone	Load: Max. 10 low power consumption TTL gates “1” level: >+2.4VDC; “0” level: <+0.2VDC Duty cycle: 45/55 Rise/fall time: <6ns
HCMOS	1MHz - 30MHz 20MHz - 100MHz	F: Fundamental O: Overtone	Load: Max. 10 low power consumption TTL/HCMOS “1” level: >+4.3VDC; “0” level: <+0.5VDC Duty cycle: 45/55 Rise/fall time: <6ns
ACMOS	10MHz - 30MHz 20MHz - 100MHz	F: Fundamental O: Overtone	Load: Max. 10 low power consumption TTL/ACMOS “1” level: >+4.3VDC; “0” level: <+0.5VDC Duty cycle: 45/55 Rise/fall time: <6ns

Please note that all parameters can not necessarily be specified in one device.
Customer to specify : Frequency, Output, Voltage, Stability, and Operating Temperature
In line with our ongoing policy of product improvement and evolvement the above specification may be subject to change without notice

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For quotations or further information, please contact us at :
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