

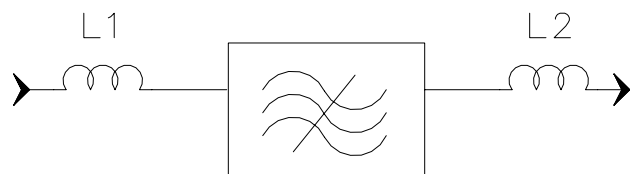
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	74.85	75	75.15
Insertion Loss	dB		24.7	26
1dB Bandwidth	MHz		29.92	
3dB Bandwidth	MHz	30	30.93	
40 dB Bandwidth	MHz		33.95	38
Passband Variation	dB		0.9	1.2
Absolute Delay	usec		1.4	
Ultimate Rejection	dB	48	50	
Material Temperature coefficient	KHz/°C	-6.15		
Ambient Temperature	°C	25		
Package Size	DIP2012 (20x12.5x4mm3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



$$L1 = 180 + 82nH \quad L2 = 15 + 120nH$$

Source/Load Impedance=50 ohm

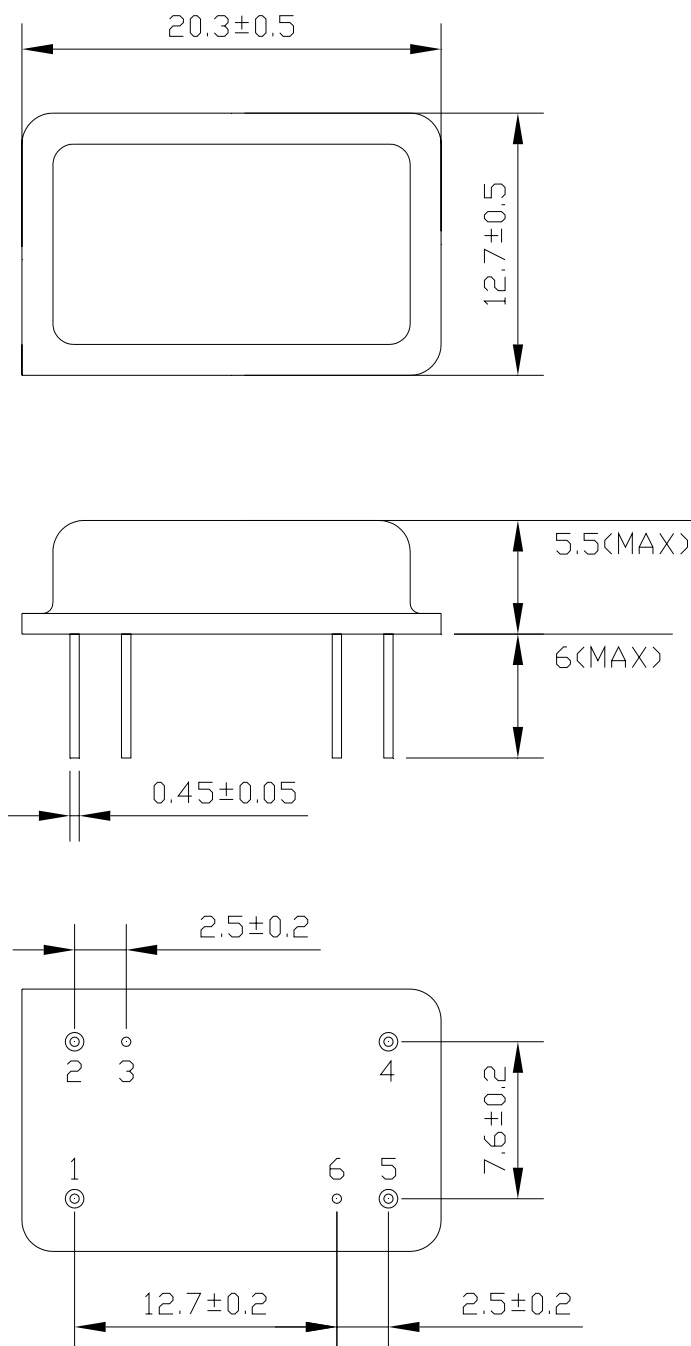
Notes - Component values may change depending
on board layout.



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Package Dimension

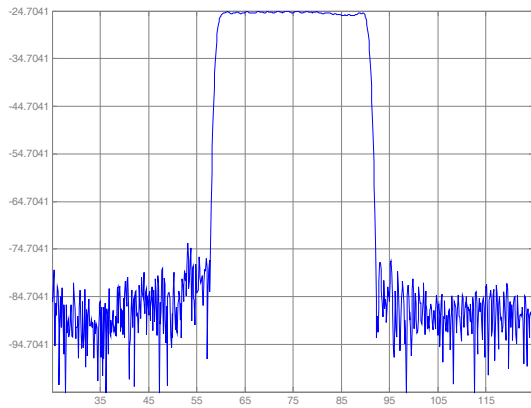


Input:1
Output:5

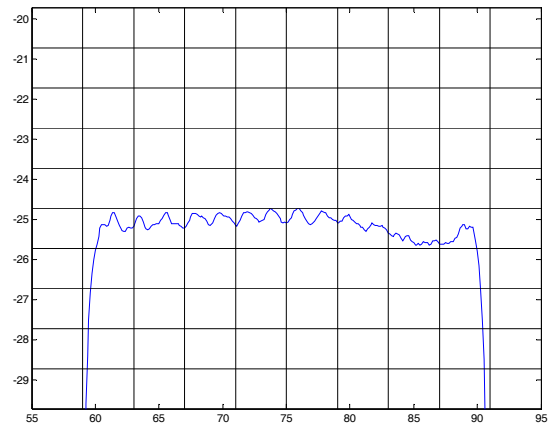
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Typical Performance

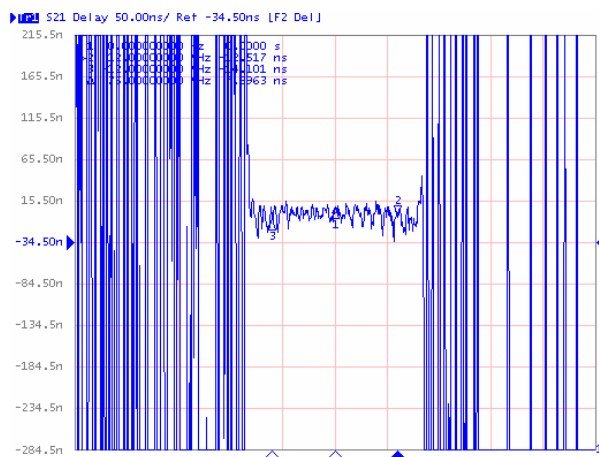
Frequency Respond



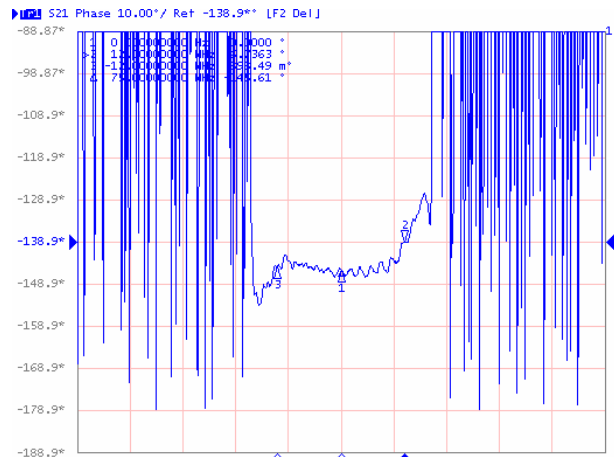
Passband Respond



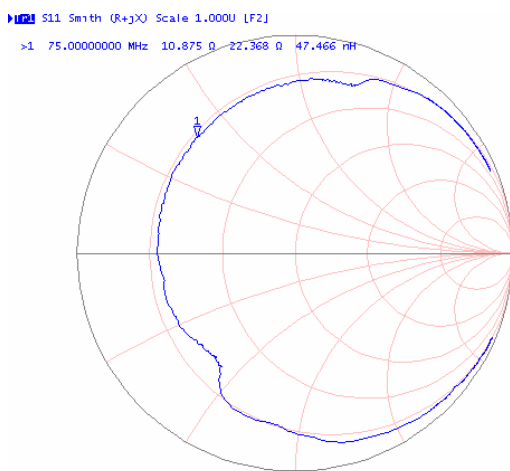
Group Delay Variation($f_0 \pm 12\text{MHz}$)



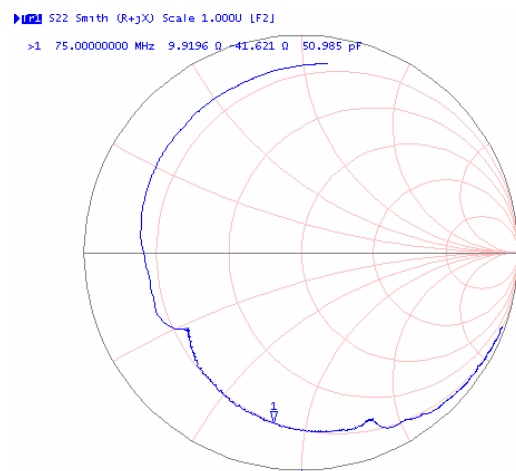
Phase Linearity($f_0 \pm 12\text{MHz}$)



Simth Chart S11



Simth Chart S22



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