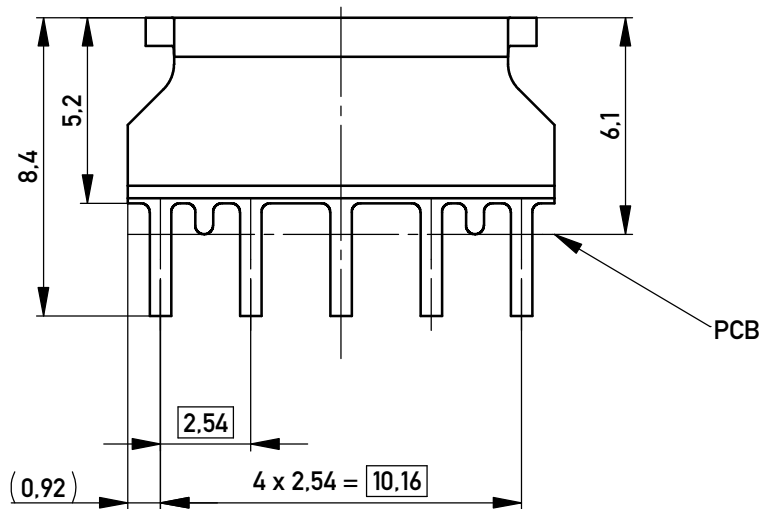
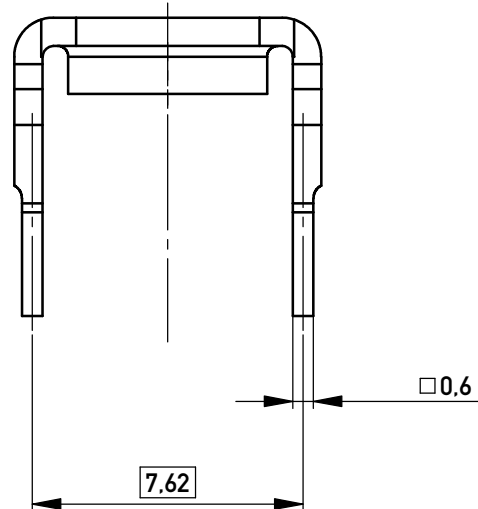
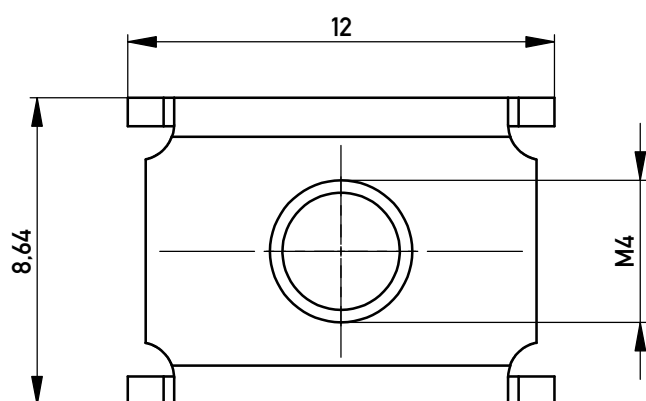
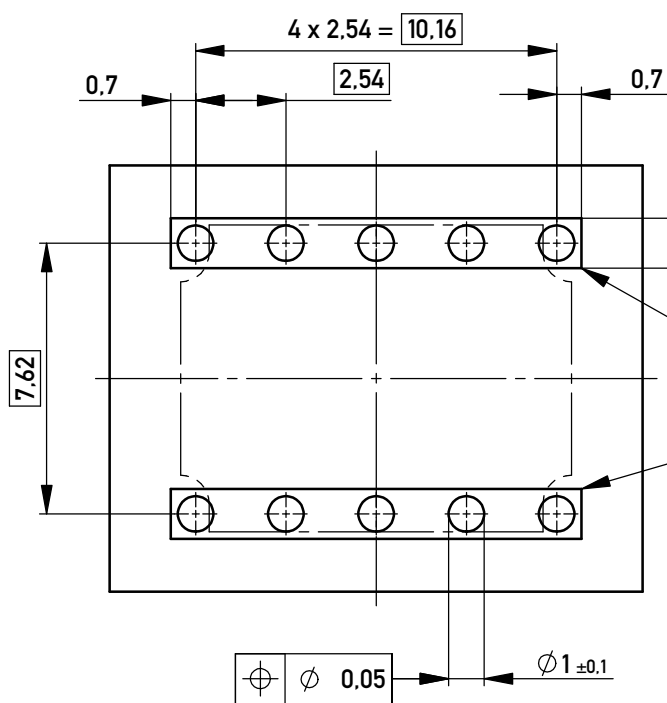


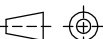
Leiterplattenbohrbild
PCB drillhole pattern



Dieser Bereich muß gleiches Potential
auf der LP-Oberfläche haben
*This area must have same electrical
potential on surface of PCB*

alle Löcher/
all holes

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Information:				Tolerances		 All Dimensions in mm		Scale		5:1	
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						SVA 10-polig TL					
						Power Bug 10 pin TL 2,3					
						100100220002					
a		07.07.2011				www.ERNI.com				K	
Index		Date						Class		EPSVA	
										A3	