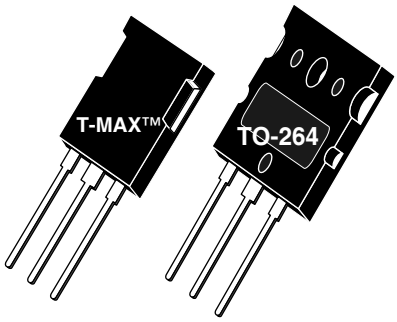
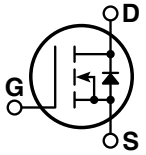
APT20M20B2FLL
APT20M20LFLL
200V 100A 0.020Ω

POWER MOS 7® FREDFET



Power MOS 7® is a new generation of low loss, high voltage, N-Channel enhancement mode power MOSFETS. Both conduction and switching losses are addressed with Power MOS 7® by significantly lowering $R_{DS(ON)}$ and Q_g. Power MOS 7® combines lower conduction and switching losses along with exceptionally fast switching speeds inherent with APT's patented metal gate structure.

- **Lower Input Capacitance**
- **Lower Miller Capacitance**
- **Lower Gate Charge, Qg**
- **Increased Power Dissipation**
- **Easier To Drive**
- **Popular T-MAX™ or TO-264 Package**
- **FAST RECOVERY BODY DIODE**



MAXIMUM RATINGS All Ratings: T_C = 25°C unless otherwise specified.

Symbol	Parameter	APT20M20B2FLL_LFLL	UNIT
V_{DSS}	Drain-Source Voltage	200	Volts
I_D	Continuous Drain Current @ T_C = 25°C ⑤	100	Amps
I_{DM}	Pulsed Drain Current ①	400	
V_{GS}	Gate-Source Voltage Continuous	±30	Volts
V_{GSM}	Gate-Source Voltage Transient	±40	
P_D	Total Power Dissipation @ T_C = 25°C	568	Watts
	Linear Derating Factor	4.55	W/°C
T_J,T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C
T_L	Lead Temperature: 0.063" from Case for 10 Sec.	300	
I_{AR}	Avalanche Current ① (Repetitive and Non-Repetitive)	100	Amps
E_{AR}	Repetitive Avalanche Energy ①	50	mJ
E_{AS}	Single Pulse Avalanche Energy ④	2500	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage (V_{GS} = 0V, I_D = 250μA)	200			Volts
$R_{DS(on)}$	Drain-Source On-State Resistance ② (V_{GS} = 10V, I_D = 50A)			0.020	Ohms
I_{DSS}	Zero Gate Voltage Drain Current (V_{DS} = 200V, V_{GS} = 0V)			250	μA
	Zero Gate Voltage Drain Current (V_{DS} = 160V, V_{GS} = 0V, T_C = 125°C)			1000	
I_{GSS}	Gate-Source Leakage Current (V_{GS} = ±30V, V_{DS} = 0V)			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage (V_{DS} = V_{GS}, I_D = 2.5mA)	3		5	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

APT Website - http://www.advancedpower.com

050-7046 Rev D 4-2004

DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{iss}	Input Capacitance	V_{GS} = 0V V_{DS} = 25V f = 1 MHz		6850		pF
C_{oss}	Output Capacitance			2180		
C_{rss}	Reverse Transfer Capacitance			95		
Q_g	Total Gate Charge ③	V_{GS} = 10V V_{DD} = 100V I_D = 100A @ 25°C		110		nC
Q_{gs}	Gate-Source Charge			43		
Q_{gd}	Gate-Drain ("Miller") Charge			47		
$t_{d(on)}$	Turn-on Delay Time	**RESISTIVE SWITCHING** V_{GS} = 15V V_{DD} = 100V I_D = 100A @ 25°C R_G = 0.6Ω		13		ns
t_r	Rise Time			40		
$t_{d(off)}$	Turn-off Delay Time			26		
t_f	Fall Time			2		
E_{on}	Turn-on Switching Energy ⑦	**INDUCTIVE SWITCHING @ 25°C** V_{DD} = 130V, V_{GS} = 15V I_D = 100A, R_G = 5Ω		465		µJ
E_{off}	Turn-off Switching Energy			455		
E_{on}	Turn-on Switching Energy ⑦	**INDUCTIVE SWITCHING @ 125°C** V_{DD} = 130V V_{GS} = 15V I_D = 100A, R_G = 5Ω		920		
E_{off}	Turn-off Switching Energy			915		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions		MIN	TYP	MAX	UNIT
I_S	Continuous Source Current (Body Diode)				100	Amps
I_{SM}	Pulsed Source Current ① (Body Diode)				400	
V_{SD}	Diode Forward Voltage ② (V_{GS} = 0V, I_S = -100A)				1.3	Volts
$^{dv}/_{dt}$	Peak Diode Recovery $^{dv}/_{dt}$ ⑥				8	V/ns
t_{rr}	Reverse Recovery Time (I_S = -100A, $^{di}/_{dt}$ = 100A/µs)	T_j = 25°C			220	ns
		T_j = 125°C			420	
Q_{rr}	Reverse Recovery Charge (I_S = -100A, $^{di}/_{dt}$ = 100A/µs)	T_j = 25°C		1.07		µC
		T_j = 125°C		2.9		
I_{RRM}	Peak Recovery Current (I_S = -100A, $^{di}/_{dt}$ = 100A/µs)	T_j = 25°C		12.1		Amps
		T_j = 125°C		20.6		

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.22	°C/W
$R_{\theta JA}$	Junction to Ambient			40	

① Repetitive Rating: Pulse width limited by maximum junction temperature

② Pulse Test: Pulse width < 380 µs, Duty Cycle < 2%

③ See MIL-STD-750 Method 3471

④ Starting T_j = +25°C, L = 0.50mH, R_G = 25Ω, Peak I_L = 100A

⑤ The maximum current is limited by lead temperature

⑥ $^{dv}/_{dt}$ numbers reflect the limitations of the test circuit rather than the device itself. $I_S \le -I_D 75A$ $^{di}/_{dt} \le 700A/\mu s$ $V_R \le V_{DSS}$ $T_J \le 150°C$

⑦ Eon includes diode reverse recovery. See figures 18, 20.

APT Reserves the right to change, without notice, the specifications and inforation contained herein.

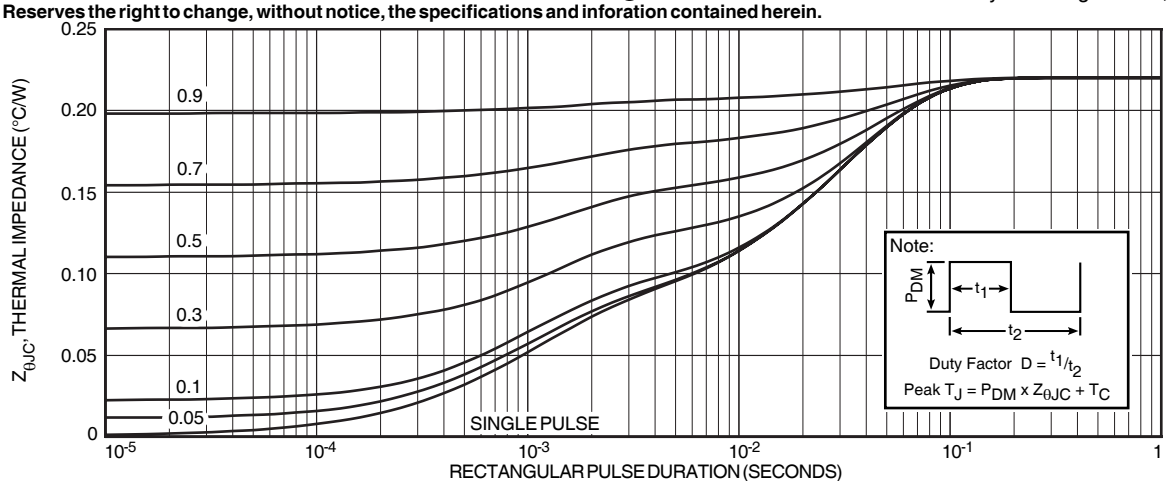


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION

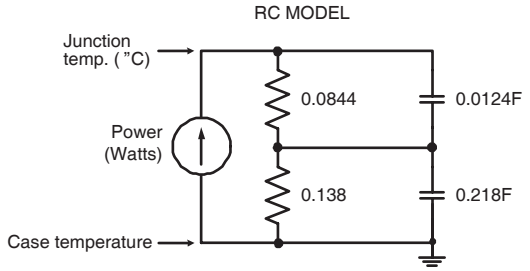


FIGURE 2, TRANSIENT THERMAL IMPEDANCE MODEL

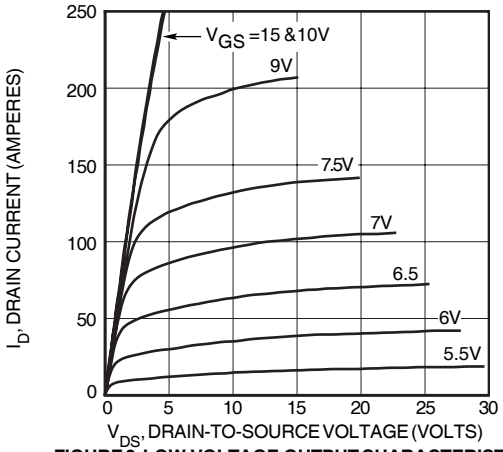


FIGURE 3, LOW VOLTAGE OUTPUT CHARACTERISTICS

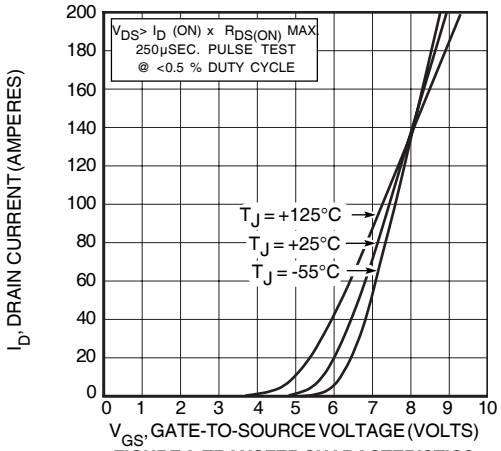


FIGURE 4, TRANSFER CHARACTERISTICS

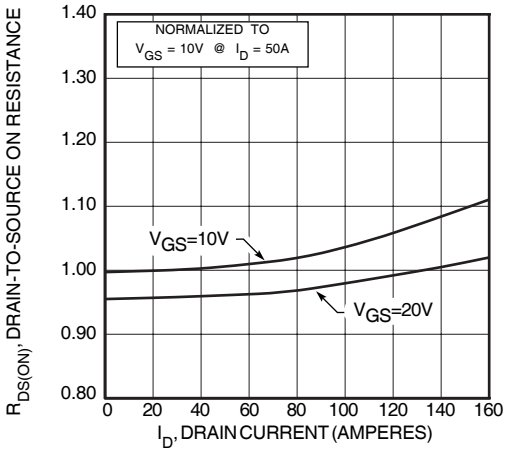


FIGURE 5, $R_{DS(ON)}$ vs DRAIN CURRENT

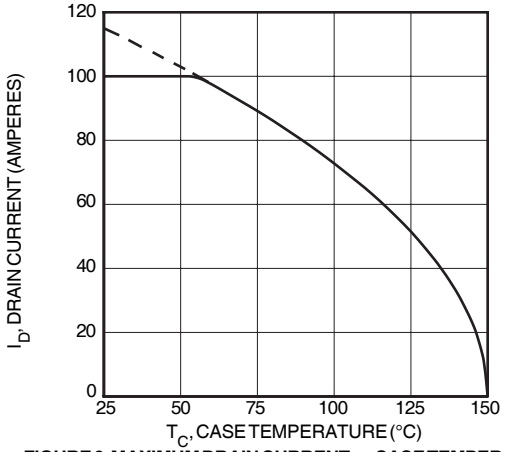


FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE

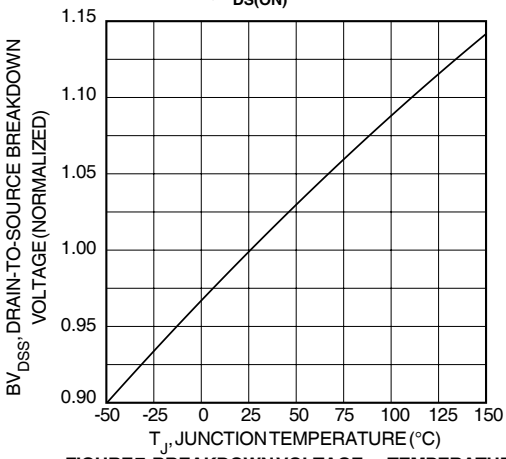


FIGURE 7, BREAKDOWN VOLTAGE vs TEMPERATURE

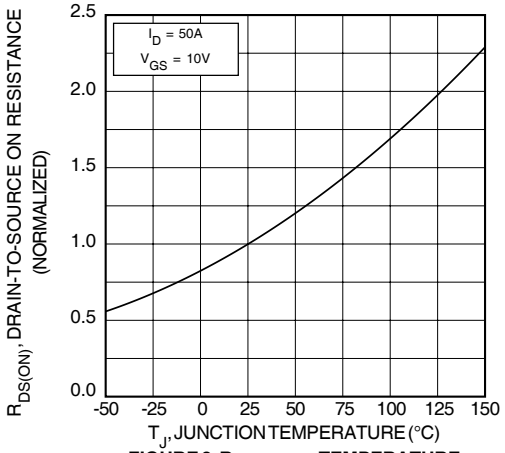


FIGURE 8, $R_{DS(ON)}$ vs. TEMPERATURE

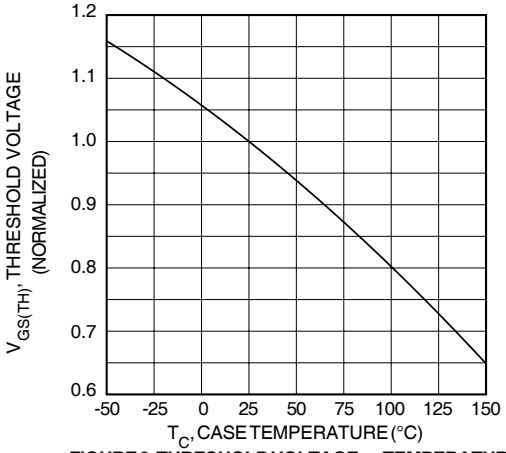


FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE

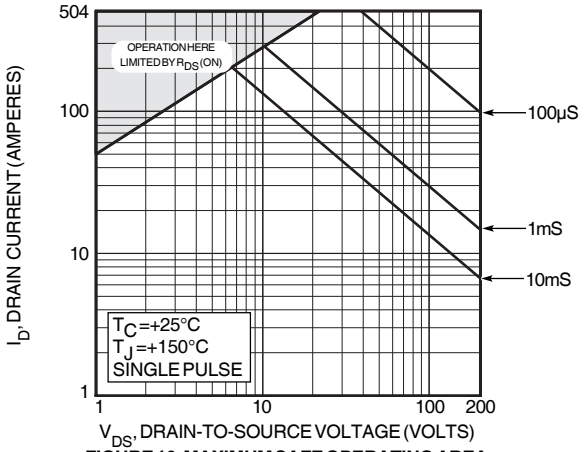


FIGURE 10, MAXIMUM SAFE OPERATING AREA

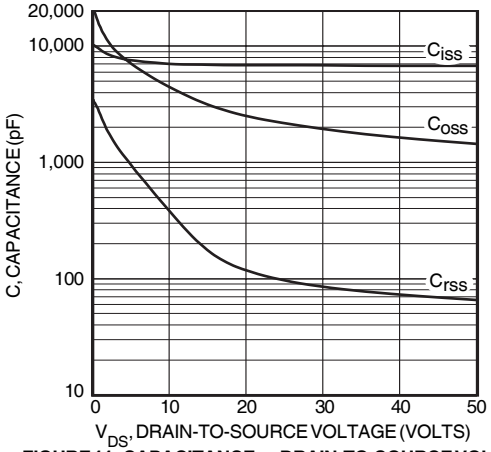


FIGURE 11, CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

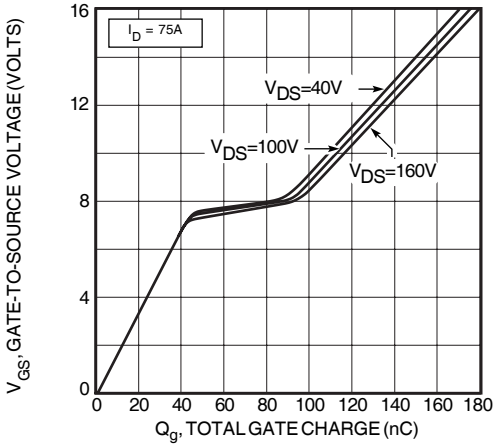


FIGURE 12, GATE CHARGE vs GATE-TO-SOURCE VOLTAGE

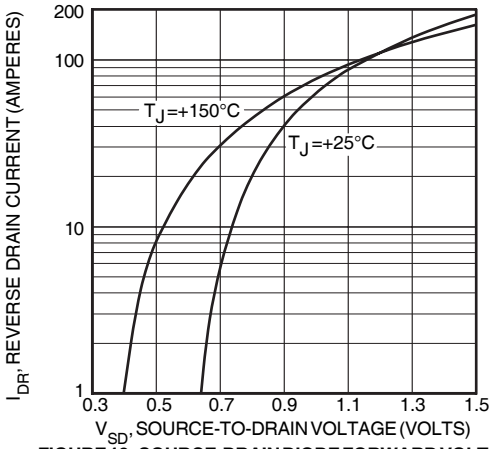


FIGURE 13, SOURCE-DRAIN DIODE FORWARD VOLTAGE

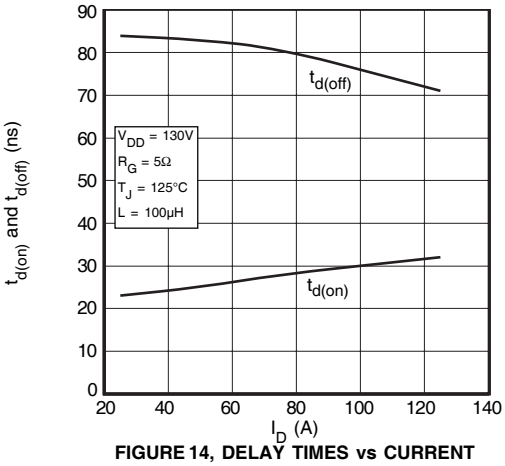


FIGURE 14, DELAY TIMES vs CURRENT

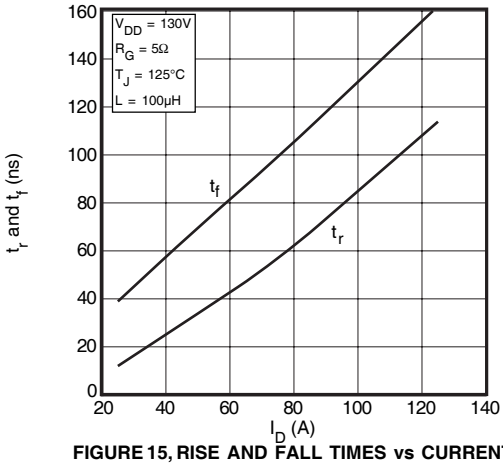


FIGURE 15, RISE AND FALL TIMES vs CURRENT

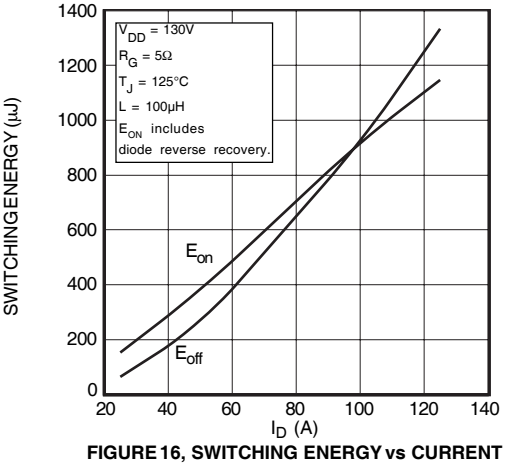


FIGURE 16, SWITCHING ENERGY vs CURRENT

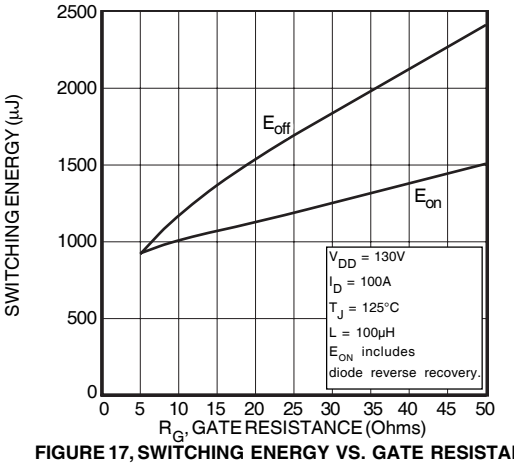


FIGURE 17, SWITCHING ENERGY VS. GATE RESISTANCE

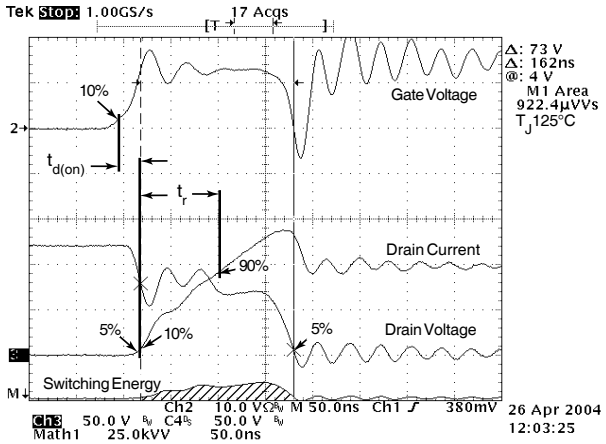


Figure 18, Turn-on Switching Waveforms and Definitions

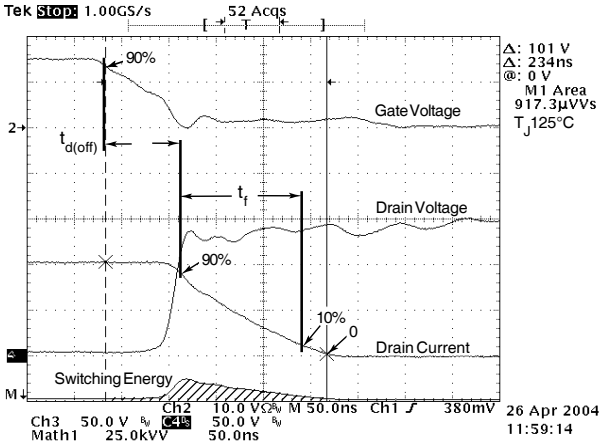


Figure 19, Turn-off Switching Waveforms and Definitions

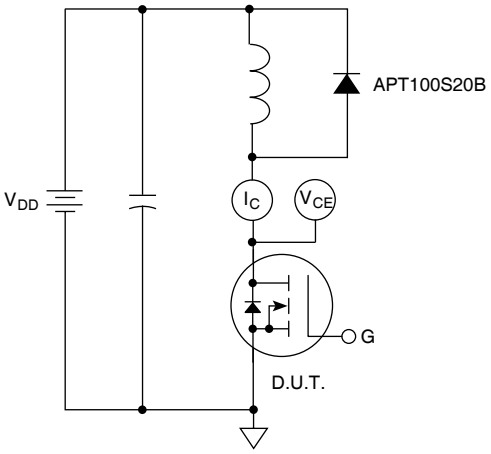
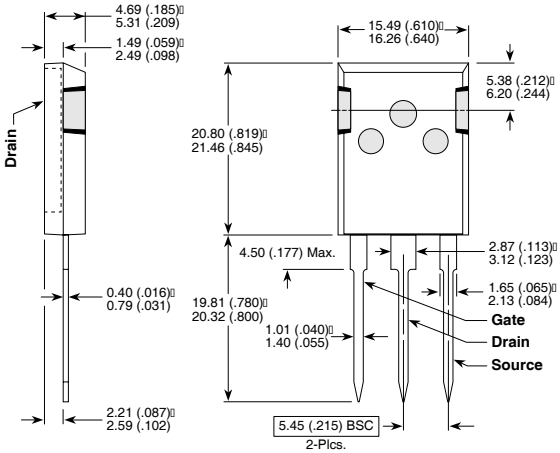


Figure 20, Inductive Switching Test Circuit

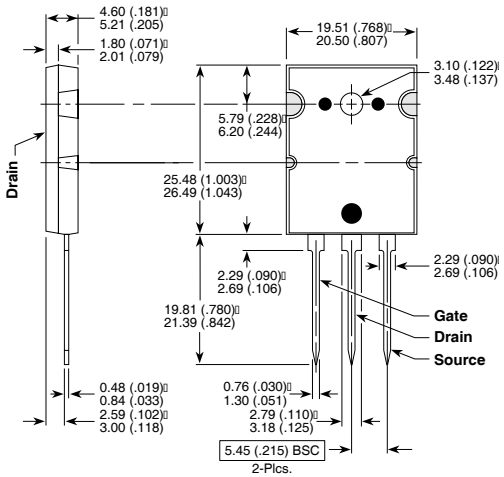
T-MAX™ (B2) Package Outline



These dimensions are equal to the TO-247 without the mounting hole.

Dimensions in Millimeters and (Inches)

TO-264 (L) Package Outline



Dimensions in Millimeters and (Inches)

APT's products are covered by one or more of U.S.patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522
5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. US and Foreign patents pending. All Rights Reserved.