

VOLTAGE RANGE: 200 - 1000V

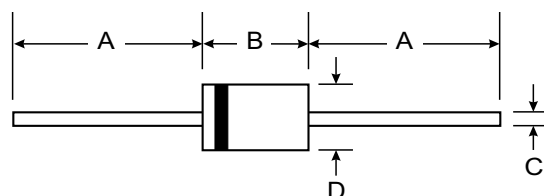
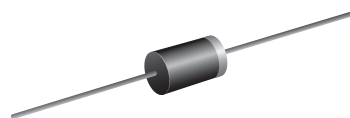
CURRENT: 2.3 A

Features

- Low forward voltage drop
- High current capability
- Easily cleaned with alcohol, Isopropanol and similar solvents
- The plastic material carries U/L recognition 94V-0

Mechanical Data

- Case: DO-201AD, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.2 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



DO-201AD		
Dim	Min	Max
A	25.40	—
B	7.20	9.50
C	1.20	1.30
D	4.80	5.30
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	BYM26A	BYM26B	BYM26C	BYM26D	BYM26E	Unit
Maximum recurrent peak reverse voltage	V _{RRM}	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	200	400	600	800	1000	V
Maximum average forward rectified current 9.5mm lead length, @T _A =75°C	I _{F(AV)}	2.3					A
Peak forward surge current 10 ms single half-sine-wave superimposed on rated load @T _J =125°C	I _{FSM}	45.0					A
Maximum instantaneous forward voltage @ 2.0A	V _F	2.65					V
Maximum reverse current @T _A =25°C at rated DC blocking voltage @T _A =100°C	I _R	10.0 150.0					μA
Maximum reverse recovery time (Note1)	t _{rr}	30			75		ns
Typical junction capacitance (Note2)	C _J	85			75		pF
Typical thermal resistance (Note3)	R _{θJA}	75					°C/W
Operating junction temperature range	T _J	- 55 ----- + 150					°C
Storage temperature range	T _{STG}	- 55 ----- + 150					°C

NOTE: 1. Measured with $I_F = 0.5\text{A}$, $I_R = 1\text{A}$, $t_{rr} = 0.25\text{A}$.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal resistance from junction to ambient.



FIG.1 -- FORWARD DERATING CURVE

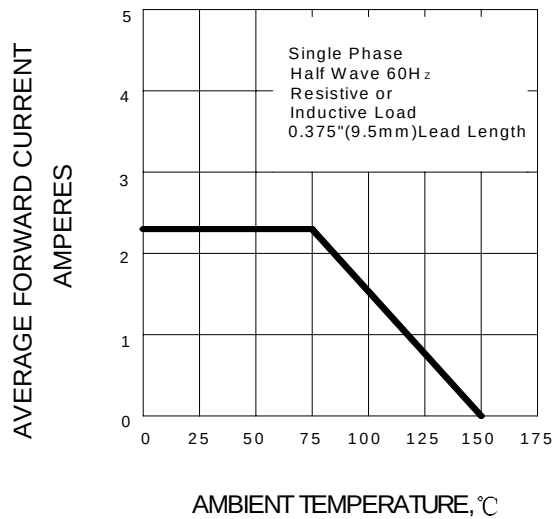


FIG.2 -- TYPICAL FORWARD CHARACTERISTIC

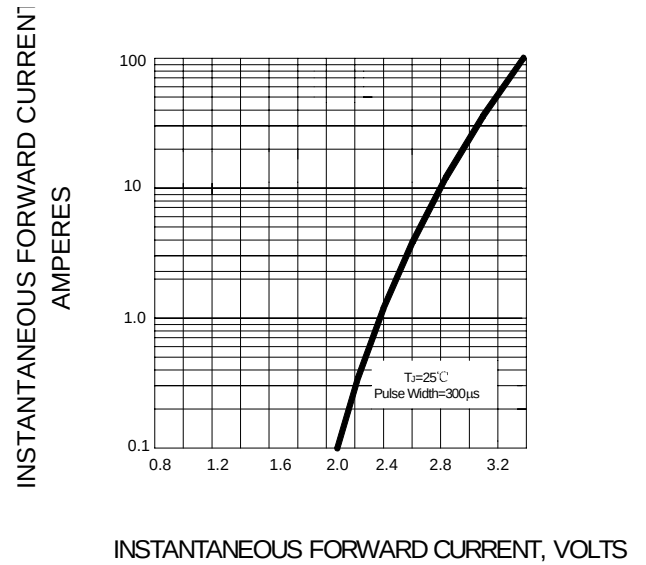


FIG.3 -- PEAK FORWARD SURGE CURRENT

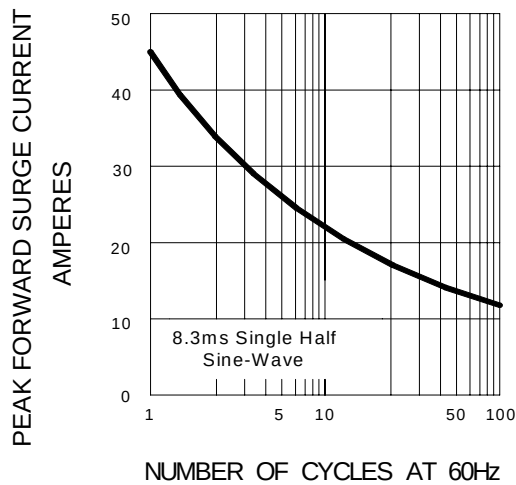


FIG.4 -- TYPICAL JUNCTION CAPACITANCE

