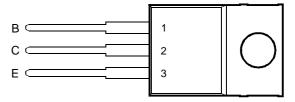
- Designed for Complementary Use with the BD243 Series
- 65 W at 25°C Case Temperature
- 6 A Continuous Collector Current
- 10 A Peak Collector Current
- Customer-Specified Selections Available

TO-220 PACKAGE (TOP VIEW)



Pin 2 is in electrical contact with the mounting base.

MDTRACA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT		
	BD244		-55		
Collector-emitter voltage ($R_{RF} = 100 \Omega$)	BD244A	\/	-70	V	
Collector-entitler voltage (R _{BE} = 100 sz)	BD244B	V _{CER}	-90		
	BD244C		-115		
	BD244		-45		
Collector emitter voltage (I = 20 mA)	BD244A	\/	-60	V	
Collector-emitter voltage (I _C = -30 mA)	BD244B	V _{CEO}	-80		
	BD244C		-100		
Emitter-base voltage	V _{EBO}	-5	V		
Continuous collector current			-6	Α	
Peak collector current (see Note 1)			-10	Α	
Continuous base current			-3	Α	
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			65	W	
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			2	W	
Unclamped inductive load energy (see Note 4)			62.5	mJ	
Operating junction temperature range			-65 to +150	°C	
Storage temperature range			-65 to +150	°C	
Lead temperature 3.2 mm from case for 10 seconds	TL	250	°C		

NOTES: 1. This value applies for $t_p \le 0.3$ ms, duty cycle $\le 10\%$.

- 2. Derate linearly to 150°C case temperature at the rate of 0.52 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = -0.4 A, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_S = 0.1 Ω , V_{CC} = -20 V.



BD244, BD244A, BD244B, BD244C PNP SILICON POWER TRANSISTORS

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electrical characteristics at 25°C case temperature

PARAMETER			TEST CONDITION	ONS	MIN			UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage			BD244	-45			
		$I_{\rm C} = -30 \text{mA}$	I _B = 0	BD244A	-60			V
		(see Note 5)		BD244B	-80			
				BD244C	-100			
		V _{CE} = -55 V	$V_{BE} = 0$	BD244			-0.4	
1	Collector-emitter	$V_{CE} = -70 \text{ V}$	$V_{BE} = 0$	BD244A			-0.4	mA
ICES	cut-off current	V _{CE} = -90 V	$V_{BE} = 0$	BD244B			-0.4	ША
		V _{CE} = -115 V	$V_{BE} = 0$	BD244C			-0.4	
1	Collector cut-off	V _{CE} = -30 V	I _B = 0	BD244/244A			-0.7	mA
I _{CEO}	current	V _{CE} = -60 V	$I_B = 0$	BD244B/244C			-0.7	ША
I _{EBO}	Emitter cut-off	V _{EB} = -5 V	I _C = 0				-1	mA
iEBO	current	AEB - 2 A	10 = 0					ША
h _{FE}	Forward current	V _{CE} = -4 V	$I_C = -0.3 \text{ A}$	(see Notes 5 and 6)				
"FE	transfer ratio	V _{CE} = -4 V	$I_C = -3 A$	(See Notes o and o)	15			
V _{CE(sat)}	Collector-emitter	I _B = -1 A	I _C = -6 A	(see Notes 5 and 6)			-1.5	V
VCE(sat)	saturation voltage	IB - IV					1.0	•
V_{BE}	Base-emitter	V _{CE} = -4 V	-4 V I _C = -6 A	(see Notes 5 and 6)			-2	V
▼ BE	voltage	VCE - TV	10 - 071	(See Notes o and o)			_	•
h _{fe}	Small signal forward	Vo= = -10 V	$I_{C} = -10 \text{ V}$ $I_{C} = -0.5 \text{ A}$	f = 1 kHz	20			
· 'te	current transfer ratio	VCE = 10 V						
h _{fe}	Small signal forward	V _{CF} = -10 V	$I_{\rm C} = -0.5 \; {\rm A}$	f = 1 MHz	3			
l' fel	current transfer ratio	*CE = 10 V			J			

NOTES: 5. These parameters must be measured using pulse techniques, t_p = 300 μ s, duty cycle \leq 2%.

thermal characteristics

PARAMETER			TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			1.92	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			62.5	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = -1 A	$I_{B(on)} = -0.1 \text{ A}$	$I_{B(off)} = 0.1 A$		0.3		μs
t _{off}	Turn-off time	$V_{BE(off)} = 3.7 \text{ V}$	$R_L = 20 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		1		μs

 $^{^{\}dagger} \ \ \mbox{Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.}$

PRODUCT INFORMATION

^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN VS COLLECTOR CURRENT $V_{CE} = -4 V$ $V_{CE} = -300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2% $V_{DE} = 300 \mu s$, duty cycle < 2%

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE

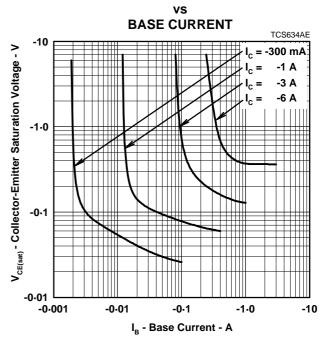


Figure 2.

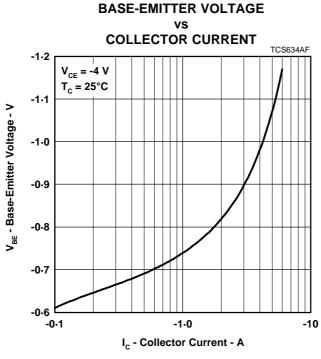
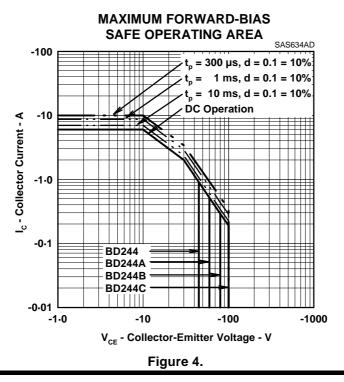


Figure 3.



MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION

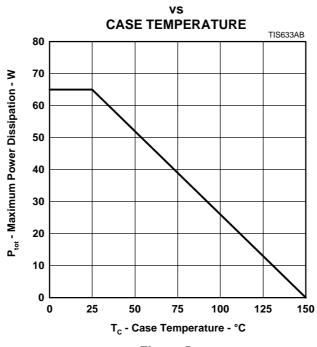


Figure 5.

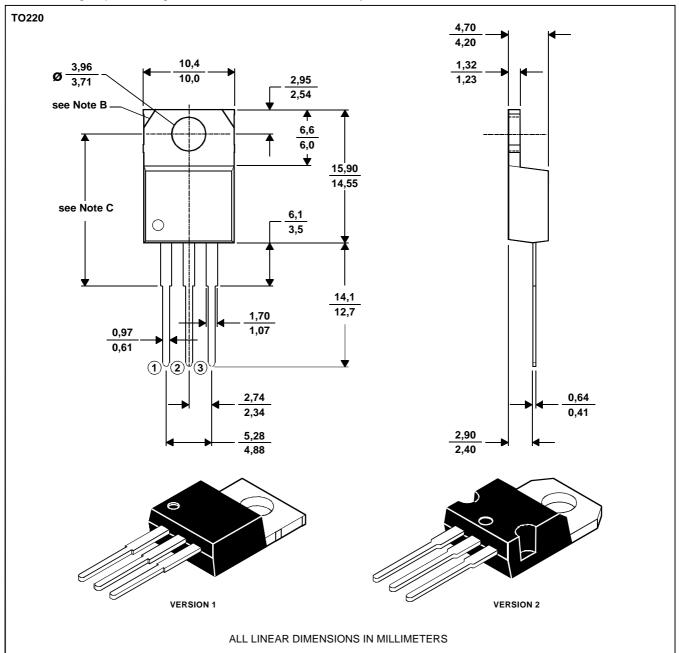
PRODUCT INFORMATION

MECHANICAL DATA

TO-220

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. The centre pin is in electrical contact with the mounting tab.

B. Mounting tab corner profile according to package version.

C. Typical fixing hole centre stand off height according to package version. Version 1, 18.0 mm. Version 2, 17.6 mm. MDXXBE



BD244, BD244A, BD244B, BD244C PNP SILICON POWER TRANSISTORS

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