AZ DISPLAYS, INC.

COMPLETE LCD SOLUTIONS

6.4" VGA Very High Brightness TFT-LCD

ATM6448C-NLW-FBD

(based on PVI:PD064VT2)

COLOR LIQUID CRYSTAL DISPLAY

TECHNICAL SPECIFICATION

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1. Application

This product applies computer peripheral, industrial meter, image communication, web-pad, e-books and multi-media.

2. Features

. Pixel in stripe configuration

. Slim and compact

. Display Colors: 262,144 colors

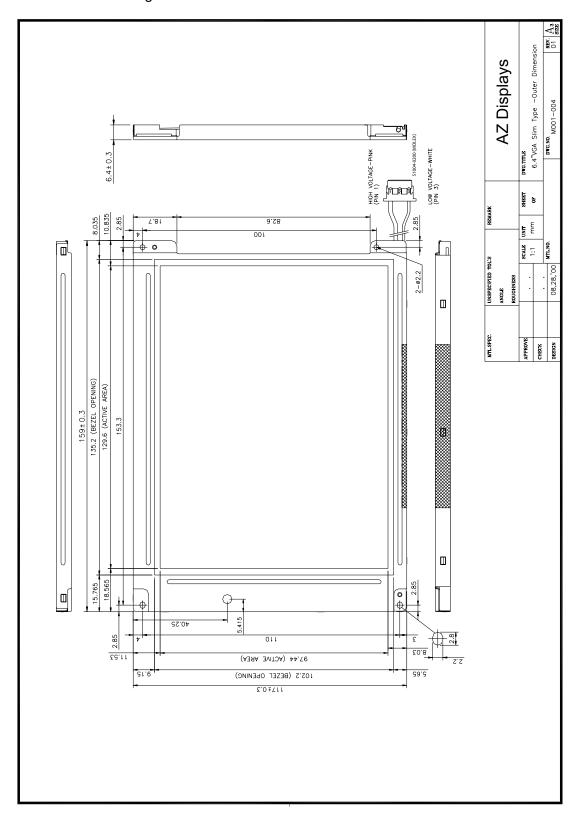
. Viewing Direction : 6 o'clock

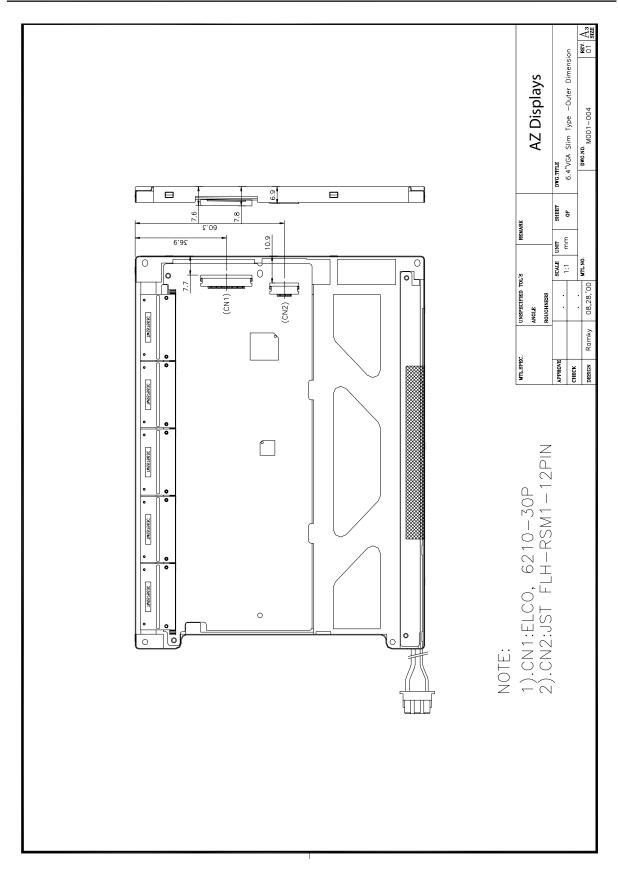
. Slim module design for mobile electronics device application

3. Mechanical Specifications

Parameter	Specifications	Unit
Screen Size	6.4 (diagonal)	inch
Display Format	640×R, G, B×480	dot
Active Area	129.6(H)×97.44 (V)	mm
Dot Pitch	0.0675 (H)×0.203 (V)	mm
Pixel Pitch	0.203 (H)×0.203 (V)	mm
Pixel Configuration	Stripe	
Outline Dimension	See Mechanical Drawing	mm
Weight	165±10	g

4. Mechanical Drawing of TFT-LCD Module





5. Input / Output Terminals

5-1) TFT-LCD Panel Driving

Connector (1) type: ELCO, 6210-30PIN

Pin No.	Symbol	Function	Remark
1	CLK	Clock Signal for Sampling Image Digital Data	
2	Hsync	Horizontal Synchronous Signal	
3	Vsync	Vertical Synchronous Signal	
4	GND	Ground (0V)	
5	R0	Red Image Data Signal (LSB)	
6	R1	Red Image Data Signal	
7	R2	Red Image Data Signal	
8	R3	Red Image Data Signal	
9	R4	Red Image Data Signal	
10	R5	Red Image Data Signal (MSB)	
11	GND	Ground (0V)	
12	G0	Green Image Data Signal (LSB)	
13	G1	Green Image Data Signal	
14	G2	Green Image Data Signal	
15	G3	Green Image Data Signal	
16	G4	Green Image Data Signal	
17	G5	Green Image Data Signal (MSB)	
18	GND	Ground (0V)	
19	B0	Blue Image Data Signal (LSB)	
20	B1	Blue Image Data Signal	
21	B2	Blue Image Data Signal	
22	B3	Blue Image Data Signal	
23	B4	Blue Image Data Signal	
24	B5	Blue Image Data Signal (MSB)	
25	GND	Ground (0V)	
26	NC	No connection	
27	VCC	DC +3.3V Power Supply	
28	VCC	DC +3.3V Power Supply	
29	NC	No connection	
30	NC	No connection	

5-2) Backlight driving

Pin No	Symbol	Description	Remark
1	VL1	Input terminal (Hi voltage side)	Wire color : Pink
2	2 VL2 Input terminal (Low voltage side)		Wire Color : White
Z VLZ	Imput terminar (Low Voltage side)	Note 5-1	

Note 5-1: Low voltage side of backlight connects with ground of LED circuits.

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5-3) Input / Output Connector

A) LCD module connector

ELCO , 6210-30PIN Down Connector

Pin No. : 30 Pitch : 0.5 mm

B) Backlight Connector MOLEX 51004-0200

Pin No. : 2

Red : LED Anode White : LED Cathode

6. Absolute Maximum Ratings:

GND=0V, Ta=25°C

Parameters	Symbol	MIN.	MAX.	Unit	Remark
+3.3V Supply Voltage	V_{cc}	-0.3	+4.0	V	
Input Signals Voltage	$V_{\rm sig}$	-0.3	V _{CC} +0.3	V	Note 6-1
Storage Temperature	T_{stg}	-20	+70	$^{\circ}\!\mathbb{C}$	Note 6-2
Operating Temperature	T _{opa}	-0	+60	$^{\circ}\!\mathbb{C}$	

Note 6-1: Input signals include CLK, Hsync, Vsync, R[0:5], G[0:5] and B[0:5].

Note 6-2 : Humidity : 95% RH Max. at Ta $\leq 40^{\circ}$ C.

Maximum wet-bulb temperature is at 39 $^{\circ}$ C or less at Ta > 40 $^{\circ}$ C.

No condensation.

7. Electrical Characteristics

- 7-1) Recommended Operating Conditions:
 - A) Driving for TFT-LCD panel

GND = 0V, Ta = 25 $^{\circ}$ C

Parameters		Symbol	Min.	Тур.	Max.	Unit	Remark
+3.3V	Supply Voltage	V_{CC}	+3.15	+3.3	+3.6	V	
TO.5V	Supply Input Ripple Voltage	V_{CCRP}			0.1	Vp-p	V _{CC} =+3.3V
Input Signals Voltage (High)		V_{IH}	+3.0	+3.3	+3.6	V	
Input Signals Voltage (Low)		V _{IL}	-	0	+0.3	V	

B) Driving for backlight

Ta = 25 ℃

-						
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
LED Current	I L	-	350	420	mA	Note 7-1
LED Voltage	V	-	11.5	12	V	
Lamp Life Time		-	30,000	-	Hr	

Note 7-1 : In order to satisfy the quality of B/L, the LED current should be less than Max. to avoid the abnormal display image caused by B/L heat.

7-2) Power Consumption

Parameters	Symbol	Тур.	Max.	Unit	Remark
+3.3V Current Dissipation	I _{CC}	170	200	mA	
Input Signals Current (High)	I _{IH}		100	μ A	V _{IH} =+3.3V
Input Signals Current (Low)	I _{IL}		100	μ A	V _{IL} =0V
LCD Panel Power Consumption		0.56	0.66	W	Note 7-4
Backlight Power Consumption		4	5	W	Note 7-5

Note 7-4: The power consumption of backlight is not included.

Note 7-5 : Backlight power consumption is calculated by $I_L \times V_L$.

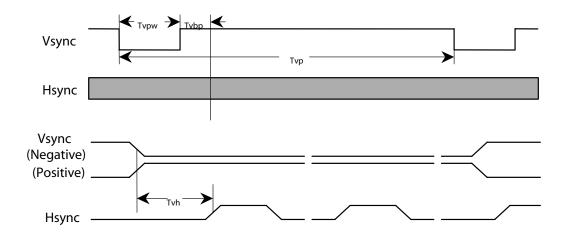
7-3) Input / Output signal timing chart

	Parameters	Symbol	Min.	Тур.	Max.	Unit	Note
	Frequency	Fc=1/Tc		25.175		MHz	Note 7-6
Clock	High Time	Tckh	10			ns	
	Low Time	Tckl	10			ns	
	Periodic = Line	Thp		31.778		μ s	Note 7-6
Hsync				800	1024	clock	Note 7-6
	Pulse Width	Thpw	2	96	200	clock	
	Back Porch	Thbp	2	49	64	clock	
			515	525	1024	line	Note 7-6
Vsync	Pulse Width	Tvpw	1	2		line	
	Back Porch	Tvbp	1	33	64	line	
Data	Setup Time	Tds	10			ns	
	Hold Time	Tdh	10			ns	
	Periodic = Line	Тер		800	1024	clock	
	Pulse Width (H)	Tepw	2	640	800	clock	
Horizon	tal Display Periodic	Thd	640	640	640	clock	
Hsync-CLK		Thc	10		Tc-10	ns	
Phase Difference							
Vsync-Hsync		Tvh	1		Thp-1	clock	
Pha	ase Difference						

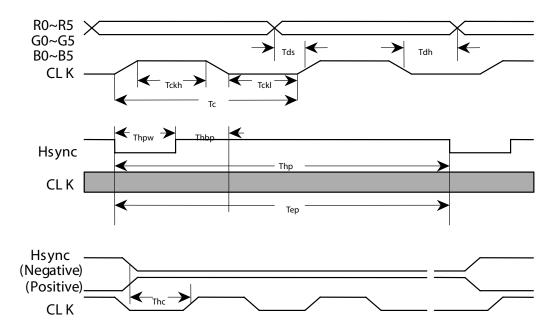
Note 7-6 : To is the period of sampling clock. In case of low-frequency, the image-flicker may occur.

7-4) Display Time Range

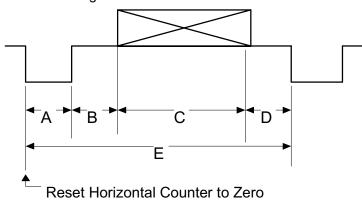
(1) Vertical Timing:



(2) Horizontal Timing:

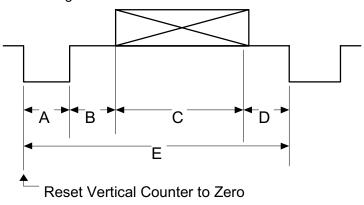


(3). Detail of Horizontal Timing:



Item	Description	Clock Cycles	Time
Α	Horizontal Width	96	3.813 μ s
В	Horizontal B-Porch	49	1.907 μ s
С	Horizontal Display	640	25.422 μs
D	Horizontal F-Porch	16	0.636 μ s
E	Horizontal Total	800	31.778 μs

(4). Detail of Vertical Timing:



ltem	Description	Horizontal Lines	Time
Α	Vertical Width	2	63.5 μ s
В	Vertical B-Porch	33	1.049 ms
С	Vertical Display	480	15.253 ms
D	Vertical F-Porch	10	317.8 μ s
E	Vertical Total	525	16.683 ms

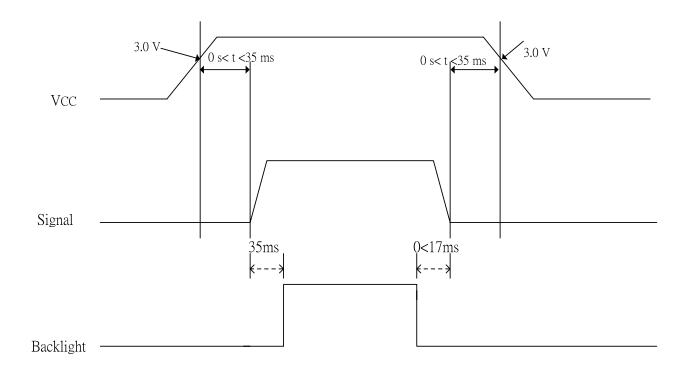
7-5) Pixel Arrangement
The LCD module pixel arrangement is the stripe.

RGBRGBRGB 1st Line RGBRGB 2 nd Line RGB 3rd Line 1 st Pixel 1 Pixel = RGB	R G B R G B R G B 640 th Pixel
RGBRGBRGB 478th Line RGBRGBRGB 479th Line	R G B R G B R G B

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Color		Input Color Data																	
		Red					Green						Blue						
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	В4	В3	B2	B1	B0
Basic	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker																		
Red	↓	\downarrow	↓	\downarrow	\downarrow	\downarrow	↓	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow
	Brighter																		
	Red (61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker																		
Green	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\rightarrow	\downarrow	\	\rightarrow	\rightarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow
	Brighter																		
	Green (61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Blue (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Darker																		
		\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	↓	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	↓	 	\downarrow	\downarrow
	Brighter																		
	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

8. Power On Sequence



- 1. The supply voltage for input signals should be same as $V_{\text{CC.}}$
- 2. When the power is off , please keep whole signals (Hsync, Vsync, CLK, Data) low level or high impedance

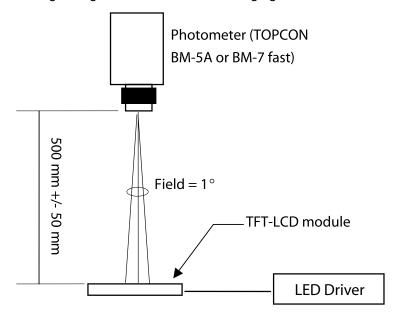
9. Optical Characteristics

9-1) Specification:

Ta=25°C

Parameter		Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remarks
	Horizontal	θ		±35	±45		deg	Note 9-3
Viewing Angle	Vertical	θ (to 12 o'clock)	CR>10	10	15	ı	deg	
	vertical	θ (to 6 o'clock)		30	35	ı	deg	
Contrast Ratio		CR		200	400	-	-	Note 9-1
Response tim	Rise	Tr	<i>θ</i> =0°	-	15	30 ms		Note 9-4
rvesponse um	Fall	Tf	0 =0	-	25	50	ms	11016 3-4
Brightness			<i>θ</i> =0°/ <i>φ</i> =0	-	700		cd/m²	Note 9-2
Luminance	Uniformity	U		55	80	-	%	Note 9-6
Lamp Li	fe Time			-	30,000	-	hr	
White Chromaticity		Х		0.230	0.280	0.330	-	
		У		0.270	0.320	0.370	-	
Cross	Talk		<i>θ</i> =0°	-	-	3	%	Note 9-5

All the optical measurement shall be executed 30 minutes after backlight being turn-on. The optical characteristics shall be measured in dark room (ambient illumination on panel surface less than 1 Lux). The measuring configuration shows as following figure.



Optical characteristics measuring configuration

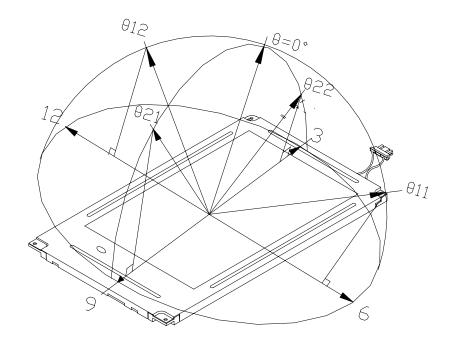
Note 9-1 : CR = Luminance when LCD is White

Luminance when LCD is Black

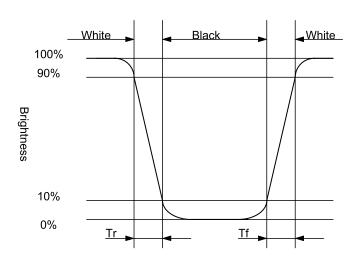
Contrast Ratio is measured in optimum common electrode voltage.

Note 9-2 : Topcon BM-7(fast) luminance meter 1° field of view is used in the testing immediately when the display is powered on (after warm-up, brightness can decrease by up to 5%).

Note 9-3: The definitions of viewing angle diagrams:

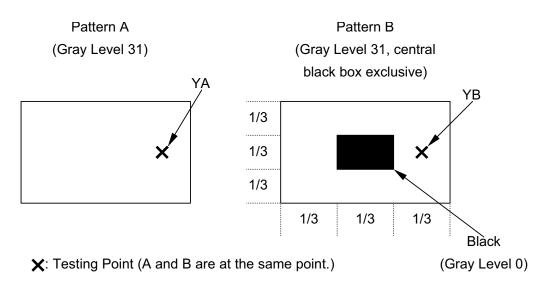


Note 9-4: Definition of Response Time Tr and Tf:



Note 9-5 : Cross Talk (CTK) =
$$\frac{|YA-YB|}{YA} \times 100\%$$

YA: Brightness of Pattern A
YB: Brightness of Pattern B



Note 9-6: The uniformity of LCD is defined as

 $U = \frac{\text{The Minimum Brightness of the 9 testing Points}}{\text{The Minimum Brightness of the 9 testing Points}}$

The Maximum Brightness of the 9 testing Points

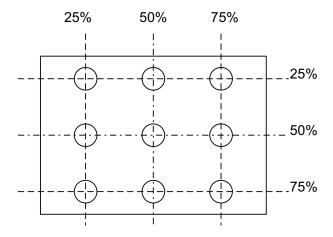
Luminance meter: BM-5A or BM-7 fast(TOPCON)

Measurement distance : 500 mm +/- 50 mm

Ambient illumination : < 1 Lux

Measuring direction: Perpendicular to the surface of module

The test pattern is white (Gray Level 63).



10. Handling Cautions

10-1) Mounting of module

- a) Please power off the module when you connect the input/output connector.
- b) Please connect the ground pattern of the inverter circuit surely. If the connection is not perfect, some following problems may happen possibly.
 - 1. The noise from the backlight unit will increase.
 - 2. The output from inverter circuit will be unstable.
 - 3.In some cases a part of module will heat.
- c) Polarizer which is made of soft material and susceptible to flaw must be handled carefully.
- d) Protective film (Laminator) is applied on surface to protect it against scratches and dirts. It is recommended to peel off the laminator before use and taking care of static electricity.

10-2) Precautions in mounting

- a) When metal part of the TFT-LCD module (shielding lid and rear case) is soiled, wipe it with soft dry cloth.
- b) Wipe off water drops or finger grease immediately. Long contact with water may cause discoloration or spots.
- c) TFT-LCD module uses glass which breaks or cracks easily if dropped or bumped on hard surface. Please handle with care.
- d) Since CMOS LSI is used in the module. So take care of static electricity and earth yourself when handling.

10-3) Adjusting module

- a) Adjusting volumes on the rear face of the module have been set optimally before shipment.
- b) Therefore, do not change any adjusted values. If adjusted values are changed, the specifications described may not be satisfied.

10-4) Others

- a) Do not expose the module to direct sunlight or intensive ultraviolet rays for many hours.
- b) Store the module at a room temperature place.
- c) The voltage of beginning electric discharge may over the normal voltage because of leakage current from approach conductor by to draw lump read lead line around.
- d) If LCD panel breaks, it is possibly that the liquid crystal escapes from the panel. Avoid putting it into eyes or mouth. When liquid crystal sticks on hands, clothes or feet. Wash it out immediately with soap.
- e) Observe all other precautionary requirements in handling general electronic components.
- f) Please adjust the voltage of common electrode as material of attachment by 1 module.
- g) The UL number for PCB is EE2956.

11. Reliability Test

No	Test Item	Test Condition						
1	High Temperature Storage Test	Ta = +70 ℃, 240 hrs						
2	Low Temperature Storage Test	Ta = -20 ℃, 240 hrs						
3	High Temperature Operation Test	Ta = +60 ℃, 240 hrs						
4	Low Temperature Operation Test	Ta = 0 ℃, 240 hrs						
5	High Temperature & High Humidity Operation Test	Ta = +40 ℃, 95%RH, 240 hrs						
6	Thermal Cycling Test	-20°C →+70°C , 200 Cycles						
	(non-operating))	30 min 30min						
		Frequency: 10 ~ 57 H _Z /Vibration Width: 0.075mm						
7	Vibration Test	58-500 H _Z / Gravity :9.8m/s ²						
'	(non-operating)	Sweep time: 11 minutes						
		Test period: 3 hrs for each direction of X, Y, Z						
8	Charle Tant	Gravity :490m/s²						
	Shock Test	Direction: ±X, ±Y, ±Z						
	(non-operating)	Pulse Width :11ms,half sine wave						
9		150pF $$, 330 $$ Ω						
	Electrostatic Discharge Test	Air : ±15KV;Contact : ±8KV						
	(non-operating)	10 times/point , 9 point/panel face						

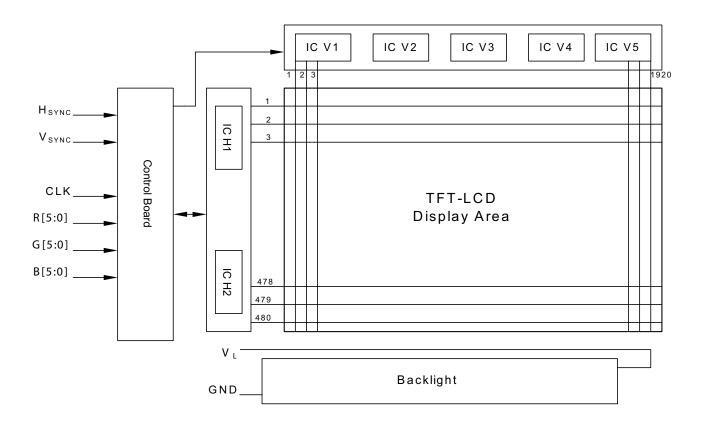
Ta: ambient temperature

Note 11-1: The protective film must be removed before temperature test.

[Judgement Criteria]

Under the display quality test conditions with normal operation state, there should be no change which may affect practical display function.

AZ Displays 12. Block Diagram



13. Packing

