



LD29150XX LD29150XXxx

1.5 A, very low drop voltage regulators

Features

- Very low dropout voltage (typ. 0.4 at 1.5 A)
- Guaranteed output current up to 1.5 A
- Fixed and adjustable output voltage ($\pm 1\%$ at 25 °C)
- Internal current and thermal limit
- Logic controlled electronic shutdown available in PPAK and P²PAK

Description

The LD29150xx is a high current, high accuracy, low-dropout voltage regulator series. These regulators feature 400 mV dropout voltage and very low ground current. Designed for high current loads, these devices are also used in lower current, extremely low dropout-critical systems, where their tiny dropout voltage and ground current values are important attributes. Typical applications are in power supply switching post regulation, series power supply for monitors, series power supply for VCRs and TVs, computer systems and battery powered systems.

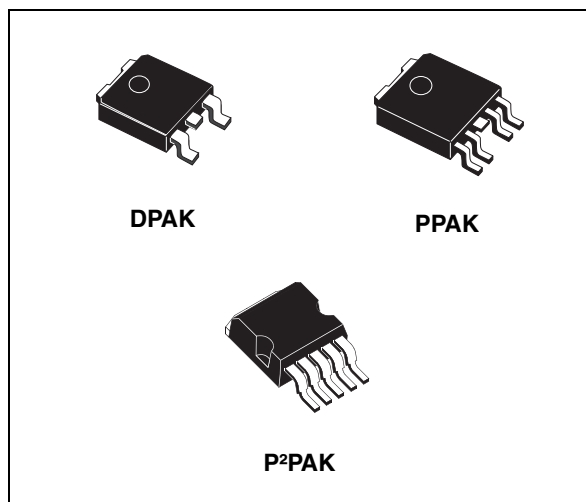


Table 1. Device summary

Part numbers	Order codes			Output voltages
	DPAK	PPAK	P ² PAK	
LD29150XX18	LD29150DT18R			1.8 V
LD29150XX25	LD29150DT25R			2.5 V
LD29150XX33	LD29150DT33R		LD29150P2T33R	3.3 V
LD29150XX50	LD29150DT50R	LD29150PT50R		5.0 V
LD29150XX		LD29150PTR		ADJ

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1 Diagram

The diagram illustrates the internal architecture of the CS15250 voltage regulator. Key components and their connections include:

- Inputs:** V_I (Input Voltage) and INH (Inhibit).
- Functional Blocks:**
 - DUMP PROTECTION:** Connected to V_I and the INH input.
 - START-UP:** Connected to INH and the V_I rail.
 - VOLTAGE REFERENCE:** Provides a reference voltage to the error amplifier.
 - CURRENT LIMIT:** Monitors the output current and provides feedback to the error amplifier.
 - THERMAL PROTECTION:** Monitors the device temperature and provides feedback to the error amplifier.
- Control Loop:** An error amplifier (represented by a triangle) compares the voltage reference with feedback from the current limit and thermal protection blocks. Its output drives the base of a pass transistor.
- Output Stage:** A power MOSFET (represented by a large transistor symbol) acts as the pass element, connected between the V_I rail and the output node.
- Output:** V_O (Output Voltage).
- Other Components:** A **POWER** block is connected to the output, and a **GND** connection is shown for the feedback network.

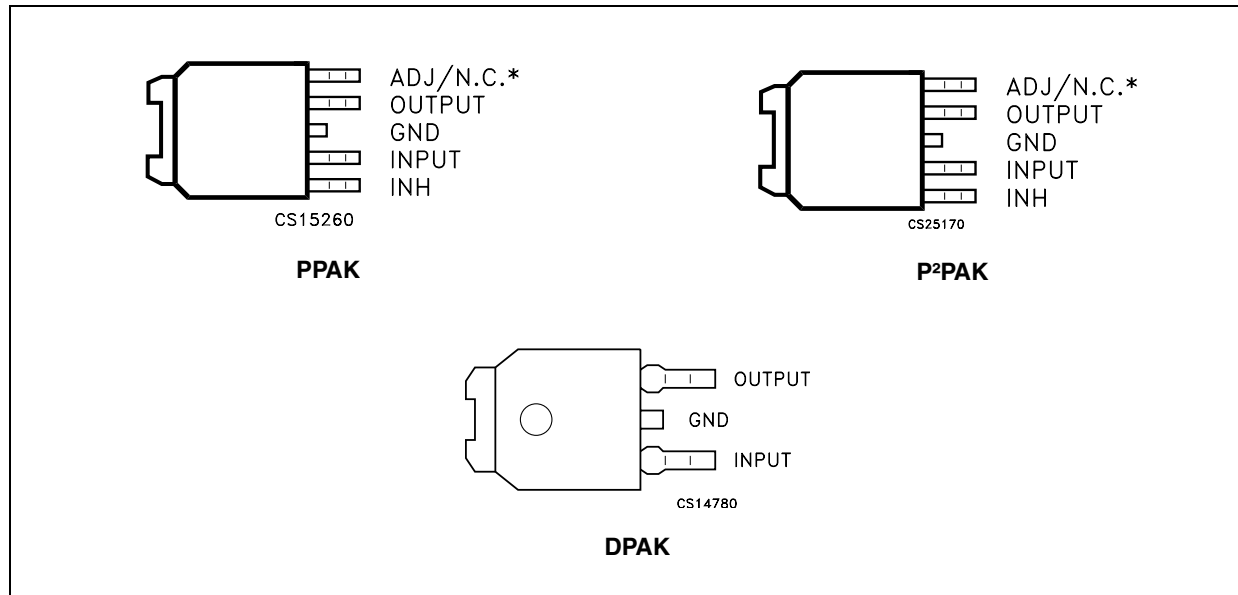
The block diagram illustrates the internal architecture of the CS12430 voltage regulator. Key components and their connections include:

- Inputs:** V_I (Input Voltage) and INH^* (Inhibit Input).
- Functional Blocks:**
 - DUMP PROTECTION:** Connected to V_I and the INH^* input.
 - START-UP:** Connected to INH^* and the V_I line.
 - VOLTAGE REFERENCE:** Provides a reference voltage to the error amplifier.
 - CURRENT LIMIT:** Monitors the output current and provides a feedback signal to the error amplifier.
 - THERMAL PROTECTION:** Monitors the device temperature and provides a feedback signal to the error amplifier.
 - POWER:** The main power stage, connected to V_I and the output V_O .
- Output Stage:** The error amplifier (represented by a triangle with '+' and '-' inputs) drives a pass transistor (NPN) that regulates the output voltage V_O . The output is also connected to a load (represented by a resistor) and a feedback network (represented by two resistors in series).
- Grounding:** The output is connected to GND through a feedback network.

* Only for version with inhibit function.

2 Pin configuration

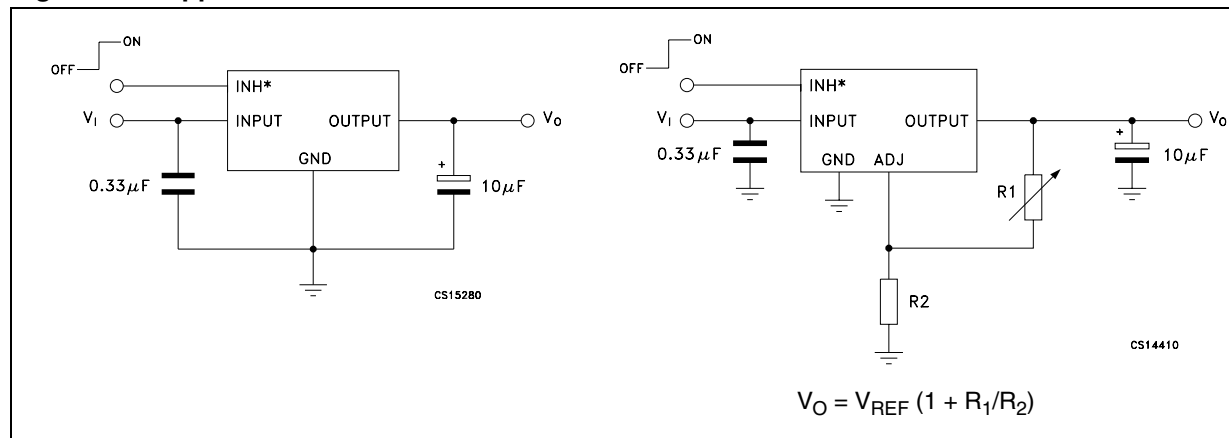
Figure 3. Pin connections (top view)



* Not connected for fixed version.

3 Typical application

Figure 4. Application circuit



* Only for version with inhibit function.

4 Maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_I	DC input voltage	30 ⁽¹⁾	V
V_O	DC output voltage	-0.3 to 20	V
V_{INH}	Inhibit input voltage	-0.3 to 20	V
I_O	Output current	Internally limited	mA
P_D	Power dissipation	Internally limited	mW
T_{STG}	Storage temperature range	-55 to 150	°C
T_{OP}	Operating junction temperature range	-40 to 125	°C

1. Above 14 V the device is automatically in shut-down.

Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied.

Table 3. Thermal data

Symbol	Parameter	DPAK	PPAK	P ² PAK	Unit
R_{thJA}	Thermal resistance junction-ambient	100	100	60	°C/W
R_{thJC}	Thermal resistance junction-case	8	8	3	°C/W

5 Electrical characteristics

$I_O = 10\text{ mA}$, $T_J = 25\text{ °C}$, $V_I = 3.8\text{ V}$, $V_{INH} = 2\text{ V}$ ([Note 3](#)), $C_I = 330\text{ nF}$, $C_O = 10\text{ }\mu\text{F}$, unless otherwise specified.

Table 4. Electrical characteristics of LD29150#18

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$I_O = 10\text{ mA}$ to 1.5 A , $V_I = 3$ to 7.3 V $T_J = -40$ to 125 °C	1.782	1.8	1.818	V
			1.764		1.836	
ΔV_O	Load regulation	$I_O = 10\text{ mA}$ to 1.5 A		0.2	1.0	%
ΔV_O	Line regulation	$V_I = 3$ to 13 V		0.06	0.5	%
SVR	Supply voltage rejection	$f = 120\text{ Hz}$, $V_I = 3.8 \pm 1\text{ V}$, $I_O = 0.75\text{ A}$ (Note 1)	62	72		dB
V_{DROP}	Dropout voltage	$I_O = 250\text{ mA}$, $T_J = -40$ to 125 °C (Note 2)		0.1		V
		$I_O = 0.75\text{ A}$, $T_J = -40$ to 125 °C (Note 2)		0.2		
		$I_O = 1.5\text{ A}$, $T_J = -40$ to 125 °C (Note 2)		0.4	0.7	
I_q	Quiescent current	$I_O = 0.75\text{ A}$, $T_J = -40$ to 125 °C		15	40	mA
		$I_O = 1.5\text{ A}$, $T_J = -40$ to 125 °C		30	80	
		$V_I = 13\text{ V}$, $V_{INH} = \text{GND}$, $T_J = -40$ to 125 °C		130	180	μA
I_{sc}	Short circuit current	$V_I - V_O = 5.5\text{ V}$		2.2		A
V_{IL}	Control input logic low	OFF MODE, (Note 3), $T_J = -40$ to 125 °C			0.8	V
V_{IH}	Control input logic high	ON MODE, (Note 3), $T_J = -40$ to 125 °C	2			V
I_{INH}	Control input current	$T_J = -40$ to 125 °C , $V_{INH} = 13\text{ V}$		5	10	μA
eN	Output noise voltage	$B_P = 10\text{ Hz}$ to 100 kHz , $I_O = 100\text{ mA}$		72		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		°C

Note: 1 Guaranteed by design.

2 Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99 % of its nominal value with $V_O + 1\text{ V}$ applied to V_I .

3 Only for version with Inhibit function.

$I_O = 10 \text{ mA}$, $T_J = 25 \text{ }^\circ\text{C}$, $V_I = 4.5 \text{ V}$, $V_{INH} = 2 \text{ V}$ (*Note 3*), $C_I = 330 \text{ nF}$, $C_O = 10 \text{ }\mu\text{F}$, unless otherwise specified.

Table 5. Electrical characteristics of LD29150#25

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$I_O = 10 \text{ mA}$ to 1.5 A , $V_I = 3.5$ to 8 V $T_J = -40$ to 125°C	2.475	2.5	2.525	V
			2.45		2.55	
ΔV_O	Load regulation	$I_O = 10 \text{ mA}$ to 1.5 A		0.2	1.0	%
ΔV_O	Line regulation	$V_I = 3.5$ to 13 V		0.06	0.5	%
SVR	Supply voltage rejection	$f = 120 \text{ Hz}$, $V_I = 4.5 \pm 1 \text{ V}$, $I_O = 0.75 \text{ A}$ (<i>Note 1</i>)	55	70		dB
V_{DROP}	Dropout voltage	$I_O = 250 \text{ mA}$, $T_J = -40$ to 125°C (<i>Note 2</i>)		0.1		V
		$I_O = 0.75 \text{ A}$, $T_J = -40$ to 125°C (<i>Note 2</i>)		0.2		
		$I_O = 1.5 \text{ A}$, $T_J = -40$ to 125°C (<i>Note 2</i>)		0.4	0.7	
I_q	Quiescent current	$I_O = 0.75 \text{ A}$, $T_J = -40$ to 125°C		15	40	mA
		$I_O = 1.5 \text{ A}$, $T_J = -40$ to 125°C		30	80	
		$V_I = 13 \text{ V}$, $V_{INH} = \text{GND}$, $T_J = -40$ to 125°C		130	180	μA
I_{sc}	Short circuit current	$V_I - V_O = 5.5 \text{ V}$		2.2		A
V_{IL}	Control input logic low	OFF MODE, (<i>Note 3</i>), $T_J = -40$ to 125°C			0.8	V
V_{IH}	Control input logic high	ON MODE, (<i>Note 3</i>), $T_J = -40$ to 125°C	2			V
I_{INH}	Control input current	$T_J = -40$ to 125°C , $V_{INH} = 13 \text{ V}$		5	10	μA
eN	Output noise voltage	$B_P = 10 \text{ Hz}$ to 100 kHz , $I_O = 100 \text{ mA}$		100		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		$^\circ\text{C}$

Note: 1 Guaranteed by design.

2 Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99 % of its nominal value with $V_O + 1 \text{ V}$ applied to V_I .

3 Only for version with Inhibit function.

$I_O = 10\text{ mA}$, $T_J = 25\text{ °C}$, $V_I = 5.3\text{ V}$, $V_{INH} = 2\text{ V}$ (Note 3), $C_I = 330\text{ nF}$, $C_O = 10\text{ }\mu\text{F}$, unless otherwise specified.

Table 6. Electrical characteristics of LD29150#33

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$I_O = 10\text{ mA}$ to 1.5 A , $V_I = 4.3$ to 8.8 V $T_J = -40$ to 125 °C	3.267	3.3	3.333	V
			3.234		3.366	
ΔV_O	Load regulation	$I_O = 10\text{ mA}$ to 1.5 A		0.2	1.0	%
ΔV_O	Line regulation	$V_I = 4.3$ to 13 V		0.06	0.5	%
SVR	Supply voltage rejection	$f = 120\text{ Hz}$, $V_I = 5.3 \pm 1\text{ V}$, $I_O = 0.75\text{ A}$ (Note 1)	52	67		dB
V_{DROP}	Dropout voltage	$I_O = 250\text{ mA}$, $T_J = -40$ to 125 °C (Note 2)		0.1		V
		$I_O = 0.75\text{ A}$, $T_J = -40$ to 125 °C (Note 2)		0.2		
		$I_O = 1.5\text{ A}$, $T_J = -40$ to 125 °C (Note 2)		0.4	0.7	
I_q	Quiescent current	$I_O = 0.75\text{ A}$, $T_J = -40$ to 125 °C		15	40	mA
		$I_O = 1.5\text{ A}$, $T_J = -40$ to 125 °C		30	80	
		$V_I = 13\text{ V}$, $V_{INH} = \text{GND}$, $T_J = -40$ to 125 °C		130	180	μA
I_{sc}	Short circuit current	$V_I - V_O = 5.5\text{ V}$		2.2		A
V_{IL}	Control input logic low	OFF MODE, (Note 3), $T_J = -40$ to 125 °C			0.8	V
V_{IH}	Control input logic high	ON MODE, (Note 3), $T_J = -40$ to 125 °C	2			V
I_{INH}	Control input current	$T_J = -40$ to 125 °C , $V_{INH} = 13\text{ V}$		5	10	μA
eN	Output noise voltage	$B_P = 10\text{ Hz}$ to 100 kHz , $I_O = 100\text{ mA}$		132		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		$^{\circ}\text{C}$

Note: 1 Guaranteed by design.

2 Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99 % of its nominal value with $V_O + 1\text{ V}$ applied to V_I .

3 Only for version with Inhibit function.

$I_O = 10 \text{ mA}$, $T_J = 25 \text{ }^\circ\text{C}$, $V_I = 7 \text{ V}$, $V_{INH} = 2 \text{ V}$ ([Note 3](#)), $C_I = 330 \text{ nF}$, $C_O = 10 \text{ }\mu\text{F}$, unless otherwise specified.

Table 7. Electrical characteristics of LD29150#50

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$I_O = 10 \text{ mA}$ to 1.5 A , $V_I = 6$ to 10.5 V $T_J = -40$ to 125°C	4.95	5	5.05	V
			4.9		5.1	
ΔV_O	Load regulation	$I_O = 10 \text{ mA}$ to 1.5 A		0.2	1.0	%
ΔV_O	Line regulation	$V_I = 6$ to 13 V		0.06	0.5	%
SVR	Supply voltage rejection	$f = 120 \text{ Hz}$, $V_I = 7 \pm 1 \text{ V}$, $I_O = 0.75 \text{ A}$ (Note 1)	49	64		dB
V_{DROP}	Dropout voltage	$I_O = 250 \text{ mA}$, $T_J = -40$ to 125°C (Note 2)		0.1		V
		$I_O = 0.75 \text{ A}$, $T_J = -40$ to 125°C (Note 2)		0.2		
		$I_O = 1.5 \text{ A}$, $T_J = -40$ to 125°C (Note 2)		0.4	0.7	
I_q	Quiescent current	$I_O = 0.75 \text{ A}$, $T_J = -40$ to 125°C		15	40	mA
		$I_O = 1.5 \text{ A}$, $T_J = -40$ to 125°C		30	80	
		$V_I = 13 \text{ V}$, $V_{INH} = \text{GND}$, $T_J = -40$ to 125°C		130	180	μA
I_{sc}	Short circuit current	$V_I - V_O = 5.5 \text{ V}$		2.2		A
V_{IL}	Control input logic low	OFF MODE, (Note 3), $T_J = -40$ to 125°C			0.8	V
V_{IH}	Control input logic high	ON MODE, (Note 3), $T_J = -40$ to 125°C	2			V
I_{INH}	Control input current	$T_J = -40$ to 125°C , $V_{INH} = 13 \text{ V}$		5	10	μA
eN	Output noise voltage	$B_P = 10 \text{ Hz}$ to 100 kHz , $I_O = 100 \text{ mA}$		200		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		$^\circ\text{C}$

Note: 1 Guaranteed by design.

2 Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99 % of its nominal value with $V_O + 1 \text{ V}$ applied to V_I .

3 Only for version with Inhibit function.

$I_O = 10\text{ mA}$, $T_J = 25\text{ °C}$, $V_I = 3.23\text{ V}$, $V_{INH} = 2\text{ V}$ ([Note 3](#)), $C_I = 330\text{ nF}$, $C_O = 10\text{ }\mu\text{F}$ adjust pin tied to output pin.

Table 8. Electrical characteristics of LD29150#ADJ

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_I	Minimum operating input voltage	$I_O = 10\text{ mA}$ to 1.5 A , $T_J = -40$ to 125 °C	2.5			V
ΔV_O	Load regulation	$I_O = 10\text{ mA}$ to 1.5 A		0.2	1.0	%
ΔV_O	Line regulation	$V_I = 2.5\text{ V}$ to 13 V , $I_O = 10\text{ mA}$		0.06	0.5	%
V_{REF}	Reference voltage	$I_O = 10\text{ mA}$ to 1.5 A , $V_I = 2.5$ to 4.5 V $T_J = -40$ to 125 °C (Note 2)	-1%	1.23	+1%	V
			-2%		+2%	
SVR	Supply voltage rejection	$f = 120\text{ Hz}$, $V_I = 3.23 \pm 1\text{ V}$, $I_O = 0.75\text{ A}$ (Note 1)	45	75		dB
I_q	Quiescent current	$I_O = 0.75\text{ A}$, $T_J = -40$ to 125 °C		15	40	mA
		$I_O = 1.5\text{ A}$, $T_J = -40$ to 125 °C		30	80	
		$V_I = 13\text{ V}$, $V_{INH} = \text{GND}$, $T_J = -40$ to 125 °C		130	180	μA
I_{ADJ}	Adjust pin current	$T_J = -40$ to 125 °C (Note 1)			1	μA
I_{sc}	Short circuit current	$V_I - V_O = 5.5\text{ V}$		2.2		A
V_{IL}	Control input logic low	OFF MODE, (Note 3), $T_J = -40$ to 125 °C			0.8	V
V_{IH}	Control input logic high	ON MODE, (Note 3), $T_J = -40$ to 125 °C	2			V
I_{INH}	Control input current	$T_J = -40$ to 125 °C , $V_{INH} = 13\text{ V}$		5	10	μA
eN	Output noise voltage	$B_P = 10\text{ Hz}$ to 100 kHz , $I_O = 100\text{ mA}$		50		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		°C

Note: 1 Guaranteed by design.

2 Reference voltage is measured between output and GND pin, with ADJ PIN tied to V_{OUT} .

3 Only for version with Inhibit function.

6 Typical characteristics

Figure 5. Output voltage vs. temperature

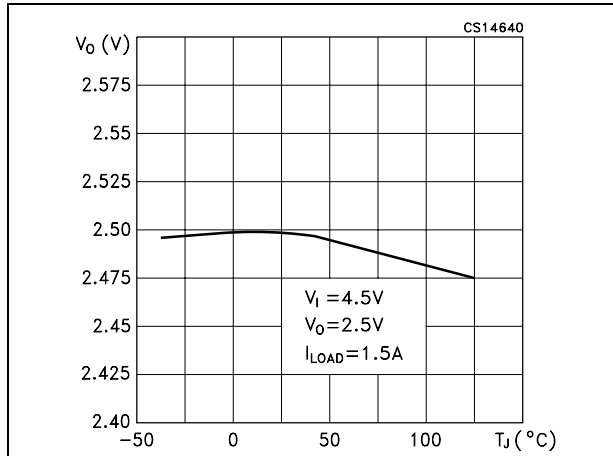


Figure 6. Reference voltage vs. temperature

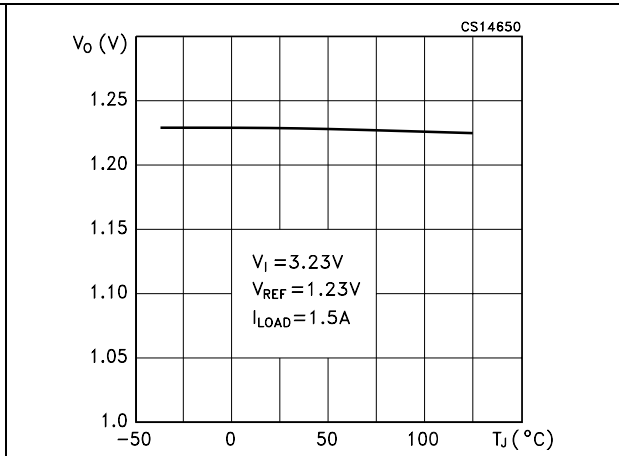


Figure 7. Dropout voltage vs. temperature

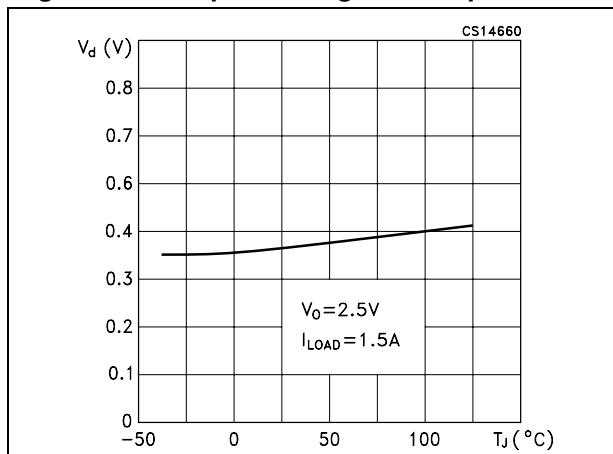


Figure 8. Dropout voltage vs. output current

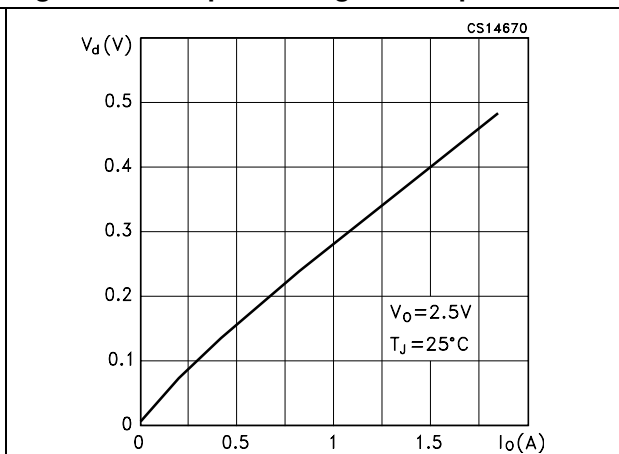
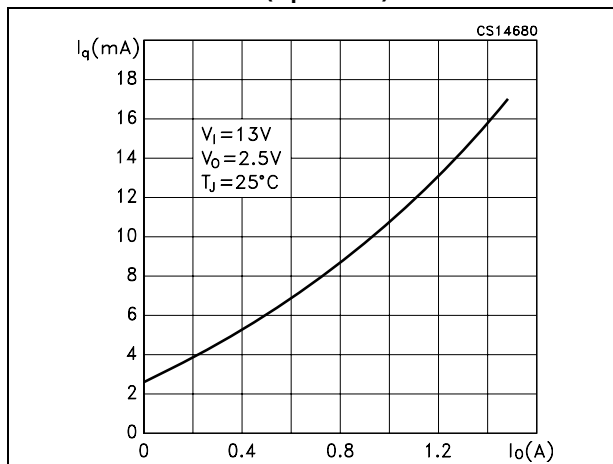
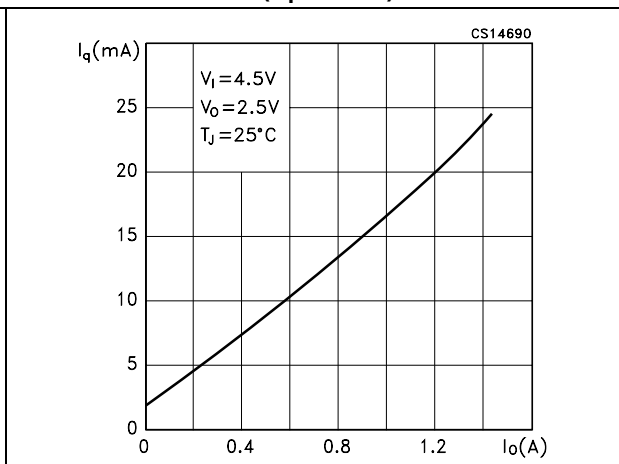
Figure 9. Quiescent current vs. output current ($V_I = 13V$)Figure 10. Quiescent current vs. output current ($V_I = 4.5V$)

Figure 11. Quiescent current vs. supply voltage

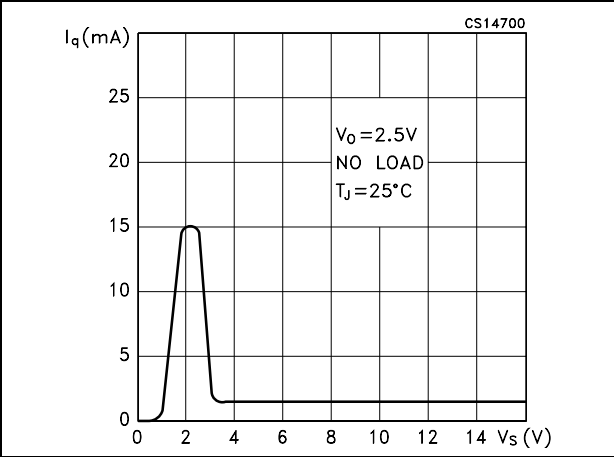


Figure 12. Quiescent current vs. temperature ($I_o = 10\text{ mA}$)

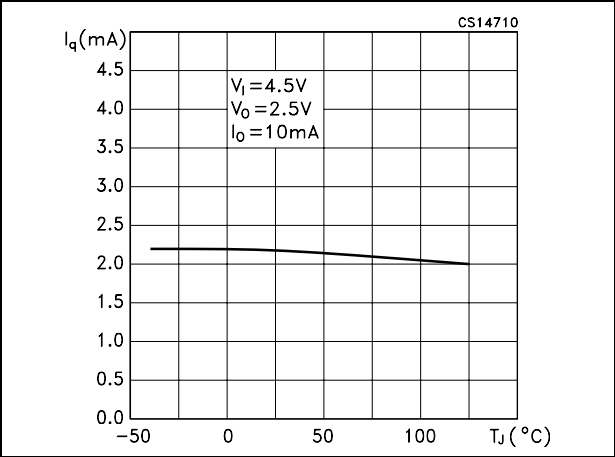


Figure 13. Quiescent current vs. temperature ($I_o = 1.5\text{ A}$)

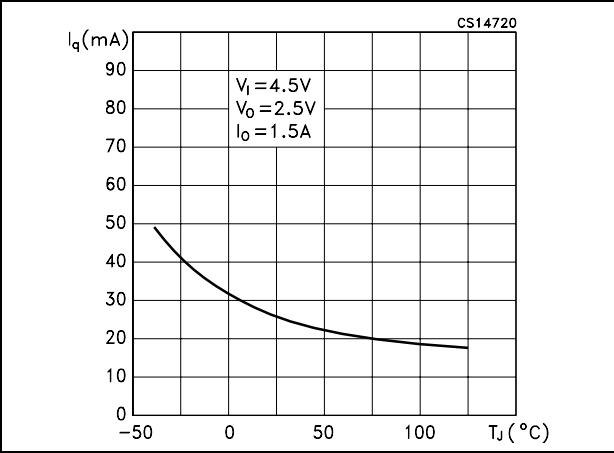


Figure 14. Short circuit current vs. temperature

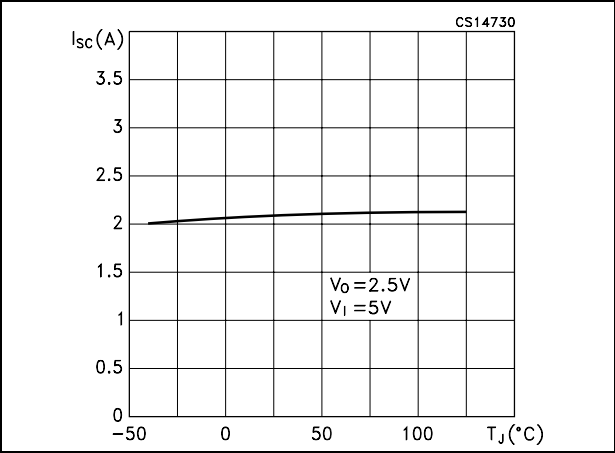


Figure 15. Adjust pin current vs. temperature

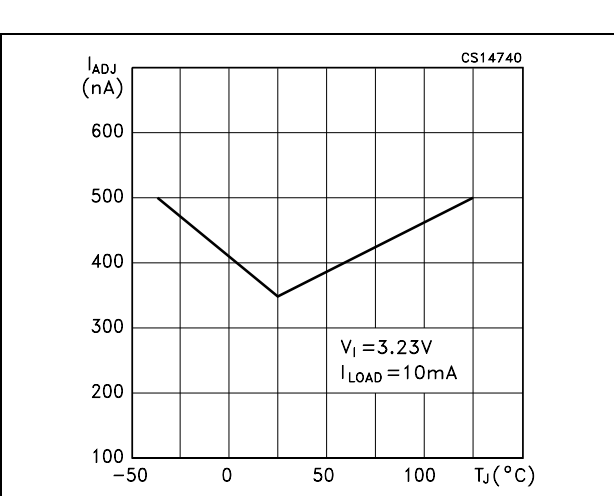


Figure 16. Supply voltage rejection vs. temperature

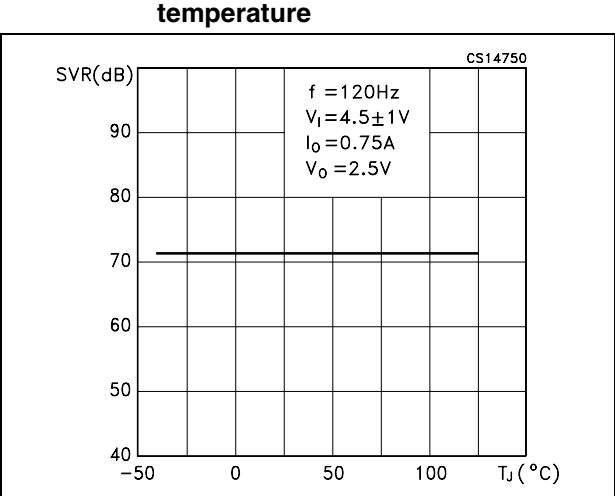


Figure 17. Output voltage vs. input voltage

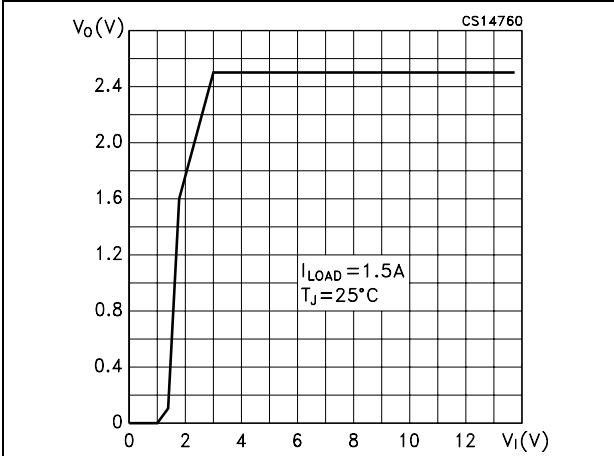


Figure 18. Stability vs. C_O

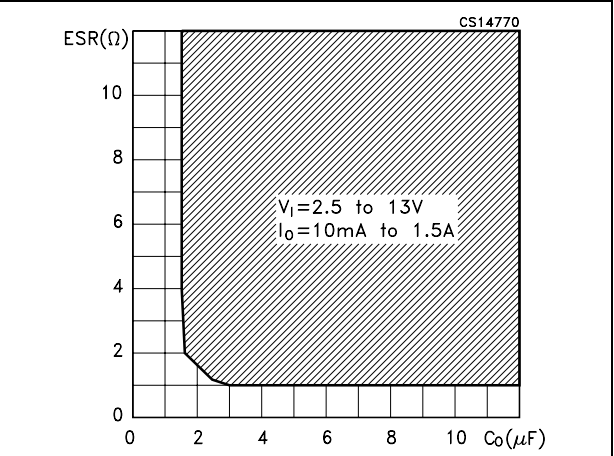


Figure 19. Line transient

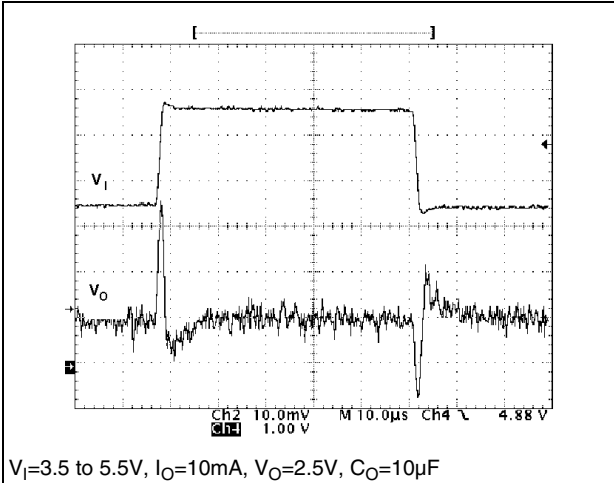


Figure 20. Load transient

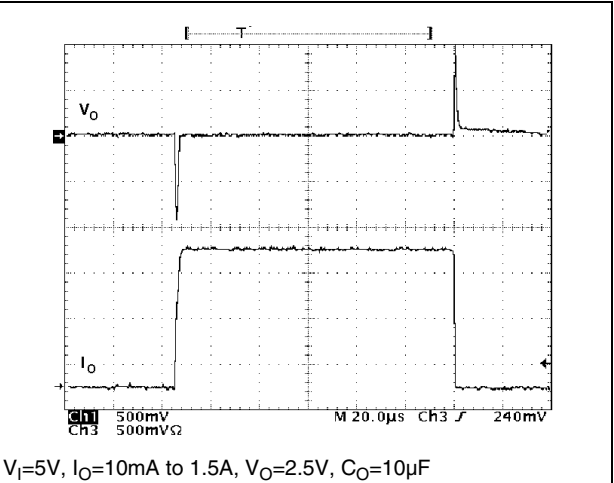


Figure 21. Start-up time 10 mA

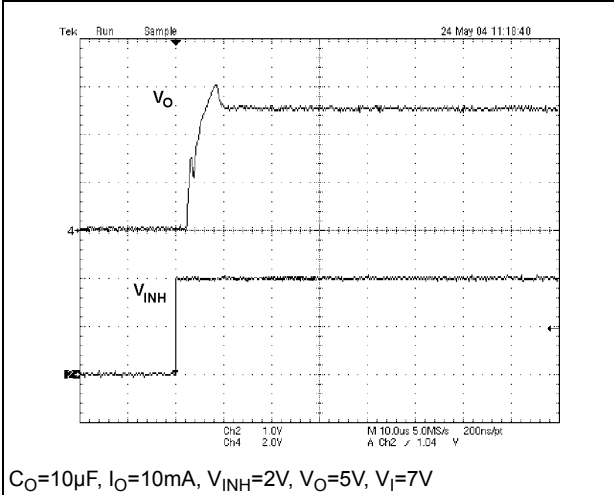
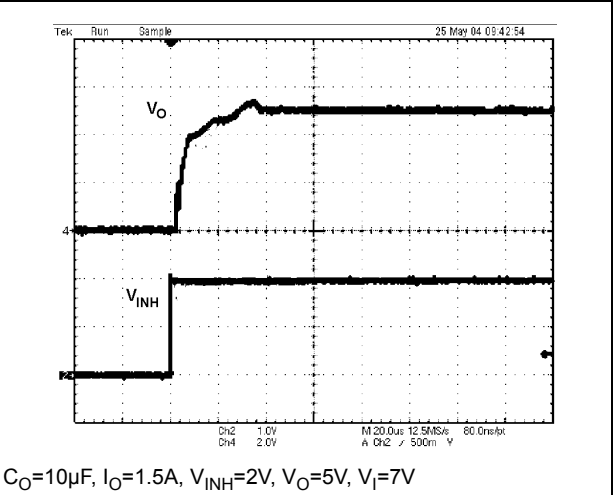


Figure 22. Start-up time 1.5 A

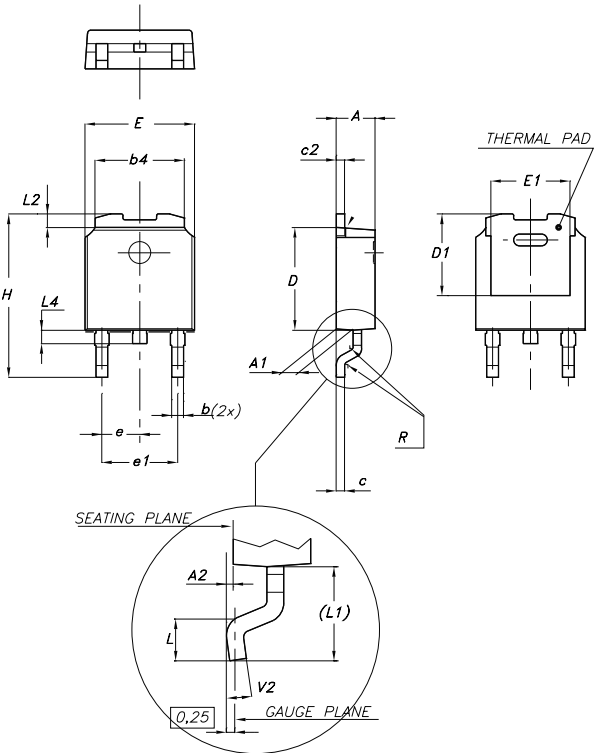


7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

DPAK mechanical data

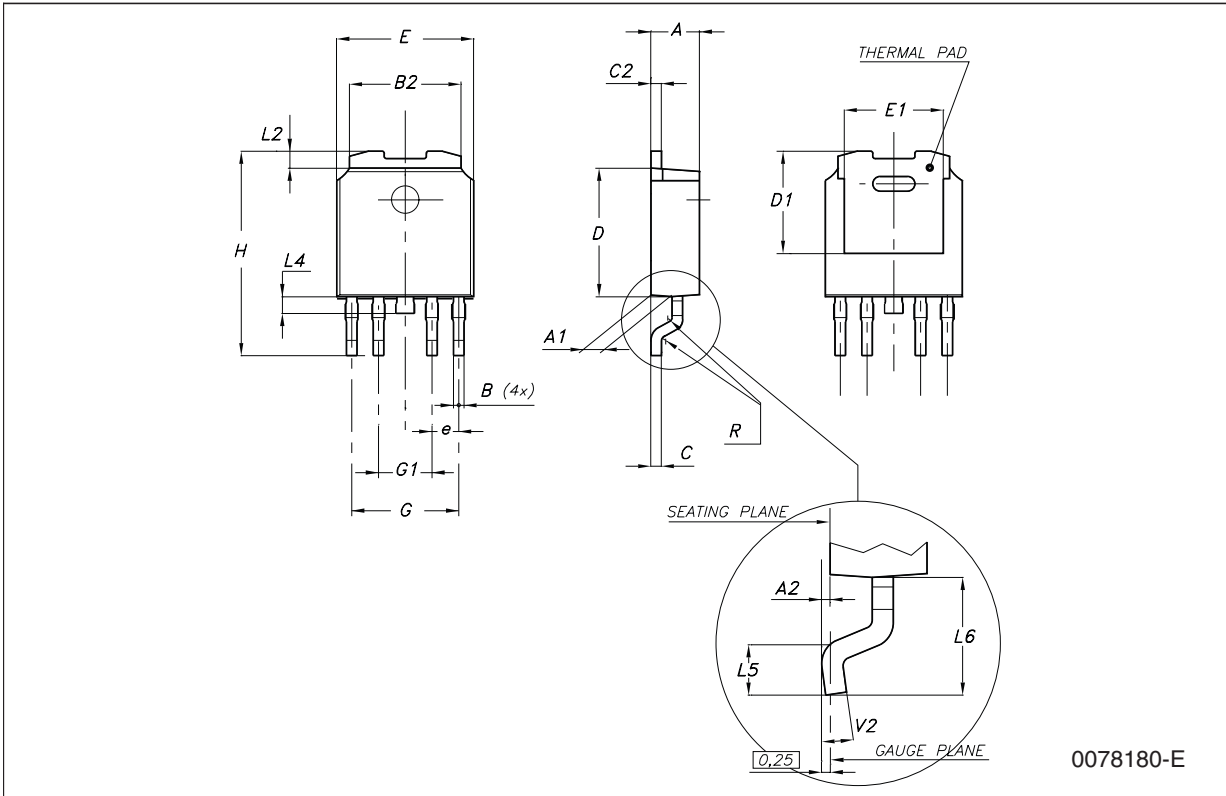
Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A2	0.03		0.23	0.001		0.009
b	0.64		0.9	0.025		0.035
b4	5.2		5.4	0.204		0.212
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
D1		5.1			0.200	
E	6.4		6.6	0.252		0.260
E1		4.7			0.185	
e		2.28			0.090	
e1	4.4		4.6	0.173		0.181
H	9.35		10.1	0.368		0.397
L	1			0.039		
(L1)		2.8			0.110	
L2		0.8			0.031	
L4	0.6		1	0.023		0.039
R		0.2			0.008	
V2	0°		8°	0°		8°



0068772-F

PPAK mechanical data

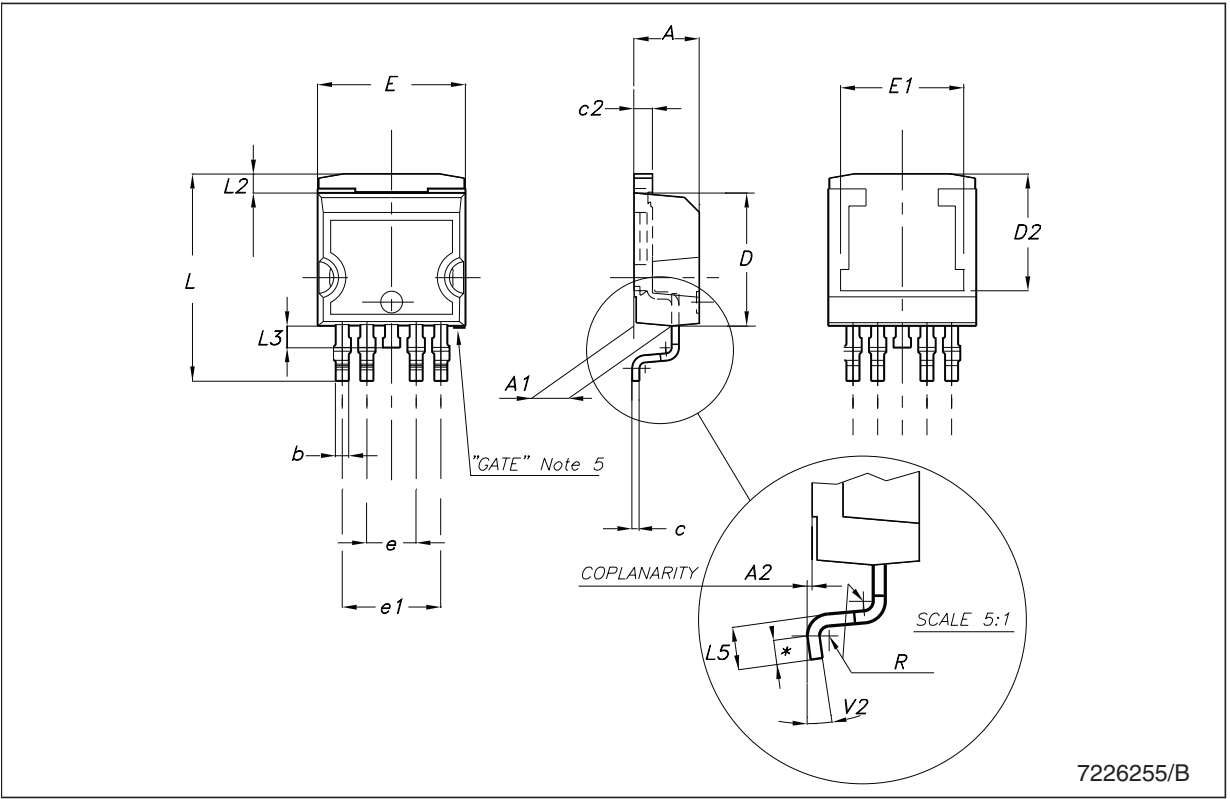
Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A2	0.03		0.23	0.001		0.009
B	0.4		0.6	0.015		0.023
B2	5.2		5.4	0.204		0.212
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
D1		5.1			0.201	
E	6.4		6.6	0.252		0.260
E1		4.7			0.185	
e		1.27			0.050	
G	4.9		5.25	0.193		0.206
G1	2.38		2.7	0.093		0.106
H	9.35		10.1	0.368		0.397
L2		0.8	1		0.031	0.039
L4	0.6		1	0.023		0.039
L5	1			0.039		
L6		2.8			0.110	



0078180-E

P²PAK mechanical data

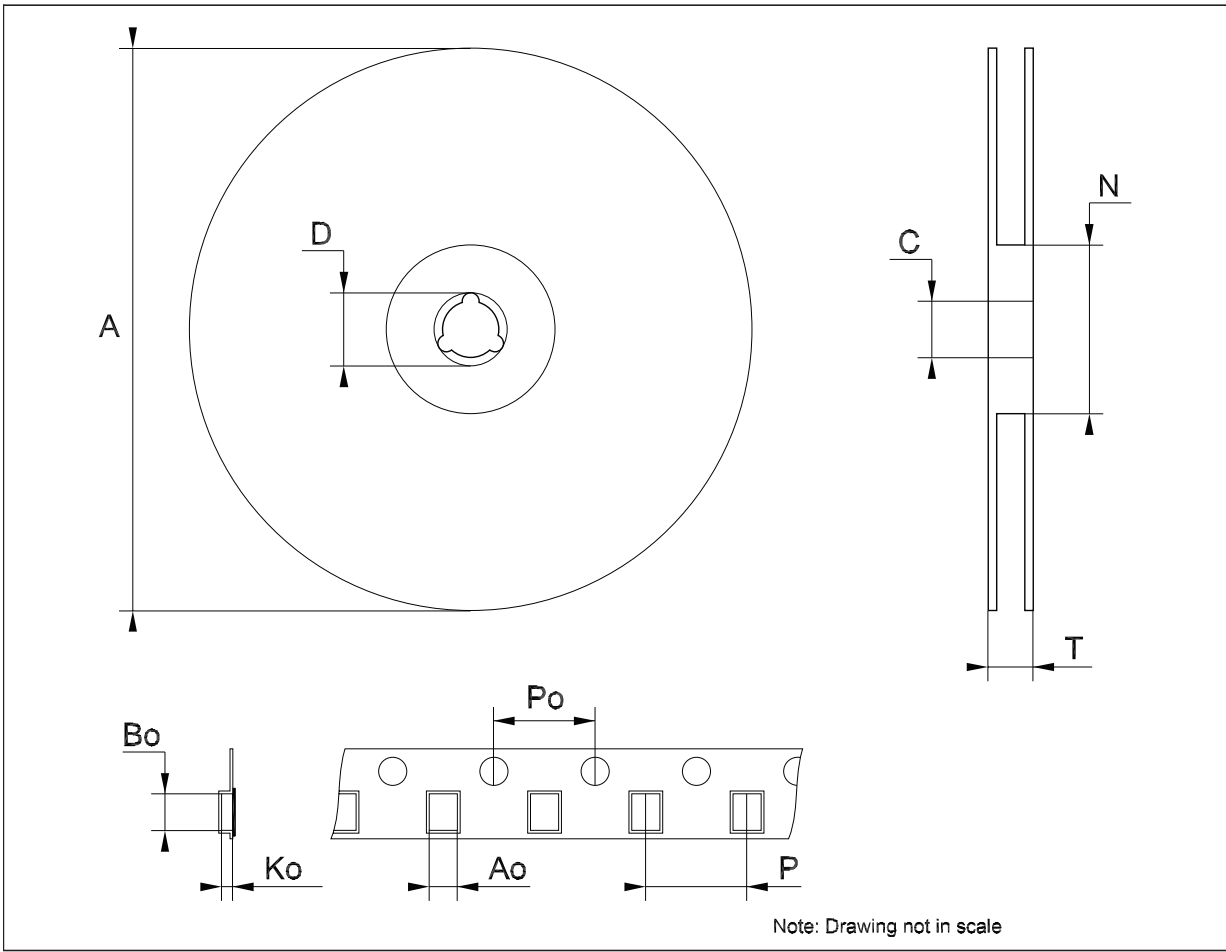
Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.30		4.80	0.169		0.188
A1	2.40		2.80	0.094		0.110
A2	0.03		0.23	0.001		0.009
b	0.80		1.05	0.031		0.041
c	0.45		0.60	0.017		0.023
c2	1.17		1.37	0.046		0.053
D	8.95		9.35	0.352		0.368
D2		8			0.315	
E	10.00		10.40	0.393		0.409
E1		8.5			0.334	0.409
e	3.20		3.60	0.126		0.142
e1	6.60		7.00	0.260		0.275
L	13.70		14.50	0.539		0.571
L2	1.25		1.40	0.049		0.055
L3	0.90		1.70	0.035		0.067
L5	1.55		2.40	0.061		0.094
R		0.40			0.016	
V2	0°		8°	0°		8°



7226255/B

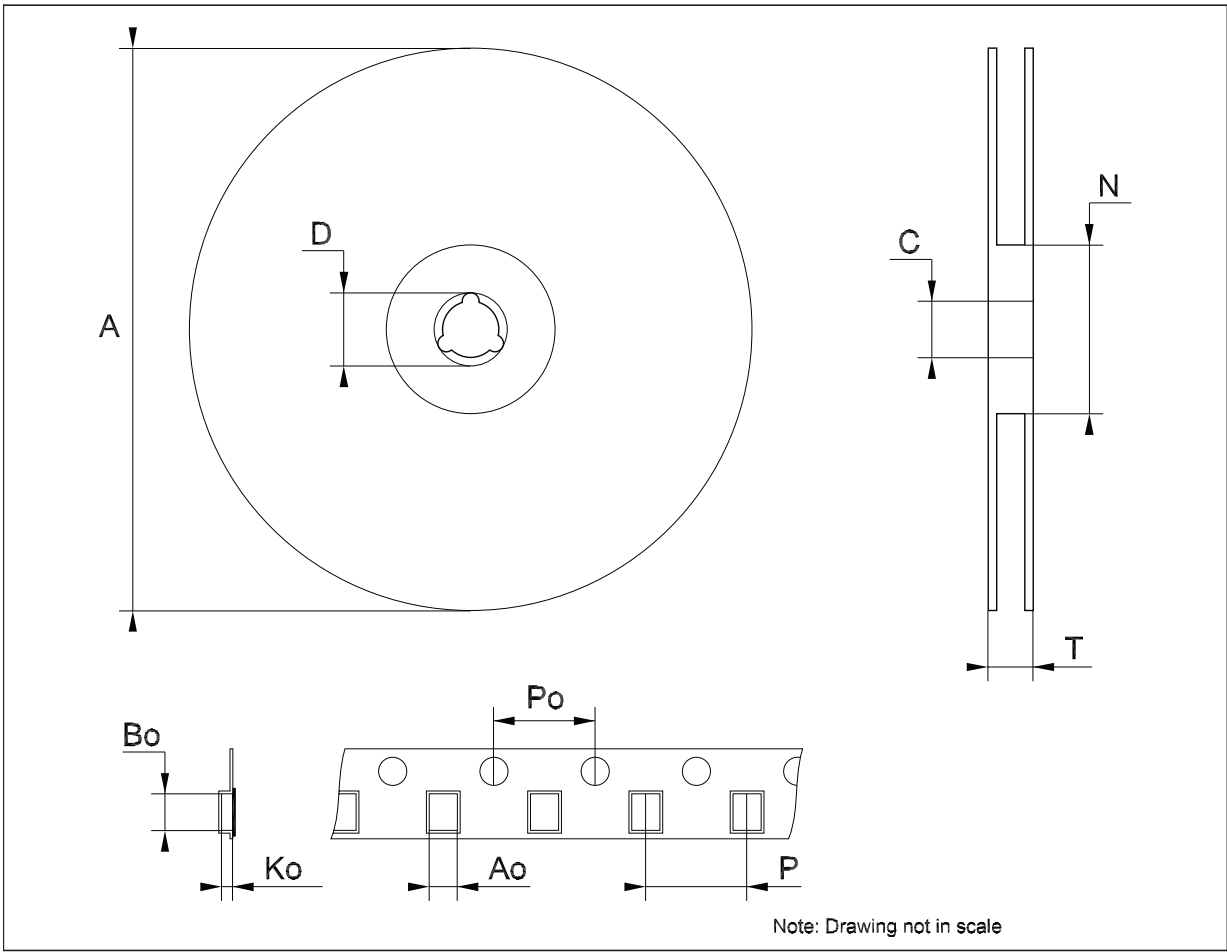
Tape & reel DPAK-PPAK mechanical data

Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			330			12.992
C	12.8	13.0	13.2	0.504	0.512	0.519
D	20.2			0.795		
N	60			2.362		
T			22.4			0.882
Ao	6.80	6.90	7.00	0.268	0.272	0.276
Bo	10.40	10.50	10.60	0.409	0.413	0.417
Ko	2.55	2.65	2.75	0.100	0.104	0.105
Po	3.9	4.0	4.1	0.153	0.157	0.161
P	7.9	8.0	8.1	0.311	0.315	0.319



Tape & reel D²PAK-P²PAK-D²PAK/A-P²PAK/A mechanical data

Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			180			7.086
C	12.8	13.0	13.2	0.504	0.512	0.519
D	20.2			0.795		
N	60			2.362		
T			14.4			0.567
Ao	10.50	10.6	10.70	0.413	0.417	0.421
Bo	15.70	15.80	15.90	0.618	0.622	0.626
Ko	4.80	4.90	5.00	0.189	0.193	0.197
Po	3.9	4.0	4.1	0.153	0.157	0.161
P	11.9	12.0	12.1	0.468	0.472	0.476



8 Revision history

Table 9. Document revision history

Date	Revision	Changes
17-Jun-2004	5	Add figures 20 and 21, PPAK, TO-220 and TO-220FP mechanical data updated.
19-Jul-2004	6	Remove Package TO-220FP4.
08-Nov-2004	7	Mistake Figure 7.
21-Mar-2005	8	Add V_O and V_{INH} on Table 2.
21-Oct-2005	9	Order Codes Has Been Updated.
17-Oct-2006	10	Add new package P ² PAK.
13-Nov-2006	11	Add row T_{SHDN} on tables of the electrical characteristics.
11-May-2007	12	Order codes updated.
15-Feb-2008	13	Added: Table 1 on page 1 .
28-Jul-2009	14	Modified: Table 1 on page 1 .
22-Sep-2010	15	Modified: Table 1 on page 1 .
27-Oct-2010	16	Updated: DPAK mechanical data on page 16 .

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