DELPHI SERIES



FEATURES

- High efficiency: 85.3% @ 1.2V/13A, 3.3V/8A
- Low profile: 0.33”
- Industry standard footprint and pinout
- Flexible current allocation on each output
- Low voltage output (O/P 1) starts up first
- Fixed frequency operation
- Input UVLO, Output OCP, OVP, OTP
- No minimum load required
- Basic insulation
- ISO 9001, TL 9000, ISO 14001, QS9000, OHSAS18001 certified manufacturing facility
- UL/cUL 60950 (US & Canada) Recognized, and TUV (EN60950) Certified
- CE mark meets 73/23/EEC and 93/68/EEC directives.

Delphi Series Q48DW, 45W Quarter Brick, Dual Output DC/DC Power Modules: 1.2V/13A and 3.3V/8A

The Delphi Series Q48DW Quarter Brick, 48V input, dual output, isolated DC/DC converters are the latest offering from a world leader in power system and technology and manufacturing — Delta Electronics, Inc. This product family provides dual positive regulated outputs with a flexible combination of output current and power up to 45W in a very cost effective industry standard footprint. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions. All models are fully protected from abnormal input/output voltage, current, and temperature conditions. The Delphi Series Q48DW converters meet all safety requirements with basic insulation.

OPTIONS

- Positive On/Off logic
- Short pin lengths
- Heatsink available for extended operation

APPLICATIONS

- Telecom/DataCom
- Wireless Networks
- Optical Network Equipment
- Server and Data Storage
- Industrial/Test Equipment

Delta Electronics, Inc.

DATASHEET

DS_ Q48DW3R312_07202006

TECHNICAL SPECIFICATIONS (T_A=25°C, airflow rate=200 LFM, V_{in}=48Vdc, nominal Vout unless otherwise noted.)

PARAMETER	NOTES and CONDITIONS		Q48DW3R312NRFA			
			Min.	Typ.	Max.	Units
ABSOLUTE MAXIMUM RATINGS						
Input Voltage						
Continuous					80	Vdc
Transient (100ms)	<100ms				100	Vdc
Operating Temperature	Refer to Figure 27 for measuring point		-40		118	°C
Storage Temperature			-55		125	°C
Input/Output Isolation Voltage	<1 minute		1500			Vdc
INPUT CHARACTERISTICS						
Operating Input Voltage			36	48	75	Vdc
Input Under-Voltage Lockout						
Turn-On Voltage Threshold			33	34	35	Vdc
Turn-Off Voltage Threshold			31	32	33	Vdc
Lockout Hysteresis Voltage			1	2	3	Vdc
Maximum Input Current					1.8	A
No-Load Input Current				40	60	mA
Off Converter Input Current				5	10	mA
Inrush Current(I^2t)				0.015		A^2s
Input Reflected-Ripple Current	P-P thru 12μH inductor, 5Hz to 20MHz			5	10	mA
Input Voltage Ripple Rejection	120Hz			66		dB
OUTPUT CHARACTERISTICS						
Output Voltage Set Point	Vin=48V, Io=Io.max, Tc=25℃	Vout 1	1.200	1.240	1.260	Vdc
		Vout 2	3.300	3.330	3.360	
Output Voltage Regulation						
Over Load	Io1=Io, min to Io, max, Io2=0A	Vout 1		±5	±15	mV
	Io2=Io, min to Io, max, Io1=0A	Vout 2				
Over Line	Vin=36V to 75V,Io1=Io2=full load	Vout 1		±3	±10	mV
		Vout 2				
Cross Regulation	Worse Case			±5	±15	mV
Over Temperature	Tc=-40℃ to 110℃			±15	±50	mV
Total Output Voltage Range	Over sample load, line and temperature			±30	±85	mV
Output Voltage Ripple and Noise	5Hz to 20MHz bandwidth					
Peak-to-Peak	Io1, Io2 Full Load, 1μF ceramic, 10μF tantalu	Vout 1		30	50	mV
		Vout 2		30	50	
RMS	Io1, Io2 Full Load, 1μF ceramic, 10μF tantalum	Vout 1		15	30	mV
		Vout 2		15	30	
Operating Output Current Range		Vout 1	0		13	A
		Vout 2	0		8	
Output DC Current-Limit Inception		Vout 1	14.5			A
		Vout 2	9.5			
DYNAMIC CHARACTERISTICS						
Output Voltage Current Transient	48V, 10μF Tan & 1μF Ceramic load cap, 0.1A/μs					
Positive Step Change in Output Current	Iout1and Iout2 from 50% Io, max to 75% Io, max	Vout 1		100		mV
		Vout 2		100		
Negative Step Change in Output Current	Iout2 and Iout1 from 75% Io, max to 50% Io, max	Vout 1		100		mV
		Vout 2		100		
Cross dynamic				100		mV
Settling Time (within 1% Vout nominal)				100		us
Turn-On Transient						
Delay Time, From On/Off Control				5		ms
Delay Time, From Input				5		ms
Start-up Time, From On/Off Control				10		ms
Start-up Time, From Input				10		ms
Maximum Output Capacitance	Full load; 5% overshoot of Vout at startup	Vout 1			10000	μF
		Vout 2			5000	
EFFICIENCY						
100% Load	Iout1, Iout2 full load, 48vdc Vin			85.3		%
60% Load	Iout1, Iout2 60% of full load, 48vdc Vin			86.5		%
ISOLATION CHARACTERISTICS						
Input to Output	<1 minute		1500			Vdc
Input to Case						
Output to Case						
Isolation Resistance			10			MΩ
Isolation Capacitance				2000		pF
FEATURE CHARACTERISTICS						
Switching Frequency				300		kHz
ON/OFF Control, (Logic Low-Module ON)						
Logic Low	Von/off at Ion/off=1.0mA		0		0.8	V
Logic High	Von/off at Ion/off=0.0 μA				18	V
ON/OFF Current	Ion/off at Von/off=0.0V				1	mA
Leakage Current	Logic High, Von/off=15V				1	mA
Output Voltage Trim Range	Just trim Vout1, Pout ≦ max rated power		-10		+10	%
Output Boltage Remoote Sense Range	No Remote Sense Function					
Output Over-Voltage Protection	Over full temp range; %of nominal Vout		120	135	150	%
GENERAL SPECIFICATIONS						
MTBF	Io=80% of Io, max; Ta=25°C			2.69		M hours
Weight				25		grams
Over-Temperature Shutdown	Refer to Figure 27 for measuring point			120		°C

ELECTRICAL CHARACTERISTICS CURVES

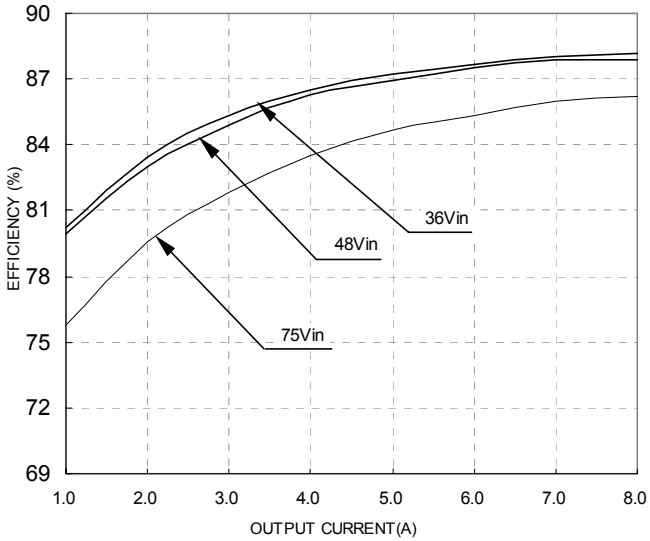


Figure 1: *Efficiency vs. load current Iout2 for minimum, nominal, and maximum input voltage at 25°C, for Iout1=6A.*

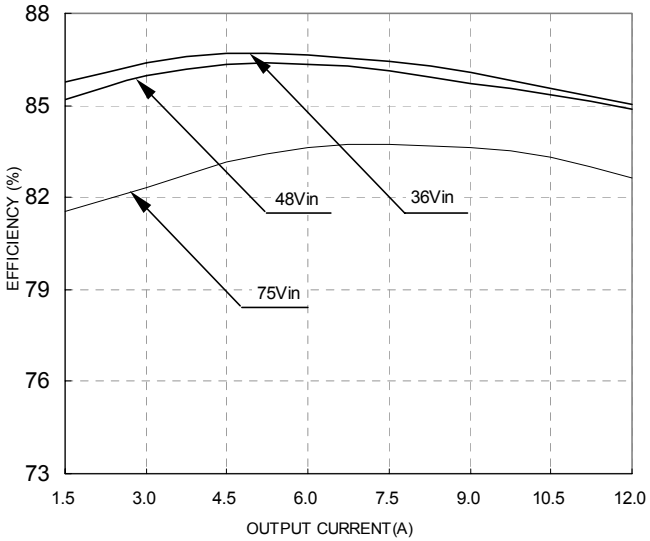


Figure 2: *Efficiency vs. load current Iout1 for minimum, nominal, and maximum input voltage at 25°C, for Iout2=4A*

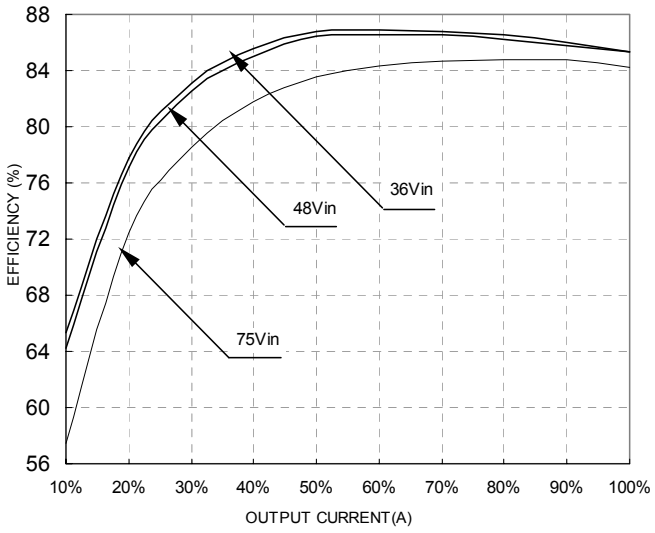


Figure 3: *Efficiency vs. load current Iout1 and Iout2 for minimum, nominal, and maximum input voltage at 25°C, for Iout1=Iout2*

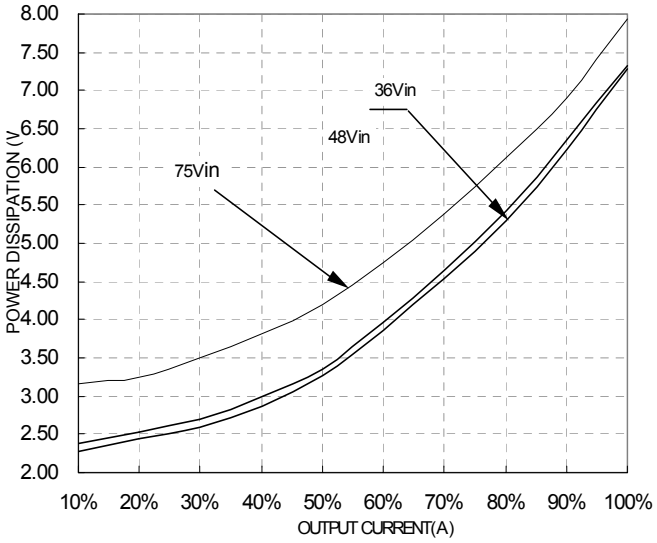


Figure 4: *Power dissipation vs. load current for minimum, nominal, and maximum input voltage at 25°C. for Iout1=Iout2*

ELECTRICAL CHARACTERISTICS CURVES

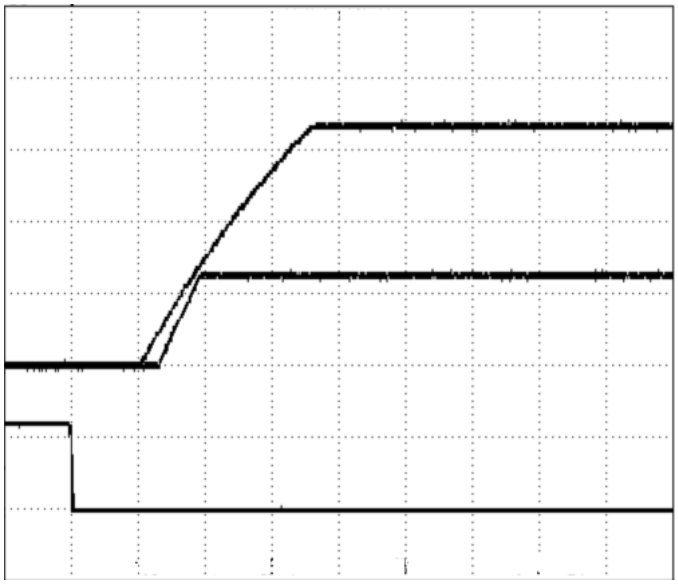


Figure 5: *Turn-on transient at zero load current (5ms/div). Vin=48V. Negative logic turn on. Top Trace: Vout; 1V/div; Bottom Trace: ON/OFF input: 5V/div*

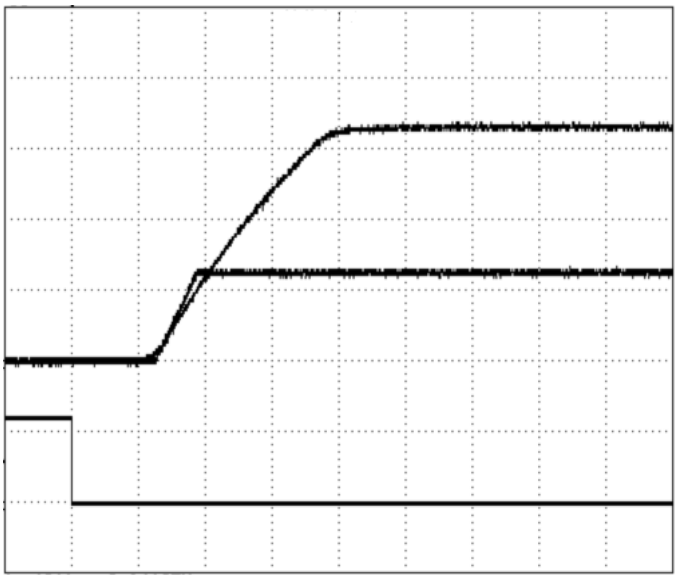


Figure 6: *Turn-on transient at full rated load current (resistive load) (5 ms/div). Vin=48V. Negative logic turn on. Top Trace: Vout; 1V/div; Bottom Trace: ON/OFF input: 5V/div*

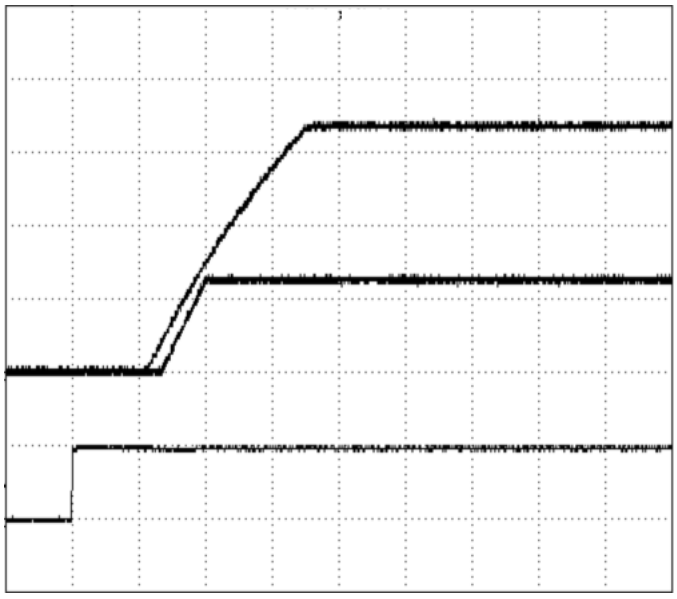


Figure 7: *Turn-on transient at zero load current (5ms/div). Vin=48V. Positive logic turns on. Top Trace: Vout; 1V/div; Bottom Trace: ON/OFF input: 5V/div*

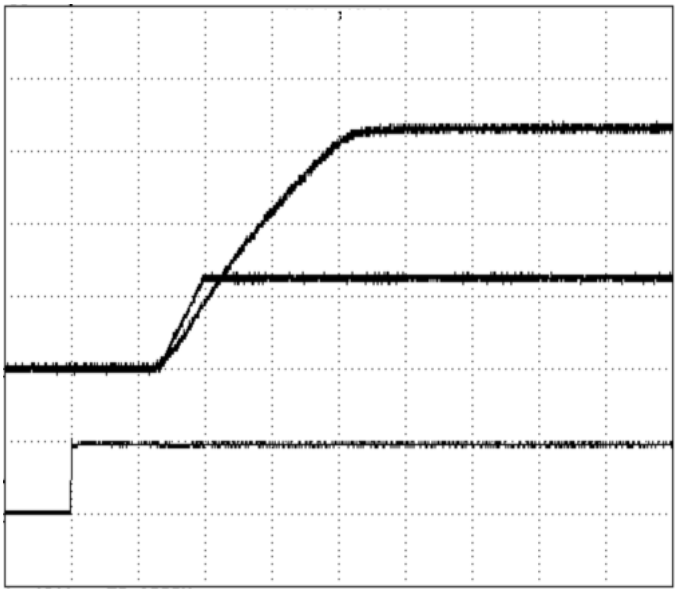


Figure 8: *Turn-on transient at full load current (5ms/div). Vin=48V. Positive logic turns on. Top Trace: Vout; 1V/div; Bottom Trace: ON/OFF input: 5V/div*

ELECTRICAL CHARACTERISTICS CURVES

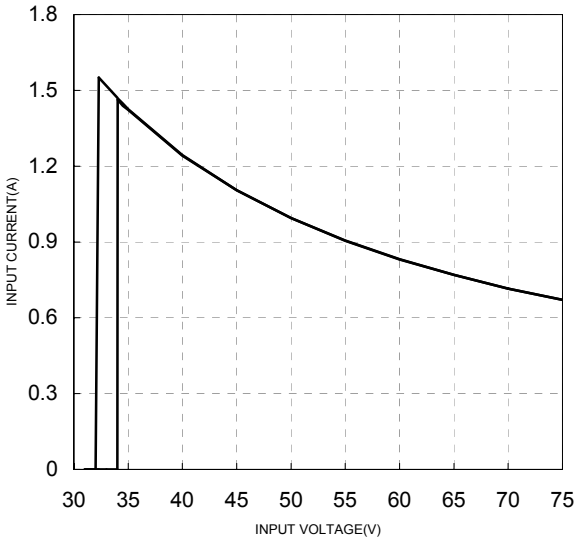


Figure 9: *Typical full load input characteristics at room temperature*

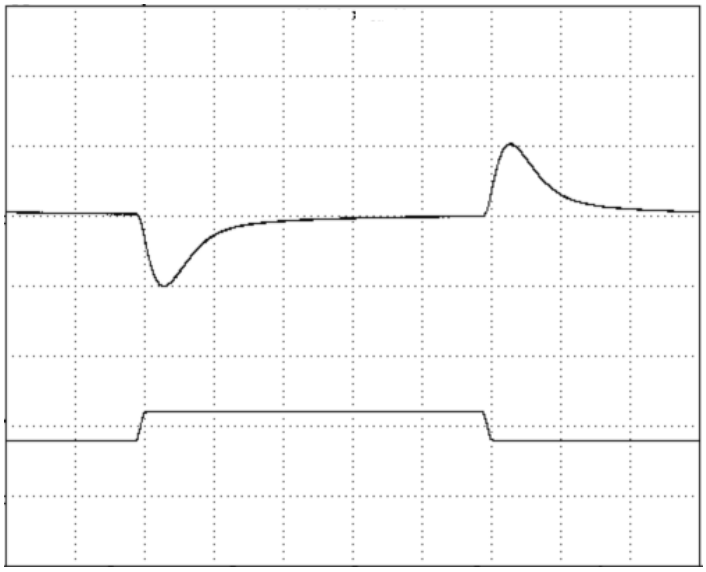


Figure 10: *Output voltage response to step-change in load current Iout2 (75%-50%-75% of Io, max; di/dt = 0.1A/µs) at Iout1=0A. Load cap: 10µF, tantalum capacitor and 1µF ceramic capacitor. Ch1=Vout2 (50mV/div), Ch2=Iout2 (5A/div), Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.*

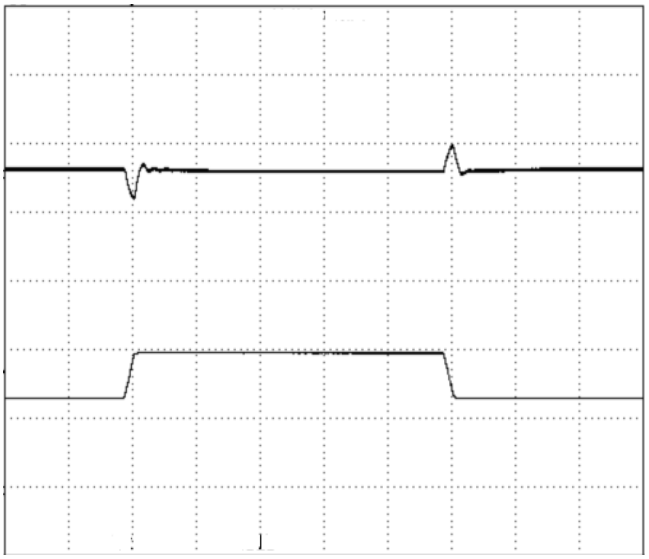


Figure 11: *Output voltage response to step-change in load current Iout1 (75%-50%-75% of Io, max; di/dt = 0.1A/µs) at Iout2=0. Load cap: 10µF, tantalum capacitor and 1µF ceramic capacitor. Ch1=Vout1 (50mV/div), Ch2=Iout1 (5A/div),Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.*

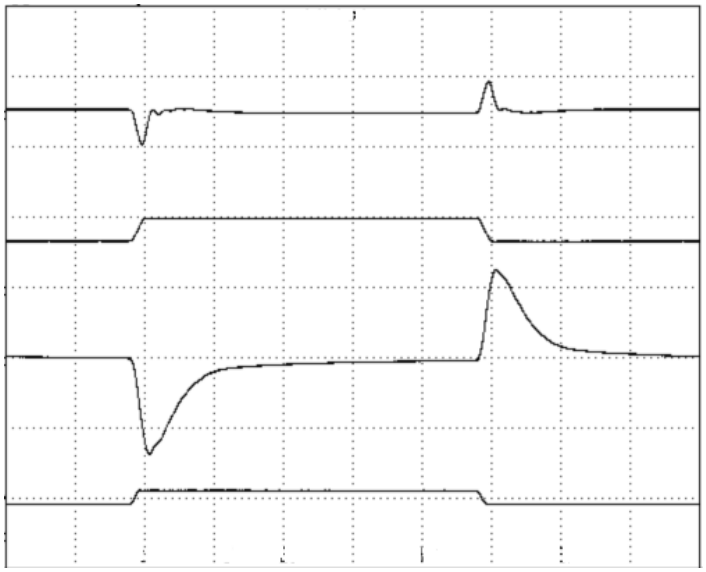


Figure 12: *Output voltage response to step-change in load current Iout2 and Iout1 (75%-50%-75% of Io, max; di/dt = 0.1A/µs). Load cap: 10µF, tantalum capacitor and 1µF ceramic capacitor. Ch1=Vout1 (50mV/div), Ch2=Iout1 (10A/div), Ch3=Vout2 (100mV/div), Ch4=Iout2 (10A/div) Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.*

ELECTRICAL CHARACTERISTICS CURVES



Figure 13: *Output voltage response to step-change in load current Iout2 (75%-50%-75% of Io, max; di/dt = 2.5A/µs) at Iout1=0. Load cap: 470µF, 35mΩ ESR solid electrolytic capacitor and 1µF ceramic capacitor. Ch1=Vout2 (50mV/div), Ch2=Iout2 (5A/div), Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.*

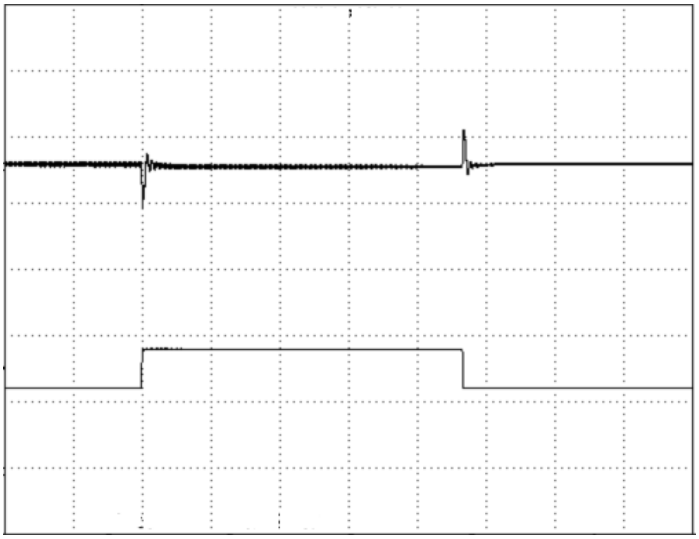


Figure 14: *Output voltage response to step-change in load current Iout1 (75%-50%-75% of Io, max; di/dt = 2.5A/µs) at Iout2=0A, Load cap: 470µF, 35mΩ ESR solid electrolytic capacitor and 1µF ceramic capacitor. Ch1=Vout1 (50mV/div), Ch2=Iout1 (5A/div), Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.*

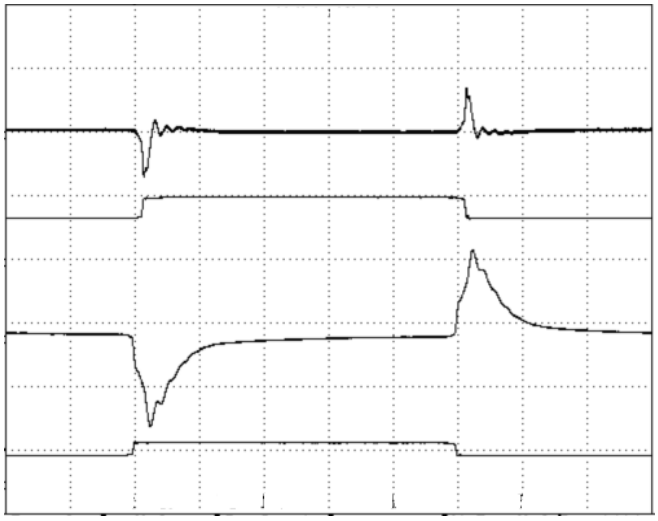


Figure 15: *Output voltage response to step-change in load current Iout2 and Iout1 (75%-50%-75% of Io, max; di/dt = 2.5A/µs). Load cap: 470µF, 35mΩ ESR solid electrolytic capacitor and 1µF ceramic capacitor. Ch1=Vout1 (50mV/div), Ch2=Iout1 (10A/div), Ch3=Vout2 (100mV/div), Ch4=Iout2 (10A/div) Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.*

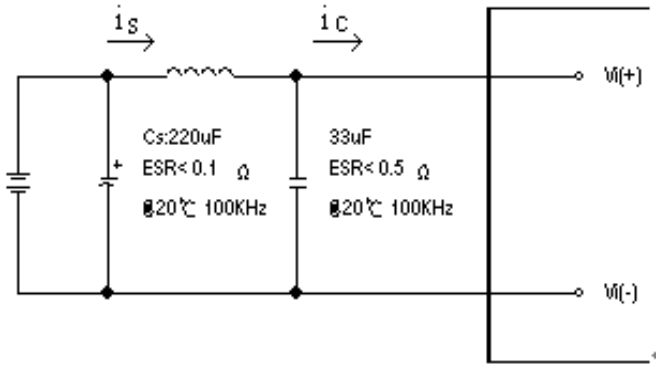


Figure 16: *Test set-up diagram showing measurement points for Input Terminal Ripple Current and Input Reflected Ripple Current. Note: Measured input reflected-ripple current with a simulated source Inductance (L_{TEST}) of 12 µH. Capacitor Cs offset possible battery impedance. Measure current as shown above.*

ELECTRICAL CHARACTERISTICS CURVES

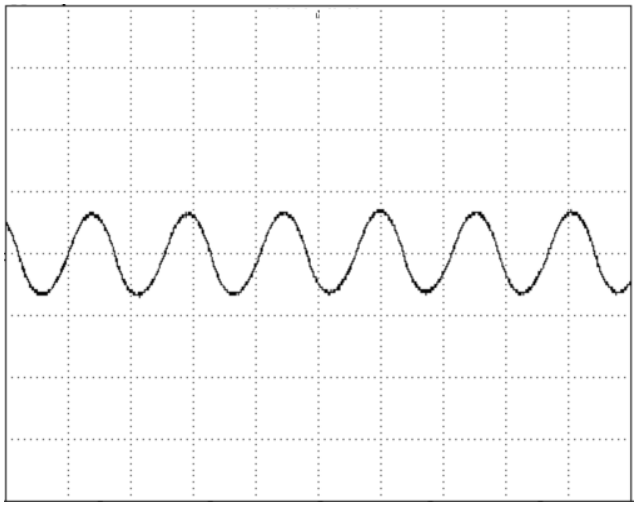


Figure 17: *Input Terminal Ripple Current-i_c, at full rated output current and nominal input voltage with 12µH source impedance and 33µF electrolytic capacitor (500 mA/div, 2us/div).*

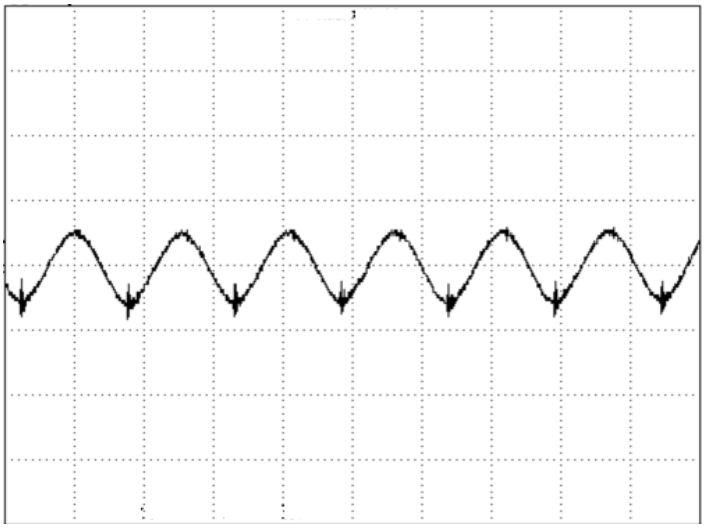


Figure 18: *Input reflected ripple current-i_s, through a 12µH source inductor at nominal input voltage and rated load current (20 mA/div, 2us/div).*

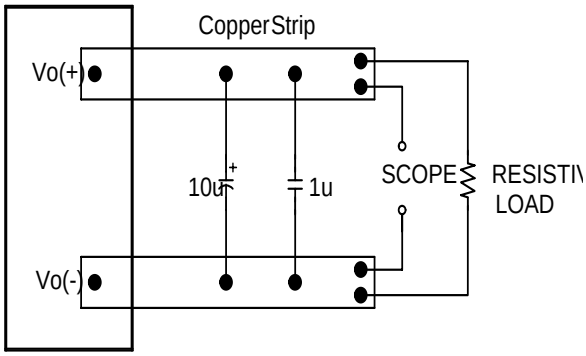


Figure 19: *Output voltage noise and ripple measurement test setup*

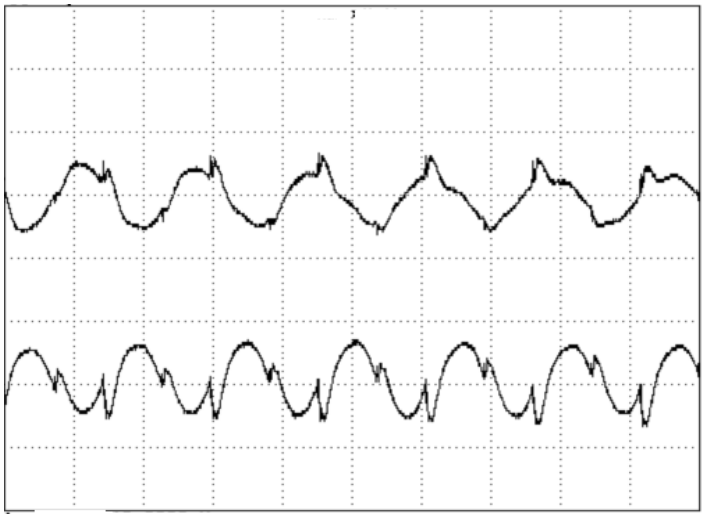


Figure 20: *Output voltage ripple at nominal input voltage and rated load current (Iout1=Iout2=Full). Top trace: Vout2 (20mV/div), Bottom trace:Vout1(20mV/div)*
Load capacitance: 1µF ceramic capacitor and 10µF tantalum capacitor. Bandwidth: 20 MHz. Scope measurements should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.

ELECTRICAL CHARACTERISTICS CURVES

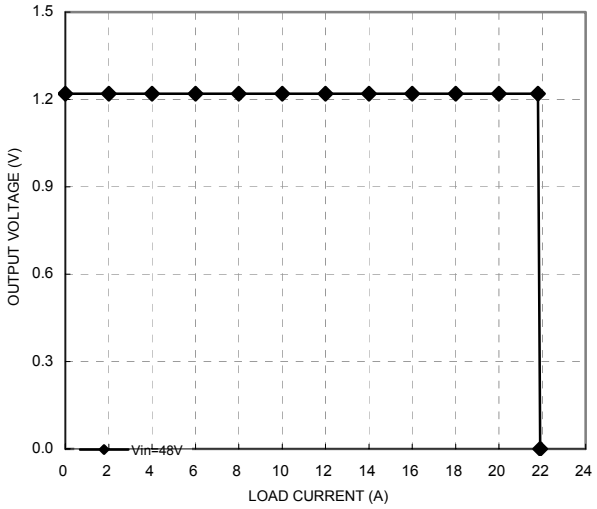


Figure 21: *Output voltage vs. load current Iout1 showing typical current limit curves and converter shutdown points.*

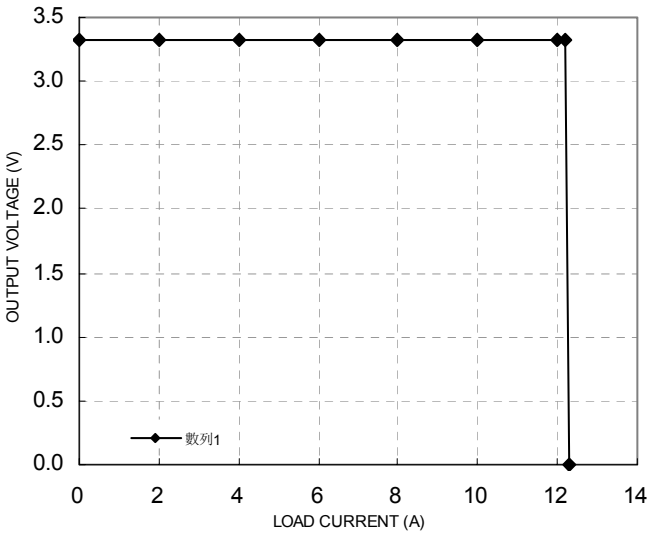


Figure 22: *Output voltage vs. load current Iout2 showing typical current limit curves and converter shutdown points.*

DESIGN CONSIDERATIONS

Input Source Impedance

The impedance of the input source connecting to the DC/DC power modules will interact with the modules and affect the stability. A low ac-impedance input source is recommended. If the source inductance is more than a few μH, we advise adding a 10 to 100 μF electrolytic capacitor (ESR < 0.7 Ω at 100 kHz) mounted close to the input of the module to improve the stability.

Layout and EMC Considerations

Delta's DC/DC power modules are designed to operate in a wide variety of systems and applications. For design assistance with EMC compliance and related PWB layout issues, please contact Delta's technical support team. An external input filter module is available for easier EMC compliance design.

Safety Considerations

The power module must be installed in compliance with the spacing and separation requirements of the end-user's safety agency standard, i.e., UL60950, CAN/CSA-C22.2 No. 60950-00 and EN60950:2000 and IEC60950-1999, if the system in which the power module is to be used must meet safety agency requirements.

When the input source is 60 Vdc or below, the power module meets SELV (safety extra-low voltage) requirements. If the input source is a hazardous voltage which is greater than 60 Vdc and less than or equal to 75 Vdc, for the module's output to meet SELV requirements, all of the following must be met:

- The input source must be insulated from any hazardous voltages, including the ac mains, with reinforced insulation.
- One Vi pin and one Vo pin are grounded, or all the input and output pins are kept floating.
- The input terminals of the module are not operator accessible.
- If the metal baseplate is grounded the output must be also grounded.
- A SELV reliability test is conducted on the system where the module is used to ensure that under a single fault, hazardous voltage does not appear at the module's output.

Do not ground one of the input pins without grounding one of the output pins. This connection may allow a non-SELV voltage to appear between the output pin and ground.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

This power module is not internally fused. To achieve optimum safety and system protection, an input line fuse is highly recommended. The safety agencies require a normal-blow fuse with 7A maximum rating to be installed in the ungrounded lead. A lower rated fuse can be used based on the maximum inrush transient energy and maximum input current.

Soldering and Cleaning Considerations

Post solder cleaning is usually the final board assembly process before the board or system undergoes electrical testing. Inadequate cleaning and/or drying may lower the reliability of a power module and severely affect the finished circuit board assembly test. Adequate cleaning and/or drying is especially important for un-encapsulated and/or open frame type power modules. For assistance on appropriate soldering and cleaning procedures, please contact Delta's technical support team.

FEATURES DESCRIPTIONS

Over-Current Protection

The modules include an internal output over-current protection circuit, which will endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the modules will automatically shut down (hiccup mode).

The modules will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

Over-Voltage Protection

The modules include an internal output over-voltage protection circuit, which monitors the voltage on the output terminals. If this voltage exceeds the over-voltage set point, the module will shut down and latch off.

The over-voltage latch of this module will be reset by either cycling the input power or by toggling the on/off signal for one second.

Over-Temperature Protection

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the over-temperature threshold the module will shut down.

The module will try to restart after shutdown. If the over-temperature condition still exists during restart, the module will shut down again. This restart trial will continue until the temperature is within specification.

Remote On/Off

The remote on/off feature on the module can be either negative or positive logic. Negative logic turns the module on during a logic low and off during a logic high. Positive logic turns the modules on during a logic high and off during a logic low.

Remote on/off can be controlled by an external switch between the on/off terminal and the Vi(-) terminal. The switch can be an open collector or open drain.

For negative logic if the remote on/off feature is not used, please short the on/off pin to Vi(-). For positive logic if the remote on/off feature is not used, please leave the on/off pin floating.

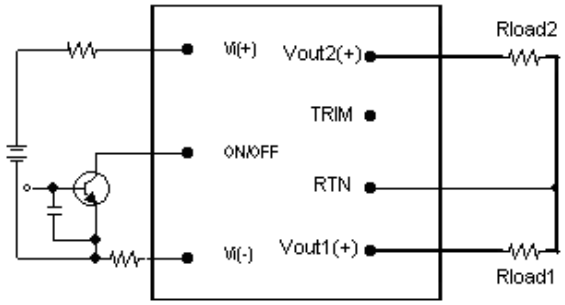


Figure 23: *Remote on/off implementation*

Output Voltage Adjustment (TRIM)

To increase or decrease the output voltage (Vout1) set point, the modules may be connected with an external resistor between the TRIM pin and either Vout1(+) or RTN. The Vout2 cannot be trimmed. The TRIM pin should be left open if this feature is not used.

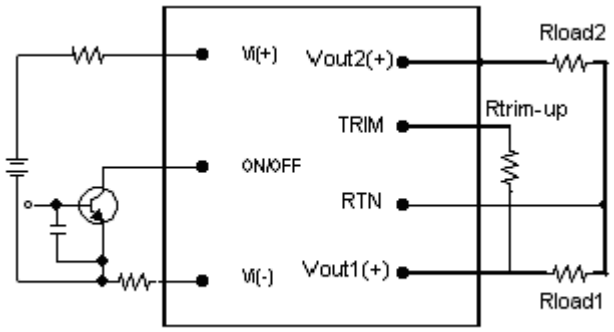


Figure 24: *Circuit configuration for trim-up (increase output voltage)*

If the external resistor is connected between the TRIM and Vout1(+) pin, the output voltage (Vout1) set point increases (Fig. 24).

FEATURES DESCRIPTIONS (CON.)

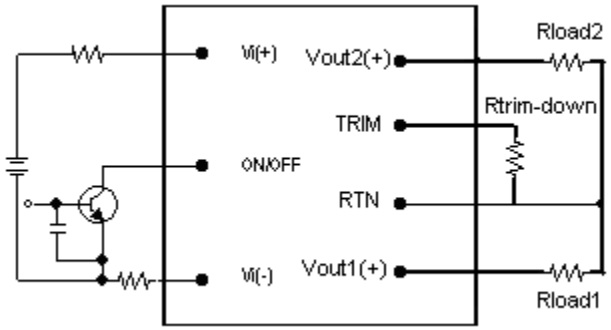


Figure 25: *Circuit configuration for trim-down (decrease output voltage)*

If the external resistor is connected between the TRIM and RTN, the output voltage (Vout1) set point decreases (Fig.25). Refer to the table below for the external resistor values.

Trim Resistor (Vout Increase)		Trim Resistor (Vout Decrease)	
Vout1	$R_{trim-up}$ [KΩ]	Vout1	$R_{trim-down}$ [KΩ]
1.2	Open	1.2	Open
1.32	14.4	1.08	18.7

When using the trim function and the output voltage of the module is increased, this will increase the power output of the module with the same output current. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

To trim up, connect trim resistor ($R_{trim-up}$) from trim pin to Vout1(+) (1.2v). The trim equation is

$R_{trim-up}$ = [Vo / (0.538Vo - 0.648)] - 6.81 Unit: K

Example: If the 1.2V output is trimmed up to +1.3V, connect the $R_{trim-up}$ from trim pin to Vout1(+). The value of the $R_{trim-up}$ is:

$R_{trim-up}$ = [1.3/ (0.538*1.3-0.648)]-6.81= 25.29-6.81= 18.5K

To trim down, connect trim resistor ($R_{trim-down}$) from trim pin to RTN (power ground). The trim equation is

$R_{trim-down}$ = [Vo / (0.362-0.296Vo)]-6.81 Unit: K

Example: If the 1.2V is trimmed down to 1.1V, connect the $R_{trim-down}$ from trim pin to RTN (Power ground). The value of $R_{trim-down}$ is:

$R_{trim-down}$ = [1.1/(0.362-0.296*1.1)]-6.81=30.22-6.81= 23.4K

THERMAL CONSIDERATIONS

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

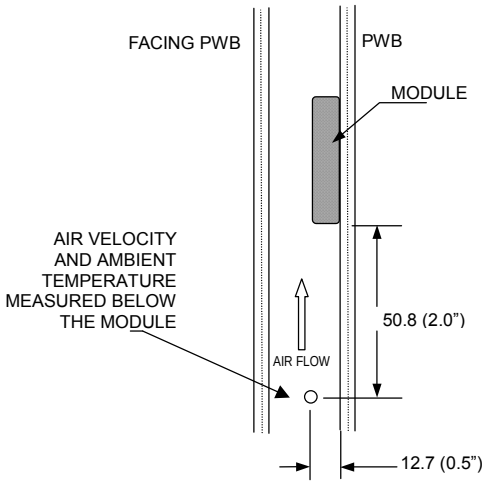
Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25'').

Thermal Derating

Heat can be removed by increasing airflow over the module. The module's hottest spot is less than 118°C. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inche

Figure 26: *Wind tunnel test setup*

THERMAL CURVES

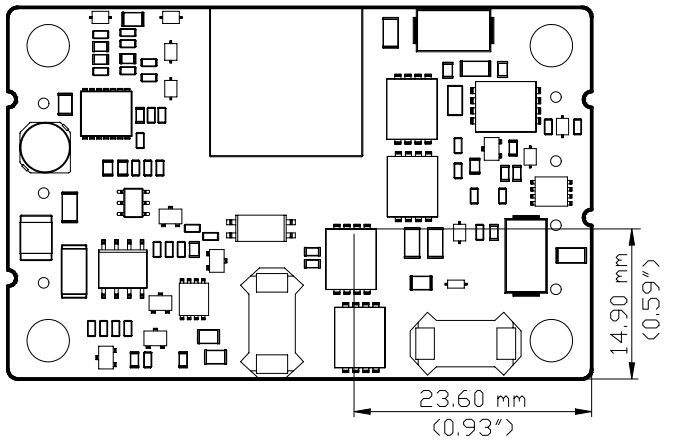


Figure 27: *Hot spot location*

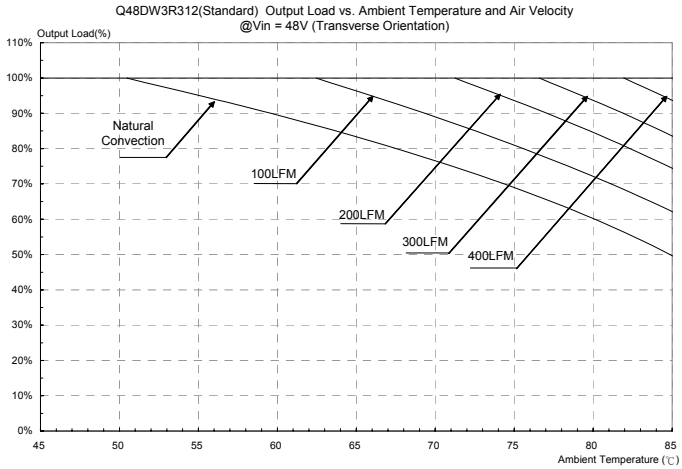
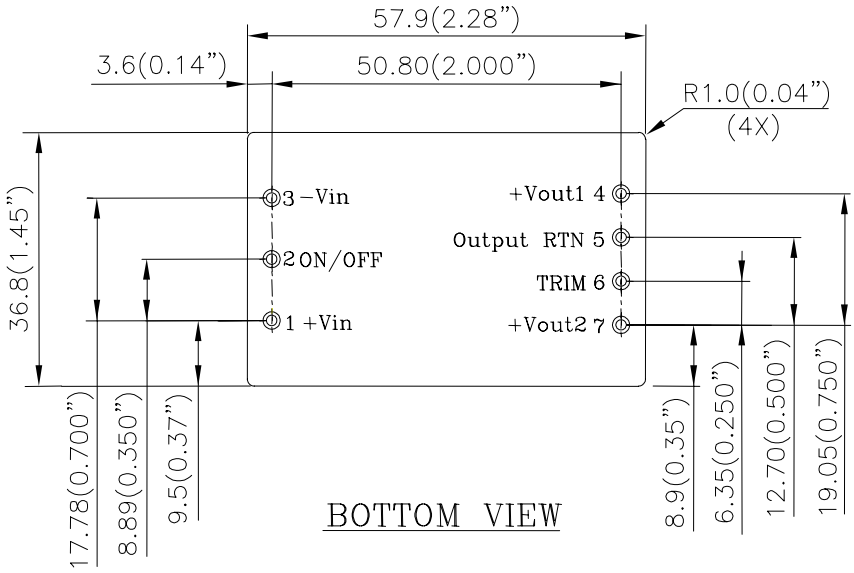
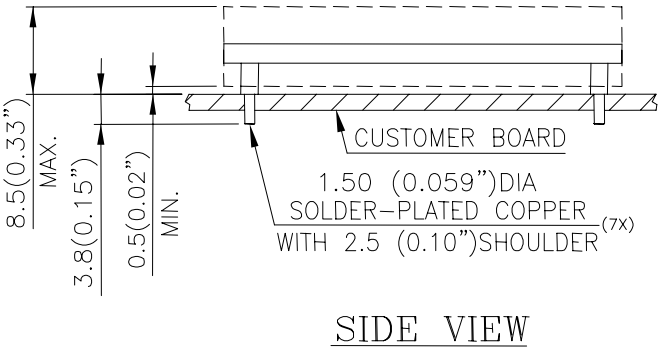


Figure 28: *Output load vs. ambient temperature and air velocity @ V_{in} = 48V (Transverse Orientation)*

MECHANICAL DRAWING





NOTES:
DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

Pin No.	Name	Function
1	+Vin	Positive input voltage
2	ON/OFF	Remote ON/OFF
3	-Vin	Negative input voltage
4	+Vout1	Positive output voltage1
5	Output RTN	Power Ground (Vout1 and Vout2)
6	TRIM	Output voltage trim
7	+Vout2	Positive output voltage2

Notes:

1 Pins 1-7 are 1.50mm (0.060") diameter
2 All pins are copper with Tin Plating (Lead Free).

PART NUMBERING SYSTEM

Q	48	D	W	3R3	12	N	R	F	A
Product Type	**Input Voltage**	**Number of Outputs**	**Product Series**	**Output Voltage 2**	**Output Voltage 1**	**ON/OFF Logic**	**Pin Length**		**Option Code**
Q - Quarter Brick	48V	D - Dual output	W – Product Feature	3R3 – 3.3V	10 - 1.0V 12 - 1.2V 15 - 1.5V 18 - 1.8V 25 - 2.5V	N - Negative P - Positive	R - 0.150” N - 0.145” K - 0.110”	F- RoHS 6/6 (Lead Free)	A - Standard functions

MODEL LIST

MODEL NAME	INPUT		OUTPUT *		EFF @ Full Load
Q48DW3R310NRFA	36V~75V	1.6A	1.0V/13A	3.3V/8A	85.5%
Q48DW3R312NRFA	36V~75V	1.7A	1.2V/13A	3.3V/8A	85.3%
Q48DW3R315NRFA	36V~75V	1.8A	1.5V/12A	3.3V/8A	85.3%
Q48DW3R318NRFA	36V~75V	1.8A	1.8V/10A	3.3V/8A	86.5%
Q48DW3R325NRFA	36V~75V	2.0A	2.5V/8A	3.3V/8A	87.0%

*** Note: Total output power should not exceed 50 watts and maximum output current for high output is 10A, for low output is 14A.**

CONTACT: www.delta.com.tw/dcdc

USA:
Telephone:
East Coast: (888) 335 8201
West Coast: (888) 335 8208
Fax: (978) 656 3964
Email: DCDC@delta-corp.com

Europe:
Phone: +41 31 998 53 11
Fax: +41 31 998 53 53
Email: DCDC@delta-es.com

Asia & the rest of world:
Telephone: +886 3 4526107 ext 6220
Fax: +886 3 4513485
Email: DCDC@delta.com.tw

WARRANTY

Delta offers a two (2) year limited warranty. Complete warranty information is listed on our web site or is available upon request from Delta.

Information furnished by Delta is believed to be accurate and reliable. However, no responsibility is assumed by Delta for its use, nor for any infringements of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Delta. Delta reserves the right to revise these specifications at any time, without notice.