Structure Silicone monolithic integrated circuit

Product name Audio I/O interface for DVD recorder

Product Mode **BD3822FS**

●Features

1. Low distortion (0.0015%) and low noises (3.2μVrms)
2. Contains an ALC circuit
3. I^2C BUS Control with the control voltage of 3.3V-5.0V
4. Use the Bi-CMOS process

●Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Applied voltage	VCC	10.0	V
Input voltage	V_{INO}	VCC+0.3～GND-0.3	V
Power Dissipation	Pd	950 *1	mW
Operating temperature	Topr	-40～+85 *2	°C
Storage temperature	Tastg	-55～+150	°C

*1 At Ta=25°C or higher, this value is decreaced to 7.6mW/°C.
When Rohm standard board is mounted. Thermal resistance θja = 131.6(°C/W).
Rohm standard board: size: 70×70×1.6 (mm^3)
material: FR4 glass-epoxy substrate (copper foil area: not more than 3%).

*2 As long as voltage stays within operating voltage range, certain circuit operation is guaranteed in the operating temperature range.
Allowable loss conditions are related to temperature, to which care must be taken.
In addition though the standard value of its electrical characteristics cannot be guaranteed under the conditions other than those specified, original functions are maintained.

●Operating Voltage Range

Parameter	Symbol	Min.	Typ.	Max.	Unit
Power supply voltage *3	VCC	7.0	-	9.5	V

Basic operation shall be available at Ta=25°C.

*3 As long as temperature components must be set in accordance with the operating voltage and temperature ranges before using this IC.
In addition, though the standard value of its electrical characteristics cannot be guaranteed under the conditions other than those specified, original functions are maintained.

●Function

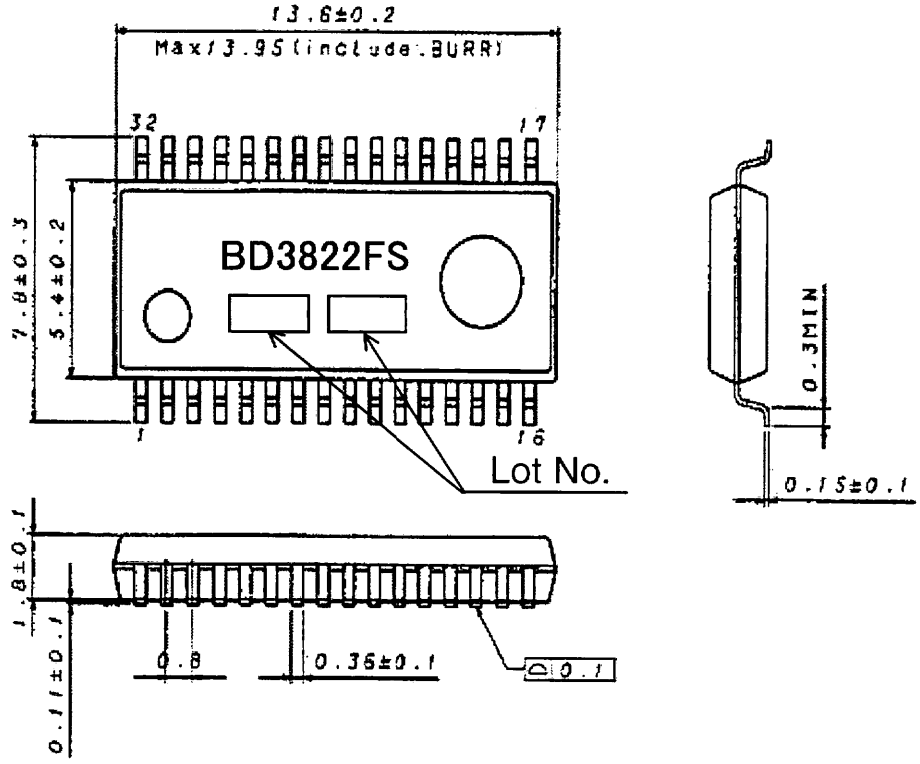
Function	Specifications
Input selector	Stereo 5 input, Tuner SAP
Tuner gain	8, 12dB
Gain Amp	0, 4.6, 4.8, 5.0, 5.2, 5.4, 5.6, 6dB
Power ON/OFF	Control can be done with I^2C BUS and external forced power ON/OFF terminal
Volume	0dB～-78dB & -∞dB, Soft switching
ALC	Suppression level -3dBV, -5dBV, -7dBV

●Electrical characteristics

Unless specified: Ta=25℃, VCC=9V, f=1kHz, V_{IN}=1Vrms, R_g=600Ω, R_L=10kΩ,Gain Amp=0dB,Volume=0dB, Input terminal=Front1,Output Terminal=OUT1

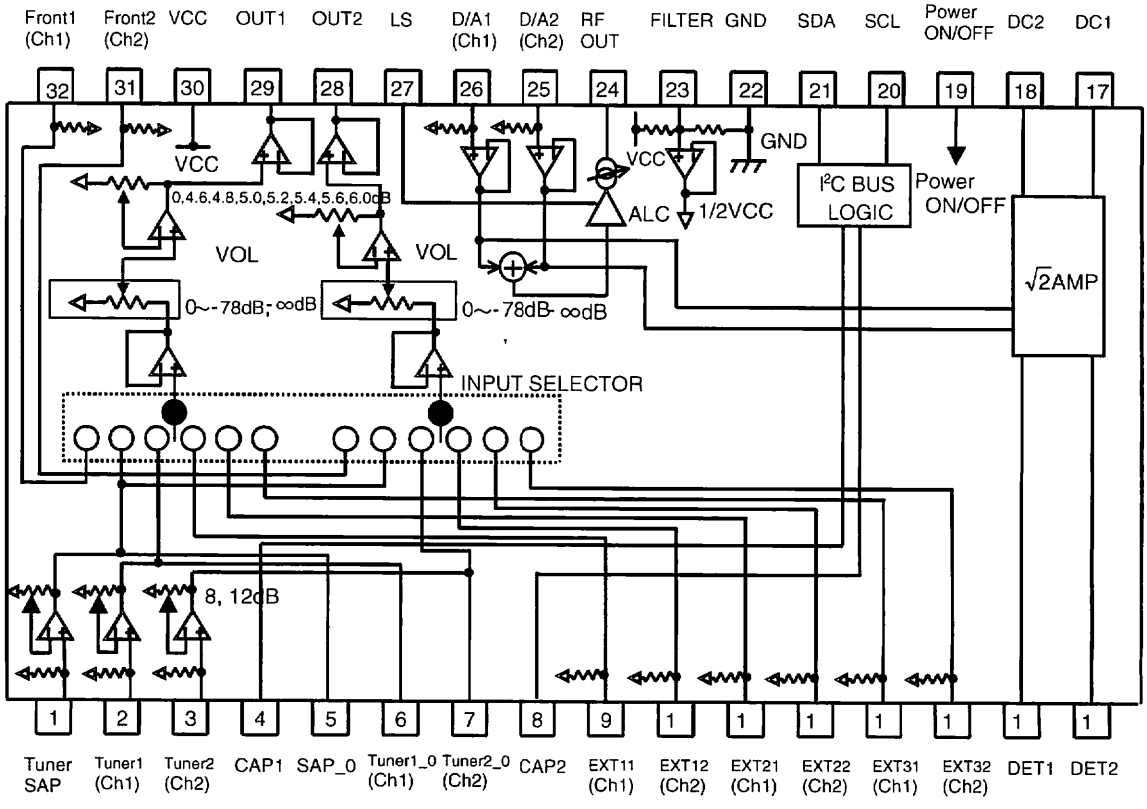
Parameter	Symbol	Limits			Unit	Conditions
		Min.	Typ.	Max.		
Circuit current upon no signal	IQ	-	7	30	mA	VIN=0Vrms
Standby current	IOFF	-	540	1000	μA	「Power OFF」 MODE
Voltage gain	GV	-1.5	0	1.5	dB	GV=20log(V_{OUT}/V_{IN})
Maximum output voltage	VOM	2.0	2.5	-	Vrms	VOM at THD(VOUT)=1% BW=400-30KHz
Channel balance	CB	-1.5	0	1.5	dB	CB = G_{V1}-G_{V2} GV1:ch1Gain GV2:ch2 Gain
Total harmonic distortion	THD	-	0.001 5	0.05	%	VIN=2Vrms,Volume=-12dB Gain Amp=5.6dB BW=400-30KHz
Output noise voltage	VNO	-	3.2	16	μVrms	Volume=-12dB Gain Amp=5.6dB Rg = 0Ω, BW=IHF-A
Residual noise voltage	VNOR	-	2	10	μVrms	Volume = -∞dB Rg = 0Ω, BW=IHF-A
Cross-talk between channels *	CTC	-	-110	-80	dB	Rg = 0Ω、 BW = IHF-A
Maximum attenuation 1ch	GV MIN1	-	-106	-85	dB	Volume = -∞dB, BW = IHF-A GV=20log(V_{OUT}/V_{IN})
Maximum attenuation 2ch	GV MIN2	-	-106	-85	dB	Volume = -∞dB GV=20log(V_{OUT}/V_{IN}) BW=IHF-A, Front2, OUT2

●Dimensional outline drawing



SSOP-A32 (Unit : mm)

●Block diagram



●Cautions on use

(1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
(2) Although we are confident in recommending the sample application circuits, carefully check their characteristics further when using them. When modifying externally attached component constants before use, determine them so that they have sufficient margins by taking into account variations in externally attached components and the Rohm LSI, not only for static characteristics but also including transient characteristics.
(3) Absolute maximum ratings
If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.
(4) GND potential
Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.
(5) Thermal design
Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.
(6) Shorts between pins and misinstallation
When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.
(7) Operation in strong magnetic fields
Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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