

32Bit High-Speed Up/Down Counter Board for PCI

CNT32-8M(PCI)



* Specifications, color and design of the products are subject to change without notice.

Features

Capable of receiving two-phase and single-phase signals.

Capable of receiving pulse signals of up to 10 MHz (The minimum discernible phase difference in two-phase signal input mode is 25 nsec.)

Capable of selecting the differential line receiver input or TTL level -compatible input mode for each channel.

Protective device attached to the input circuit, providing surge protection.

Capable of discontinuity detection in differential line receiver input mode.

One control signal input pin per channel.

Capable of count values sampling at a maximum sampling rate of 20 MHz.

Supporting PCI bus mastering, enabling high-speed data transfer between the board and the PC without intervention from the CPU.

Capable of generating an interrupt, issuing an external signal, or presetting/zero-clearing the count value when it matches an arbitrary predefined value.

On-board connectors for synchronization control to easily implement operations between two or more CNT32-8M(PCI) boards or operations in synchronization with a heterogeneous board.

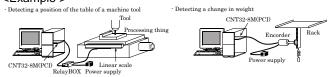
This product is a PCI-compliant interface board that inputs and counts pulse signals from an external device.

The board has eight channels of 32-bit up/down counters, allowing external devices such as a rotary encoder and a linear scale to be connected. Given below are examples of using the board for "detecting a position of the table of a machine tool" and "detecting a change in weight".

The pulse signal incoming interface is differential line receiver input or TTL level -compatible input that can receive pulse signals at high speed.

When run with the dedicated support software, the application for this board can transfer data between the board and the PC at high speed using PCI bus mastering.

<Example >



Specification

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< 1 / 2	Item	Specification	
Innut	ILCIII	Specification	
Input Coun	10.0		
		0 -11-	
	hannel count	8 channels	
Co	ount system	Up/down counting	
		(2-phase/Single-phase/Single-phase Input with Gate Control Attached)	
N 4	au aarmt		
	ax. count	FFFFFFFh(binary data, 32Bit)	
	put type	Differential line receiver input or TTL level input(Selectable by software)	
In	put signal	Phase-A/UP One x 8 channels	
		Phase-B/DOWN One x 8 channels	
 -		Phase-Z/CLR One x 8 channels	
	fferential line receiver	Element in use: Equivalent to AM26LS32(T.I)	
In	put section	Terminal resister: 150Ω(Can be disconnected switch.)	
		Receiver input sensitivity: ±200mV	
		In-phase input voltage range: ±7V Signal extension distance: 1200m(dependent on wiring	
		environment and input frequency) *1	
 	TL level input section	Element in use: Equivalent to 74ALS541NS(T.I)	
' '	Lievei iriput section	Signal extension distance: 1.5m(dependent on wiring	
		environment)	
R	esponse frequency	10MHz 50% duty	
Di	gital filter	0.1µsec - 1.6384msec or not used (can be independently set	
		for each channel.)	
	mer	1msec - 6553msec 1msec unit	
	ounter start trigger	Software/External start input/Sampling start trigger	
	ounter stop trigger	Software/External start input/Sampling stop trigger	
Samp	oling		
Sa	ampling start trigger	Software/External start input/Sync control connectors/Count match	
Sa	ampling stop trigger	Software/External stop input/Specification number/Bus master tranfer error/Sync control connectors/Count match	
Se	ampling clock	Sampling timer/External clock input/Sync control connectors	
	ampling timer	50nsec - 107sec	
E.	ternal sampling start	TTL level(Select Rise or Fall)	
siç	gnal	,	
	kternal sampling stop gnal	TTL level(Select Rise or Fall)	
	rternal sampling clock	TTL level(Fall)	
	esponse frequency	10MHz 50% duty	
Contr		· · · · · · · · · · · · · · · · · · ·	
	ontrol input signal type	TTL level	
	ontrol input channel	One x 8 channels	
	ontrol input signal	- Preset(Select Rise or Fall)	
	ontroi input signal	- Zero-clear(Select Rise or Fall) - Counter start/stop(Select Rise or Fall) - General-purpose input(positive logic) Software-selected from among the above four options	
Re	esponse time	100nsec (Max.)	
	rupt event	Count match(16 points), Counter error(2 points), Sampling factor(6 points), Sync control connectors error(2 points), Carry/Borrow(1 points), Timer(1 points)	

*1 The frequency response at an extension of 50 m is about 10 MHz (depending on the wiring environment).
The frequency response at an extension of 100 m is about 5 MHz (depending on the

The frequency response at an extension of 100 m is about 5 MHz (depending on the wiring environment).

The frequency response at an extension of 150 m is about 1.5 MHz (depending on the

The frequency response at an extension of 150 m is about 1.5 MHz (depending on the wiring environment).

The frequency response at an extension of 300 m is about 1 MHz (depending on the

wiring environment).

The frequency response at an extension of 600 m is about 500 KHz (depending on the wiring environment).

The frequency response at an extension of 1200 m is about 80 KHz (depending on the wiring environment)

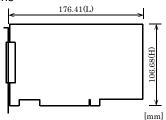


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Item	Specification			
Output				
Control				
Control output channel	One x 8 channels			
Control output signal	- Count match 0 output(one-shot pulse output) - Count match 1 output(one-shot pulse output) - Digital filter error output(one-shot pulse output) - Abnormal input error output(one-shot pulse output) - Disconnection alarm error output(one-shot pulse output) - General-purpose output(Level output) Software-selected from among the above five options (Positive/negative logic is selected with the on-board switch.)			
One shot output signal amplitude	Selected between 10μsec, 100μsec, 1msec, 10msec and 100 msec (Can be set for each channel, within precision + 1μsec)			
Element in use	Non-Isolated Open Collector Output: Equivalent to 74LS07NS(T.I)			
Output rating	30V 40mA			
Response speed	5μsec (Max.)			
TP				
Test pulse output signal	One line receiver output for each of phases-A and B (For TTL output, use the positive line receiver output.)			
Element in use	Equivalent to AM26LS31(T.I)			
Frequency	100kHz			
Bus master				
DMA channel	1 channel			
Transfer bus width	32-Bit width			
Transfer data length	8 PCI Words length(Max.)			
Transfer rate	80MB/sec(Max.133MB/sec)			
FIFO	1K-DWord			
Scatter/Gather function	64MB			
Interrupt event	Bus master event(7 points)			
Synchronization				
Control output signal	Select the output signal by software when setting the synchronization slave mode.			
Control input signal	Select the synchronization event by software when setting the synchronization slave mode.			
Connectable number of device	16 boards including the master board			
Connector used	PS-10PE-D4T1-B1 (JAE) or equivalent x 2			
Common	<u> </u>			
I/O address	Occupies 2 locations, any 32-bytets and 64-byte boundary			
Power consumption	5VDC, 1A (Max.)			
Operating condition	0 - 50°C, 10 - 90%RH (No condensation)			
PCI bus specification	32bit, 33MHz, Universal key shapes supported *2			
Dimension (mm)	176.41(L) x 106.68(H)			
Weight	120g			
Certification	RoHS,CE,VCCI			
	supply at +5V from an expansion slot (it does not work or			

machine with a +3.3V power supply alone).

Board Dimensions



The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.

Difference in bus mastering transfer rate by system configuration

When it inserts in the expansion slot of a personal computer

	Limited	Unlimited
430TX/Pentium233MHz	20	13.4
440BX/PentiumII450MHz	20	13.4
i820/PentiumIII800MHz	20	13.4
i815E/PentiumIII800MHz	20	13.4
		FR 41 1 3

"Limited" indicates that the number of transfers is specified; "Unlimited" specifies that it is not

When CONTEC's extension unit FA-PAC (PCI) series is used

	Limited	Unlimited
430TX/Pentium233MHz	20	10
440BX/PentiumII450MHz	20	10
i820/PentiumIII800MHz	20	10
i815E/PentiumIII800MHz	20	10

These values may not be satisfied depending on the system configuration including other boards and applications.

Support Software

Driver Library API-PAC(W32) (Bundled)

Windows version of counter input driver API-CNT(WDM) / API-CNT(98/PC)

[Stored on the bundled Disk driver library API-PAC(W32)] The API-CNT(WDM) / API-CNT(98/PC) is the Windows version driver library software that provides products in the form of Win32 API functions (DLL). Various sample programs such as Visual Basic and Visual C++, etc and diagnostic program useful for checking operation is provided.

You can download the updated version from the CONTEC's Web site (http://www.contec.com/apipac/). For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

Cable & Connector

Cable (Option)

Shielded cable with double-ended connector for 96-pin

half-pitch connector (Molded type) : PCB96PS-0.5P (0.5m)

: PCB96PS-1.5P (1.5m)

Flat Cable with 96-Pin Half-Pitch Connectors

at Both Ends : PCB96P-1.5 (1.5m)

Shielded cables with single-ended connector for 96-pin

: PCA96PS-0.5P (0.5m) half-pitch connector (Molded type)

: PCA96PS-1.5P (1.5m)

Flat Cable with One 96-Pin Half-Pitch Connector

: PCA96P-1.5 (1.5m)

Connector (Option)

Half Pitch 96-Pin Female Connector Set(5Pieces)

: CN5-H96F

Accessories

Accessories (Option)

Screw Terminal Unit (M3 x 96P) :EPD-96A *1*2 Screw Terminal Unit (M3.5 x 96P) :EPD-96 *1 Terminal Unit for Cables (M2.5 x 96P) :DTP-64(PC)

Packing List

Board [CNT32-8M(PCI)] ...1 First step guide ...1

Disk *1 [API-PAC(W32)] ...1

Synchronization control cable(10cm)...1

Serial number label...1

Product Registration Card & Warranty Certificate...1

The Disk contains the driver software and User's Guide

specified.
These values may not be satisfied depending on the system configuration including other boards and applications.

Limited" indicates that the number of transfers is specified; "Unlimited" specifies that it is not

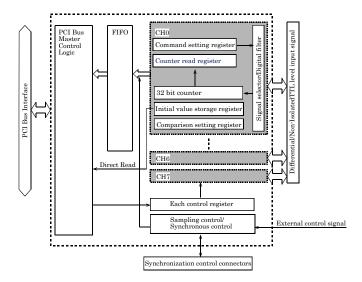
^{*1}A PCB96PS or PCB96PS optional cable are required separately.

^{*2&}quot;Spring-up" type terminal is used to prevent terminal screws from falling off.

^{*}Check the CONTEC's Web site for more information on these options.



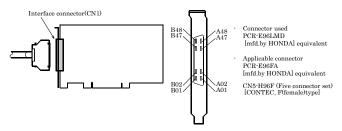
Block Diagram



Using the On-board Connectors

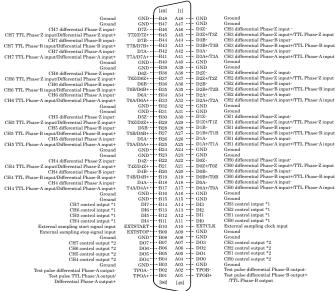
Connecting a Device to a Connector

To connect an external device to this board, plug the cable from the device into the interface connector (CN1) shown below.



Connector Pin Assignment

This interface board is connected to an external device through the on-board connector.



The numbers in square brackets [] are pin numbers designated by HONDATSUSHIN KOGYO CO., LTD

*1 The control inputs can serve as the general-purpose, counter start/stop, preset, and zero clear inputs.
*2 The control outputs can serve as the general-purpose output, count match, abnormal input error, digital filter error, and discontinuity alarm error outputs.

External Device Connection 1 -differential line receiver input-

Connecting the differential line receiver input

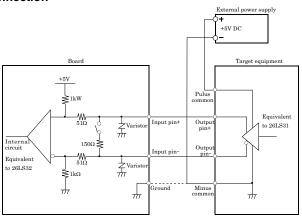
Use the differential line receiver input to connect the board to the line receiver output circuit of a rotary encoder or linear scale. The maximum input frequency is 10 MHz.

For use in two-phase input mode, connect both of the phase-A and phase-B inputs. For use in single-phase input mode, connect either of them. If phase-Z is not used, the input need not be connected.

For differential line receiver input mode, you can select whether to insert the terminal resister.

Detailed description of differential line receiver input circuit

Differential line receiver input circuit and its sample connection



Input signal

 $t_{PWH}: \mbox{ High-level count input pulse width 50nsec (Min.)} \\ t_{PWL}: \mbox{ Low-level count input pulse width 50nsec (Min.)}$

⚠ CAUTION

In the input pin+, TTL level input circuit is parallel-connected.

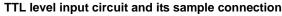
External Device Connection 2 - TTL level input -

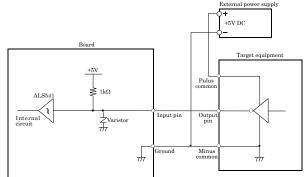
Connecting the TTL level input

Use the TTL level -compatible input to connect the board to the TTL level -compatible output circuit of a rotary encoder or linear scale. The maximum input frequency is 10 MHz.

For use in two-phase input mode, connect both of the phase-A and phase-B inputs. For use in single-phase input mode, connect either of them. If phase-Z is not used, the input need not be connected.

Detailed description of TTL level input circuit







Input signal tpwL tpwH

 $t_{PWH}: \mbox{ High-level count input pulse width 50nsec (Min.)} \\ t_{PWL}: \mbox{ Low-level count input pulse width 50nsec (Min.)}$

⚠ CAUTION

The connection cable length should be within 1.5 m. To prevent noise from causing a malfunction, arrange the connection cable as away from any other signal conductor or noise source as possible.

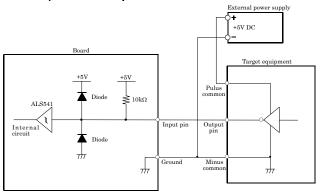
In the input pin+, TTL level input circuit is parallel-connected.

Connecting the control signal input/output

Connection of a control input

For control signal input, the board has one pin per channel to be used to selectively start/stop or preset the counter for the channel and one pin per channel to be used to start or stop the sampling clock.

Control input circuit and its sample connection Control input circuit(DI0 - DI7, EXTCLK, EXTSTART, EXTSTOP) and its sample connection



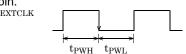
⚠ CAUTION

The connection cable length should be within 1.5 m. To prevent noise from causing a malfunction, arrange the connection cable as away from any other signal conductor or noise source as possible.

External sampling clock signal (EXTCLK)

This pin feeds the external pacer clock signal. The maximum frequency is 10 MHz.

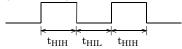
When the sampling clock input has been set to the external clock input, sampling is performed at the falling edge of the signal at this pin.



tpWH: High-level clock pulse width 50nsec (Min.) tpWL: Low-level clock pulse width 50nsec (Min.)

Other control input signals (DI0 - DI7, EXTSTART, EXTSTOP)

These signals are TTL level compatible and the trigger edge is software-programmable at either the rising or falling edge. High- and low-level hold times of at least 50 nsec are required to detect an edge of the signal.

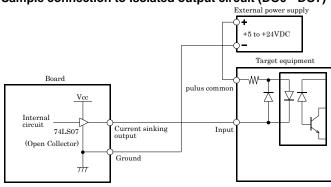


 $\begin{array}{ll} t_{HIH}: & \mbox{High-level hold time } 50 \mbox{nsec (Min.)} \\ t_{HIL}: & \mbox{Low-level hold time } 50 \mbox{nsec (Min.)} \end{array}$

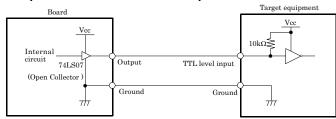
Connection of a control output

The control output of the board provides the general-purpose output signal (level output) and the one-shot pulse signals that indicate hardware events such as a count match. For the signal output, positive or negative logic can be selected with SW2

Control output circuit and its sample connection Sample connection to Isolated output circuit (D00 - D07)



Sample connection to TTL level input circuit



⚠ CAUTION

The output of this board has no surge voltage protector. To drive an inductive load such as a relay or lamp using this board, apply surge voltage protection to the load side. For surge voltage protection, see "Surge Voltage Countermeasures" in the this manual.