Z~Communications, Inc.
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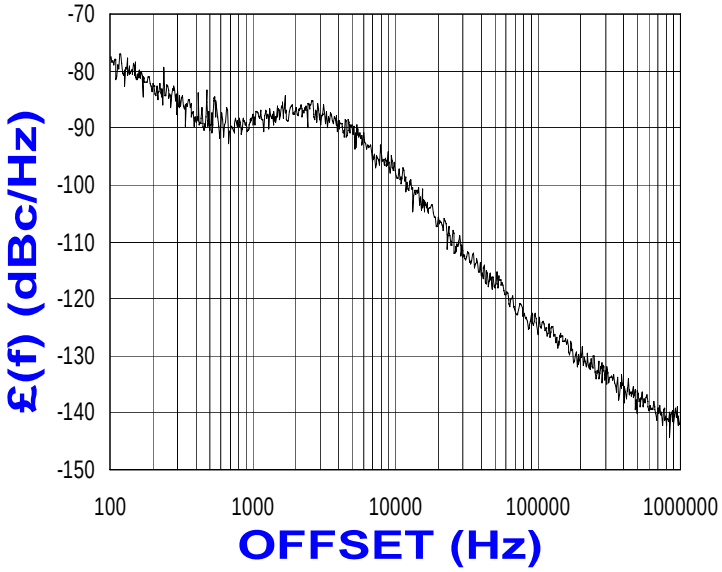
PSA3240C-LF

PHASE LOCKED LOOP

Rev A1



PHASE NOISE (1 Hz BW, typical)



FEATURES

- Frequency Range: 3175 - 3311 MHz
- Step Size: 250 KHz
- PLL-24H - Style Package

APPLICATIONS

- Digital Radio Equipment
- Fixed Wireless Access
- Satellite Communication Systems

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	3175 - 3311	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-98	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	0±3	dBm
Load Impedance	50	Ω
Step Size	250	kHz
Charge Pump Output Current	5000	µA
Switching Speed (typ., adjacent channel)	2	mSec
Startup Lock Time (typ.)	2	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	PLL-24H	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	35	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-202 : PLL Functional Description

NOTES:

Reference Oscillator Signal: 5 MHz<f_{osc}<100 MHz

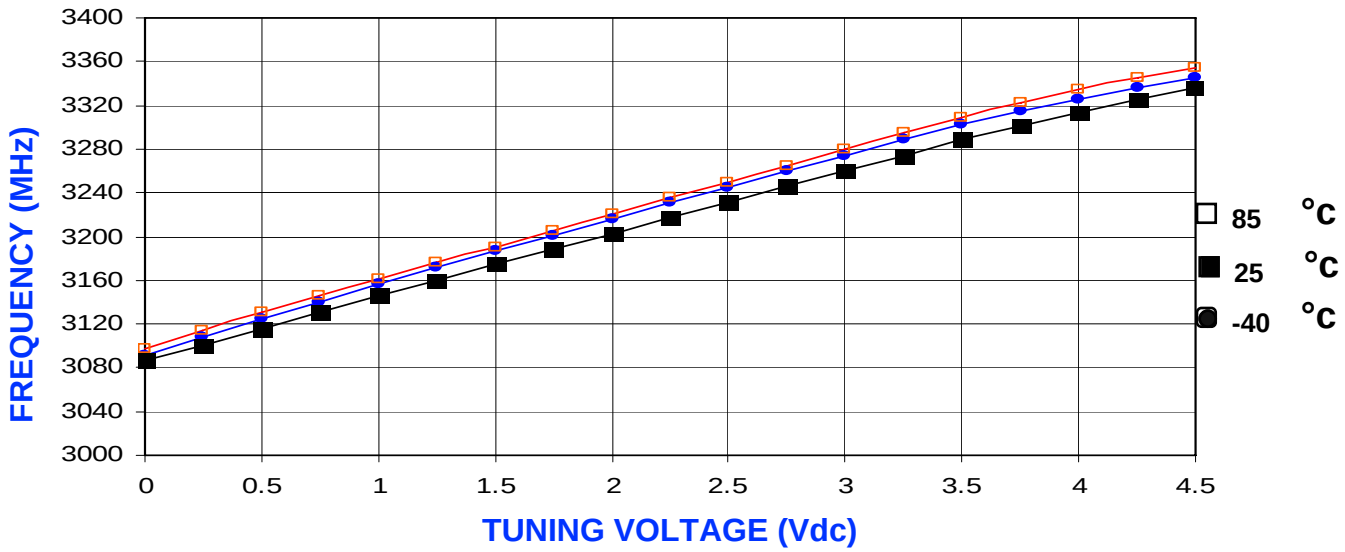
Prescaler: 32

Frequency Synthesizer: Analog Devices - ADF4106

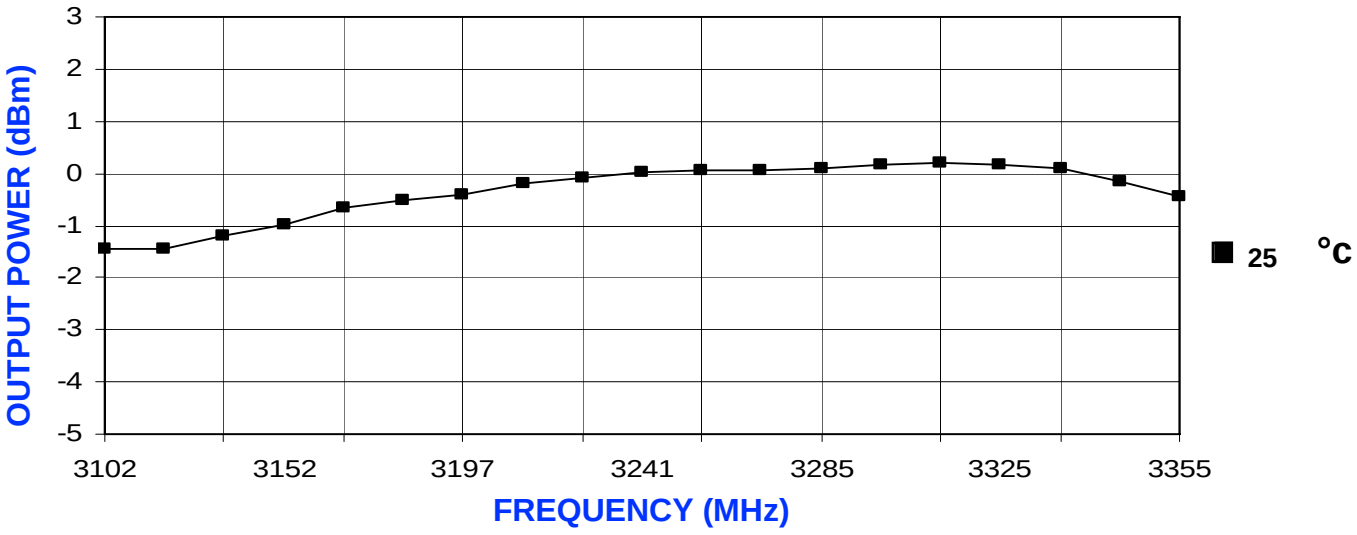
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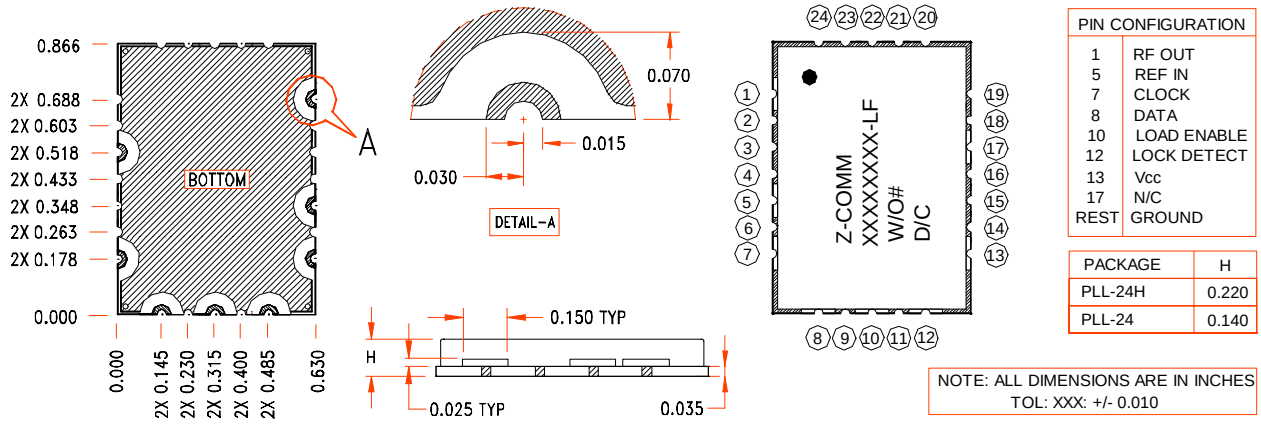
VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS



PIN CONFIGURATION	
1	RF OUT
5	REF IN
7	CLOCK
8	DATA
10	LOAD ENABLE
12	LOCK DETECT
13	Vcc
17	N/C
REST	GROUND

PACKAGE	H
PLL-24H	0.220
PLL-24	0.140

NOTE: ALL DIMENSIONS ARE IN INCHES
TOL: XXX: +/- 0.010

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