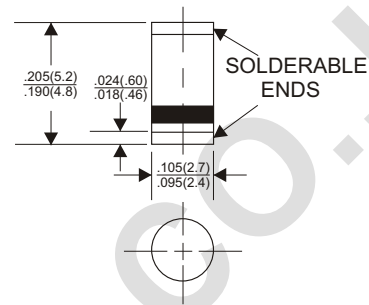


Fast recovery type

Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-O Utilizing Flame Retardant Epoxy Molding Compound.
- For surface mounted applications.
- Exceeds environmental standards of MIL-S-19500 / 228
- Low leakage current

MELF/LL41



Mechanical data

Case : Molded plastic, SM-1 (MELF)

Terminals : Solder plated, solderable per MIL-STD-750, Method 2026

Polarity : Indicated by cathode band

Mounting Position : Any

Weight : 0.015 gram

Dimensions in inches and (millimeters)

MAXIMUM RATINGS (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.2	I_0			1.0	A
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC methode)	I_{FSM}			30	A
Reverse current	$V_R = V_{RRM} \quad T_A = 25^{\circ}\text{C}$	I_R			5.0	μA
	$V_R = V_{RRM} \quad T_A = 100^{\circ}\text{C}$				100	μA
Thermal resistance	Junction to ambient	R_{JA}		75		$^{\circ}\text{C} / \text{W}$
Diode junction capacitance	$f=1\text{MHz}$ and applied 4vDC reverse voltage	C_J		15		pF
Storage temperature		T_{STG}	-55		+175	$^{\circ}\text{C}$

SYMBOLS	MARKING CODE	V_{RRM}^{*1} (V)	V_{RMS}^{*2} (V)	V_R^{*3} (V)	V_F^{*4} (V)	T_{RR}^{*5} (nS)	Operating temperature (°C)
DLF101	-	50	35	50	1.3	150	-55 to +175
DLF102	-	100	70	100			
DLF103	-	200	140	200			
DLF104	-	400	280	400		250	
DLF105	-	600	420	600			
DLF106	-	800	560	800			

*1 Repetitive peak reverse voltage

*2 RMS voltage

*3 Continuous reverse voltage

*4 Maximum forward voltage

*5 Reverse recovery time

RATING AND CHARACTERISTIC CURVES (FSM101 THRU FSM106)

FIG.1-TYPICAL FORWARD CHARACTERISTICS

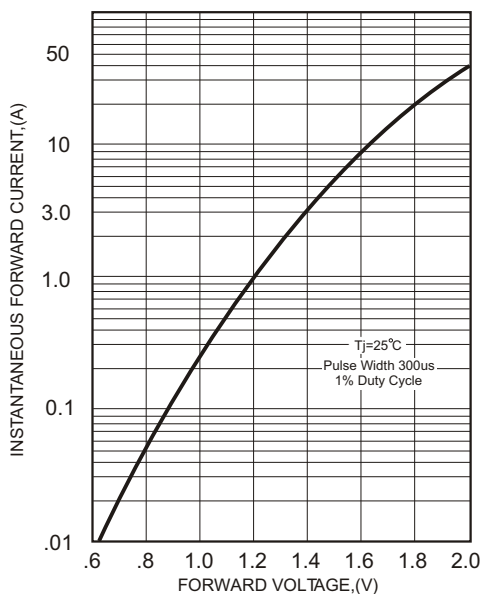


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

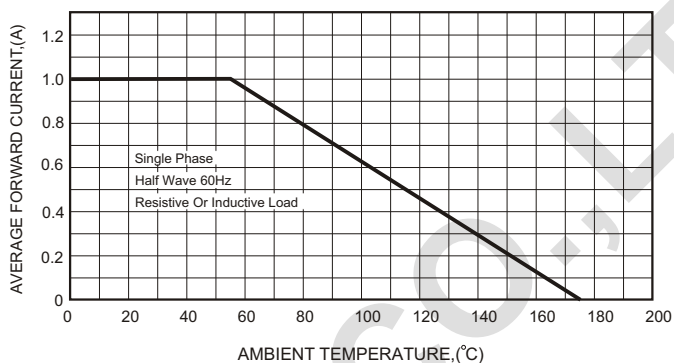
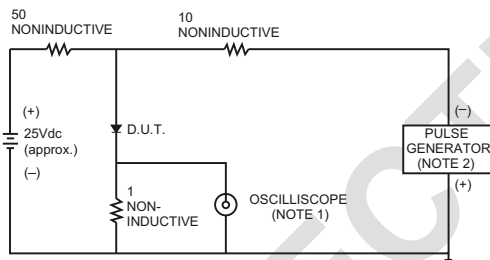


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm, 22pF.

2. Rise Time= 10ns max., Source Impedance= 50 ohms.

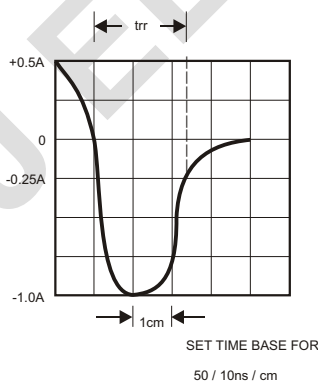


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

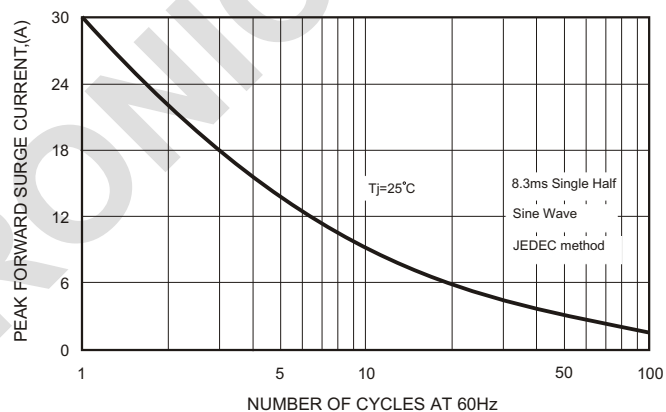


FIG.5-TYPICAL JUNCTION CAPACITANCE

