US5881

CMOS Multi-Purpose Switch

Features and Benefits

- Chopper stabilized amplifier stage
- New miniature package / thin, high reliability package
- Operation down to 3.5V
- CMOS for optimum stability, quality and cost

Applications

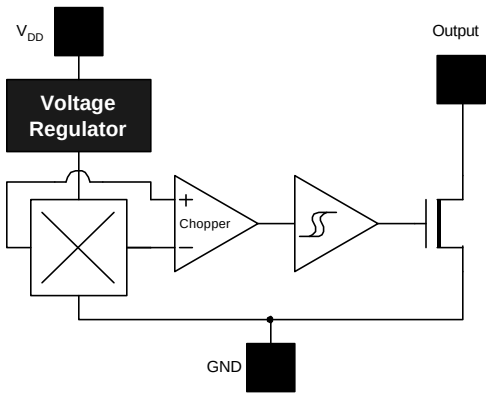
- Solid state switch
- Limit switch
- Current limit
- Interrupter
- Current sensing

Ordering Information

Part No.	Temperature Suffix	Package	Temperature Range
US5881	E	SO or UA	-40°C to 85°C Extended
US5881	L	SO or UA	-40°C to 150°C Automotive

*Contact factory or sales representative for legacy temperature options

Functional Diagram



Description

The US5881 is a unipolar Hall effect sensor IC fabricated from mixed signal CMOS technology. It incorporates advanced chopper stabilization techniques to provide accurate and stable magnetic switch points. There are many applications for this sensor in addition to those listed above. The design, specifications and performance have been optimized for applications of solid state switches.

The output transistor will be switched on (B_{OP}) in the presence of a sufficiently strong South pole magnetic field facing the marked side of the package. Similarly, the output will be switched off (B_{RP}) in the presence of a weaker South field and remain off with "0" field. The SOT-23 device is reversed from the UA package. The SOT-23 output transistor will be switched on (B_{OP}) in the presence of a sufficiently strong North pole magnetic field subjected to the marked face.

Note: *This is a static-sensitive device; please observe ESD precautions. Reverse V_{DD} protection is not included. For reverse voltage protection, a 100Ω resistor in series with V_{DD} is recommended.*

US5881 Electrical Specifications

Parameter	*Symbol*	*Test Conditions*	*Min*	*Typ*	*Max*	*Units*
Supply Voltage	V_{DD}	Operating	3.5		24	V
Supply Current	I_{DD}	B<B_{RP}	1.5	2.5	4.0	mA
Saturation Voltage	$V_{DS(on)}$	I_{OUT} = 20 mA, B>B_{OP}		0.4	0.5	V
Output Leakage	I_{OFF}	B<B_{RP}, V_{OUT} = 20V		0.01	5.0	ì A
Output Rise Time	t_r	V_{DD} = 12V, R_L = 1.1KÙ, C_L = 20pf		0.04		ì s
Output Fall Time	t_f	V_{DD} = 12V, R_L = 1.1KÙ, C_L = 20pf		0.18		ì s

US5881 Magnetic Specifications

Parameter	*Symbol*	*Test Conditions*	*Min*	*Typ*	*Max*	*Units*
Operating Point[3]	B_{OP}		15	25	30	mT
Release Point	B_{RP}		9.5	20	-	mT
Hysteresis	B_{hys}		2.0	4.3	5.5	mT

Notes:
1. 1 mT = 10 Gauss.
2. The SOT-23 device is reversed from the UA package. The SOT-23 output transistor will be switched on (BOP) in the presence of a sufficiently strong North pole magnetic field subjected to the markedface.
3. At –40ºC, maximum B_{OP} = 35 mT.

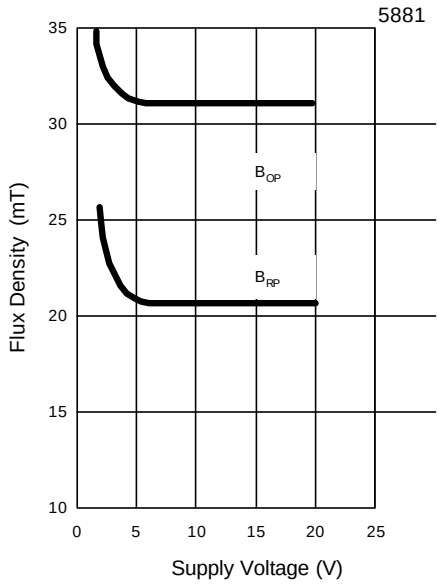
Absolute Maximum Ratings

Supply Voltage (Operating), V_{DD}	24V
Supply Current (Fault), I_{DD}	50mA
Output Voltage, V_{OUT}	24V
Output Current (Fault), I_{OUT}	50mA
Power Dissipation, P_D	100mW
Operating Temperature Range, T_A	-40°C to 150°C
Storage Temperature Range, T_S	-65°C to 150°C
Maximum Junction Temp, T_J	175°C
ESD Sensitivity (All Pins)	+/- 4KV

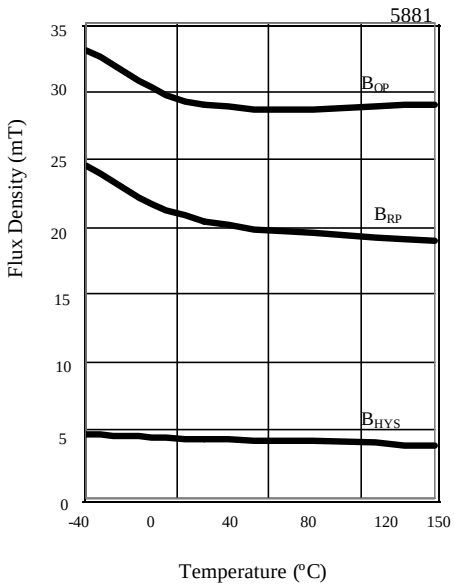
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Performance Graphs

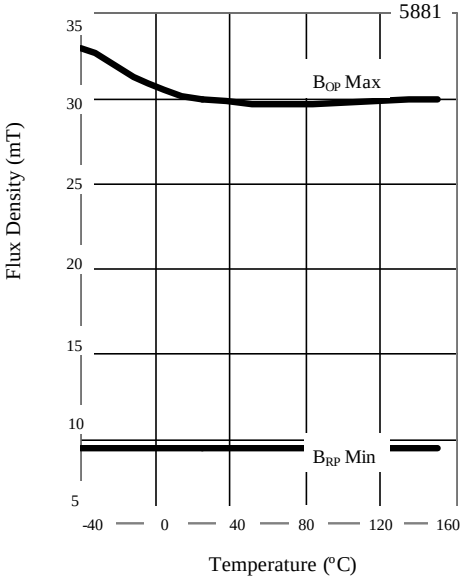
Typical Magnetic Switch Points versus Supply Voltage



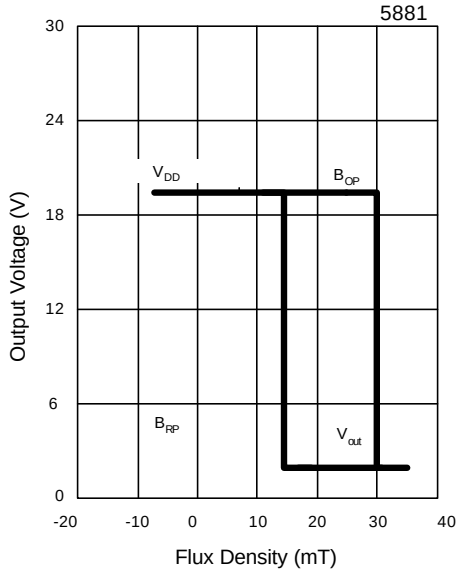
Typical Magnetic Switch Points versus Temperature



Minimum & Maximum Magnetic Switch Range versus Temperature

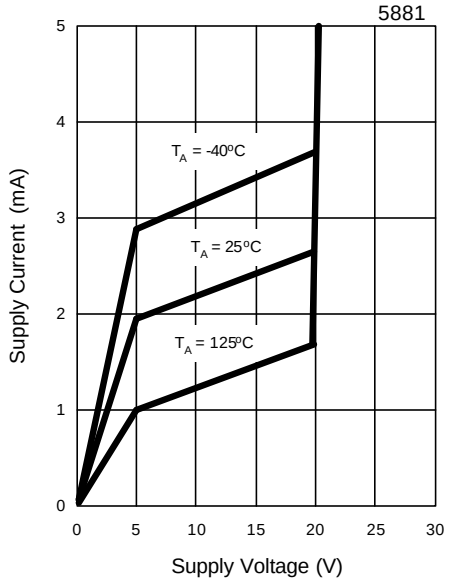


Output Voltage versus Flux Density

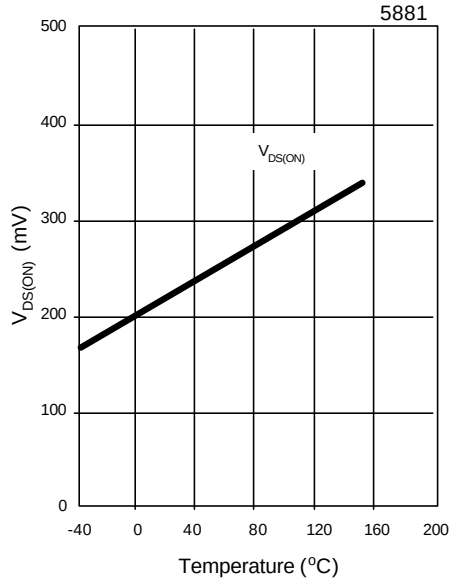


Performance Graphs

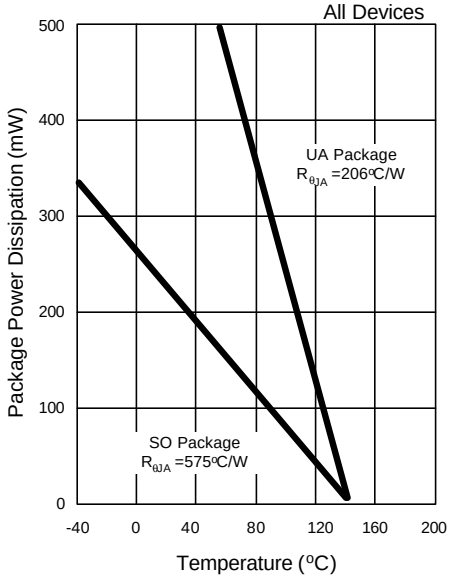
Typical Supply Current versus Supply Voltage



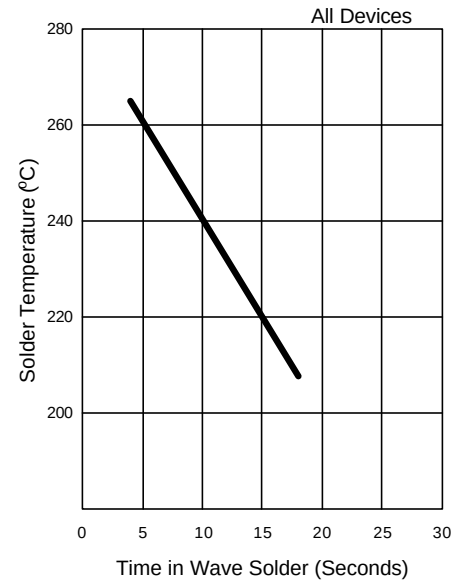
Typical Saturation Voltage versus Temperature
V_{DD} = 12 V, I_{OUT} = 20mA



Power Dissipation versus Temperature



Wave Soldering Parameters



Unique Features

CMOS Hall IC Technology

The chopper stabilized amplifier uses switched capacitor techniques to eliminate the amplifier offset voltage, which, in bipolar devices, is a major source of temperature sensitive drift. CMOS makes this advanced technique possible.

The CMOS chip is also much smaller than a bipolar chip, allowing very sophisticated circuitry to be placed in less space. The small chip size also contributes to lower physical stress and less power consumption.

Installation

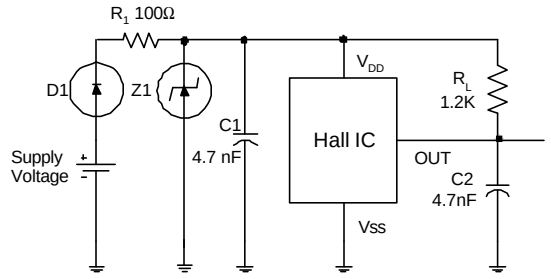
Consider temperature coefficients of Hall IC and magnetics, as well as air gap life time variations. Observe temperature limits during wave soldering.

Application Comments

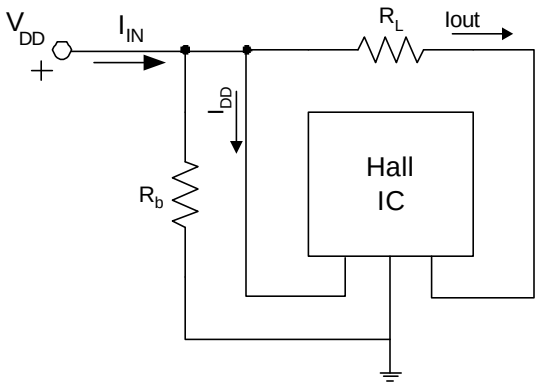
If reverse supply protection is desired, use a resistor in series with the V_{DD} pin. The resistor will limit the supply current (Fault), I_{DD}, to 50 mA. For severe EMC conditions, use the application circuit below.

Applications Examples

Automotive and Severe Environment Protection Circuit



Two Wire Optional Current Biasing Circuit

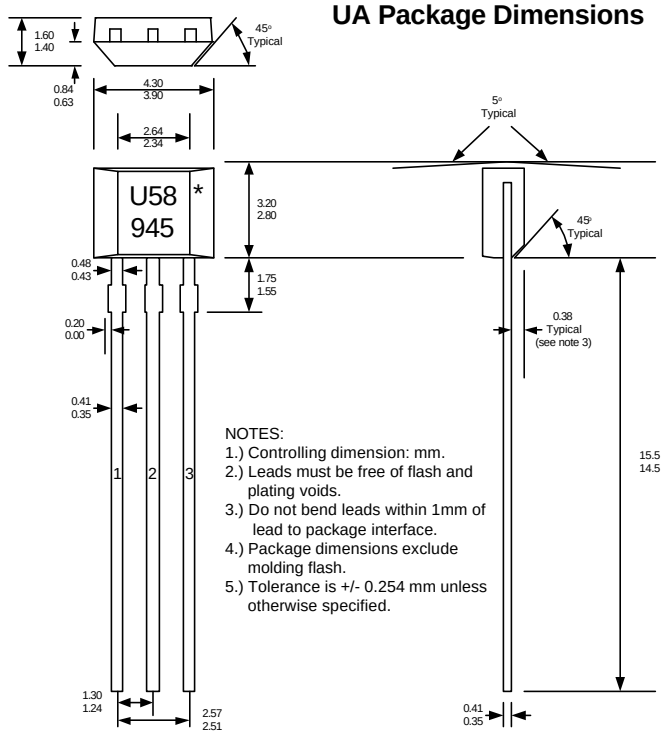


The resistors R_b and R_L can be used to bias the input current, I_{IN}. Refer to the part specification for limiting values. This circuit will help in getting the precise ON and OFF currents desired.

B_{RP} = Ioff = (V_{DD} / Rb + I_{DD})
B_{OP} = Ion = (Ioff + V_{DD} / R_L)

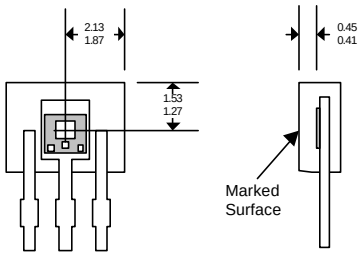
Physical Characteristics

UA Package Dimensions



NOTES:
1.) Controlling dimension: mm.
2.) Leads must be free of flash and plating voids.
3.) Do not bend leads within 1mm of lead to package interface.
4.) Package dimensions exclude molding flash.
5.) Tolerance is +/- 0.254 mm unless otherwise specified.

UA Hall Plate / Chip Location



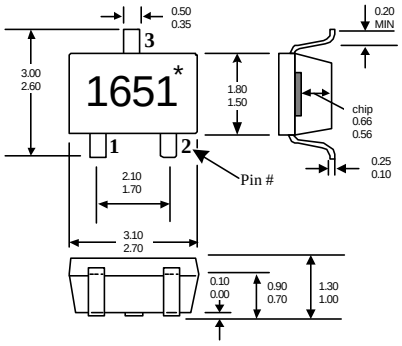
All Dimensions in millimeters

* MARKING:
Line 1:
1st digit (U) = Supplier (Melexis)
2nd and 3rd digits (58) = Series (5880)

Line 2:
1st digit (9) = Year (1999)
2nd and 3rd digits(45) = Week of Year

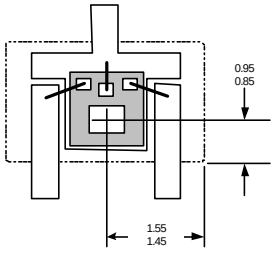
PINOUT:
Pin 1 V_{DD}
Pin 2 GND
Pin 3 Output

SOT-23 Package Dimensions (Top View)



NOTES:
1. MARKING:
   1st Digit (1) = Series (1880)
   2nd Digit(6) = Year - 1996
   Last Digits (51) = Week of Year
2. PINOUT (See Top View at left):
   Pin 1 VDD
   Pin 2 Output
   Pin 3 GND
3. Controlling dimension: mm.
4. Lead thickness after solder plating will be 0.254 mm maximum.
5. Package dimensions exclude molding flash.
6. The end flash shall not exceed 0.127 mm on each side of package.
7. Tolerance is +/- 0.254 mm unless otherwise specified.

SOT-23 Hall Plate / Chip Location (Bottom View)



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