

Low Power 16-bit Single Chip Microcontroller

- Low power MCU : lower operating voltage 1.8V, TBDuA/SLEEP, TBDuA/HALT
- Large capacity flash memory: 1MByte, font data available
- LCD driver: 160 SEG x 32 COM (max.), pseudo 64 SEG x 64 COM display support by 64com emulation mode, 4 level gray-scale, SEG outputs n-line AC reverse drive, COM outputs partial drive
- Analog I/F: A/D converter, R/F converter(for temperature and humidity instruments), Supply Voltage Detector(intermittent operation mode, supply voltage detection RESET function)
- RISC CPU core S1C17: the compact code optimized for C, and high throughput of an instruction/clock, supports serial ICE

■ DESCRIPTIONS

The S1C17706 is a 16-bit MCU featuring high-speed low-power operations, compact dimensions, wide address space, and on-chip ICE. Based on an S1C17 CPU core, this product consists of 1024K bytes of Flash memory, 12K bytes of RAM, serial interface modules supporting sensors such as UART to support high-bit rate and IrDA1.0, SPI, and I2C, various timers, 34 general input/output ports, maximum 160 segment x 32 common LCD driver and a power supply voltage booster circuit, A/D converter, R/F converter, supply voltage detector, and 32 kHz and maximum 8.2 MHz oscillator circuits. The embedded Flash memory can include font data, such as 11 x 12-dot Japanese characters (JIS level-1, JIS level-2 and other kanji fonts), Hangul characters and user defined characters.

It allows 8.2 MHz high-speed operation at a minimum of 1.8 V operating voltage, and executes a basic instruction in one clock cycle with 16-bit RISC processing. The S1C17706 also includes a coprocessor supporting multiplication, division, and MAC (multiply and accumulation) operations.

The on-chip ICE function allows onboard Flash programming/erasing, program debugging, and evaluations using the ICDmini (S5U1C17001H) that can be connected with three signal wires.

The S1C17706 is ideal for applications, such as remote controllers, health care products, and sports watches, that must be driven with battery power and require sensor interfaces and a high-definition LCD display.

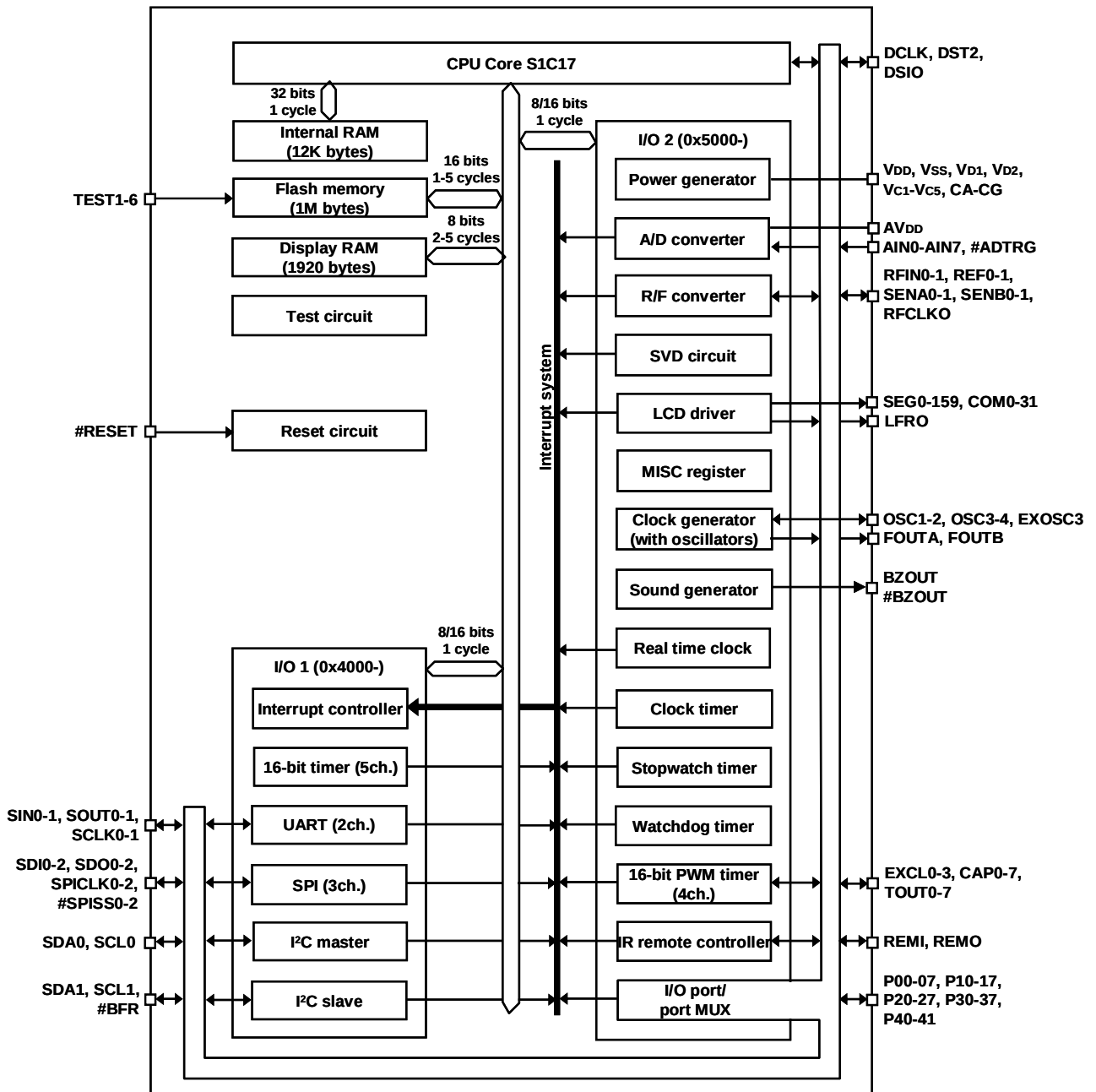
■ FEATURES

CPU	<ul style="list-style-type: none"> • Seiko Epson original 16-bit RISC CPU core S1C17 • Multiplier/Divider (COPRO) <ul style="list-style-type: none"> - 16-bit x 16-bit multiplier - 16-bit x 16-bit + 32-bit multiply and accumulation unit - 16-bit ÷ 16-bit divider 	
On-chip Flash memory	<ul style="list-style-type: none"> • 1M bytes (for both instructions and data) • 1,000 erase/program cycles (min.) • Read/program protection function • Allows on-board programming using a debugging tool such as ICDmini (S5U1C17001H) and self-programming by software control. • Allows storing 11 x 12-dot font data. 	
On-chip RAM	<ul style="list-style-type: none"> • 12K bytes 	
On-chip display RAM	<ul style="list-style-type: none"> • Display data: 960 bytes / Gray-scale data: 960 bytes 	
Clock generator	<ul style="list-style-type: none"> • Three types of built-in oscillators (system clock sources) <ul style="list-style-type: none"> - IOSC oscillator circuit 2.7 MHz (typ.) - OSC3 oscillator circuit 8.2 MHz (max.) crystal or ceramic oscillator circuit - OSC1 oscillator circuit 32.768 kHz (typ.) crystal oscillator circuit • Core clock frequency control • Peripheral module clock supply control • IOSC control for quick-restart processing from SLEEP mode 	
I/O ports	<ul style="list-style-type: none"> • Max. 34 general-purpose I/O ports (Pins are shared with the peripheral I/O.) 	
Serial interfaces	• SPI 3 channels	
	• I2C master (I2CM)	1 channel
	• I2C slave (I2CS)	1 channel
	• UART (115200 bps, IrDA 1.0)	2 channels
	• IR remote controller (REMC)	1 channel
	• 16-bit timer (T16)	5 channels
Timers	• 16-bit PWM timer (T16A2)	4 channels
	• Clock timer (CT)	1 channel

	<ul style="list-style-type: none"> • Stopwatch timer (SWT) 1 channel • Watchdog timer (WDT) 1 channel
Real-time clock	<ul style="list-style-type: none"> • Year(with bissextile correction) / month / week / day / hour / minute / second counter, 30sec time correction, 1ch
Sound generator	<ul style="list-style-type: none"> • Tones 8 level, Sound volumes 8 level, digital envelop, 1ch
LCD driver	<ul style="list-style-type: none"> • 160 SEG × 32 COM (1/5 bias) • 64 COM emulation display mode • Built-in voltage booster • 4-level gray-scale • SEG outputs n-line AC reverse drive • COM outputs partial drive
A/D converter	<ul style="list-style-type: none"> • Successive approximation type • Eight analog input channels (max.) • 10-bit resolution
R/F converter	<ul style="list-style-type: none"> • Two channels, CR oscillation type • 24-bit counter • Supports DC-bias resistive/capacitive sensors and AC-bias resistive sensors. • Supports external input for counting pulses.
Supply voltage detector (SVD)	<ul style="list-style-type: none"> • 15 programmable detection levels (1.8 V to 3.2 V) • Intermittent operation mode • Supply voltage detection RESET function
Interrupts	<ul style="list-style-type: none"> • Reset • NMI • 27 programmable interrupts (eight levels)
Power supply voltage	<ul style="list-style-type: none"> • 1.8 V to 3.6 V (for normal operation) • 2.5 V to 3.6 V (for Flash erasing/programming) • Built-in voltage regulator (two operating voltages switchable)
Operating temperature	<ul style="list-style-type: none"> • -25°C to 70°C
Current consumption	<ul style="list-style-type: none"> • SLEEP state: 1.2 μA typ. (OSC1: Off, IOSC: Off, OSC3: Off) • HALT state: 2.7 μA typ. (OSC1: 32 kHz, IOSC: Off, OSC3: Off, LCD: Off) 9.7 μA typ. (OSC1: 32 kHz, IOSC: Off, OSC3: Off, LCD On all on) • Run state: 18 μA typ. (OSC1: 32 kHz, IOSC: Off, OSC3: Off, LCD Off) 557 μA typ. (OSC1: Off, IOSC: Off, OSC3: 1 MHz ceramic, LCD On all on)
Shipping form	<ul style="list-style-type: none"> • QFP22-256pin package (28 mm × 28 mm, lead pitch: 0.4 mm) • Die form (pad pitch: 80 μm)

S1C17706

■ BLOCK DIAGRAM



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