

UTC UNISONIC TECHNOLOGIES CO., LTD

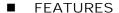
MGBR20S50 **Preliminary DIODE**

MOS GATED BARRIER RECTIFIER

DESCRIPTION

The UTC MGBR20S50 is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed etc.

The UTC MGBR20S50 suitable for supply applications.



- * Super low forward voltage drop
- * High switching speed

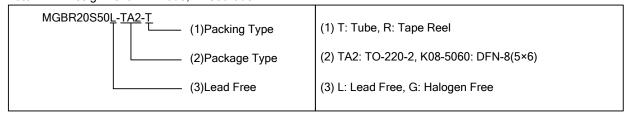


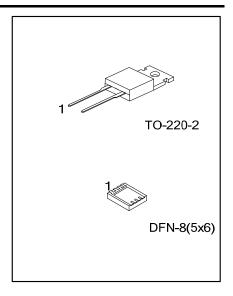


ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment							Dooking		
Lead Free	Halogen Free	Package	1	2	3	4	5	6	7	8	Packing	
MGBR20S50L-TA2-T	MGBR20S50G-TA2-T	TO-220-2	Κ	Α	-	-	1	-	-	-	Tube	
MGBR20S50L-K08-5060-R	MGBR20S50G-K08-5060-R	DFN-8(5×6)	Α	Α	Α	NC	Κ	Κ	Κ	Κ	Tape Reel	

Note: Pin Assignment: A: Anode, K: Cathode





■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V_{RM}	50	V
Working Peak Reverse Voltage	V_{RWM}	50	V
Peak Repetitive Reverse Voltage	V_{RRM}	50	V
Average Rectified Forward Current (Rated VR-20Khz Square Wave) - 50% Duty Cycle	Io	20	Α
Peak Forward Surge Current - 1/2 60hz	I _{FSM}	300	Α
Peak Repetitive Reverse Surge Current (2uS-1Khz)	I _{RRM}	2	Α
Maximum Rate of Voltage Change (at Rated V _R)	dv/dt	10000	V/µS
Operating Junction Temperature	TJ	-65~+150	°C
Storage Junction Temperature	T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
hunstian to Austriant	TO-220-2	0	60	V/V >0
Junction to Ambient	DFN-8(5×6)	$\Theta_{ m JA}$	72	°C/W

■ ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	50			V
Forward Voltage	V _{EM}	I _F =20A, T _J =25°C			0.53	V
		I _F =20A, T _J =125°C			0.48	V
Deverse Comment (Note 4)		V _R =50V, T _J =25°C			500	μA
Reverse Current (Note 1)	IRM	V _R =50V, T _J =125°C			100	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

^{2.} Thermal resistance junction to case mounted on heatsink.

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