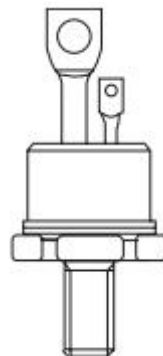


FEATURES

- Standard package
- Low thermal resistance
- Improved glass passivation for high reliability and exceptional stability at high temperature
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



APPLICATIONS

- Medium power switching
- Phase control applications
- Can be supplied to meet stringent military, aerospace and other high-reliability requirements

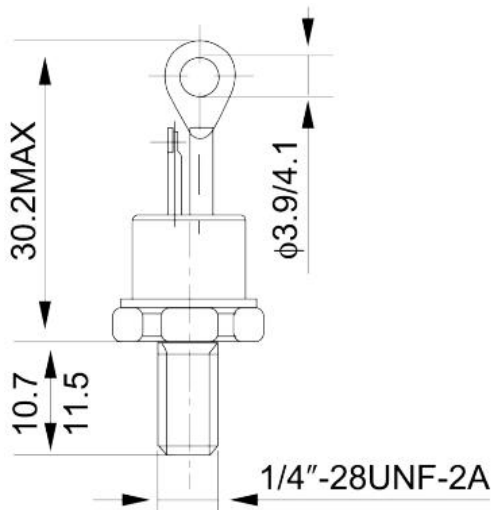
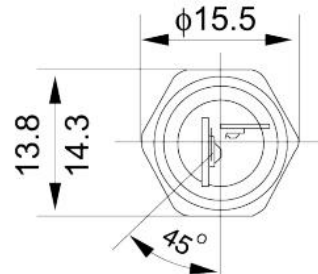
ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	CONDITIONS		VALUE	UNIT
V _{DRM} /V _{DRM}	Repetitive Peak Reverse Voltage			100	V
I _{F(AV)}	Average Forward Current	T _c =85℃, 180° conduction, half sine wave		25	A
I _{FSM}	Surge Forward Current	t=10ms	No voltage reappplied	420	A
		t=8.3ms		440	
		t=10ms	100%V _{RRM} reappplied	350	
		t=8.3ms		370	
I ² t	Maximum I ² t for fusing	t=10ms	No voltage reappplied	867	A ₂ s
		t=8.3ms		790	
		t=10ms	100%V _{RRM} reappplied	615	
		t=8.3ms		560	
T _J	Junction Temperature			-65~125	℃
T _{stg}	Storage Temperature			-65~125	℃

ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	MAX		UNIT
IRRM	Repetitive peak reverse current	$V_{RM}=V_{RRM}, R_{GK}=220\ \Omega$		20	mA
IDRM	Repetitive peak off-state current	$V_{DM}=V_{DRM}, R_{GK}=220\ \Omega$		20	mA
VTM	On-state voltage	$I_{pk}=50A, T_J=25^\circ C, t_p=10ms$ sine pulse		1.71	V
IGT	Gate-trigger current	$V_D=6V; I_T=0.1A$		60	mA
VGT	Gate-trigger voltage	$V_D=6V; I_T=0.1A$		2	V
$R_{th(j-c)}$	Thermal resistance	Junction to case		0.75	$^\circ C/W$

PACKAGE OUTLINE



*FOR METRIC DEVICE: M6×1

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