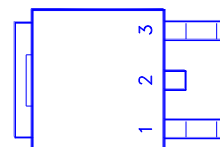
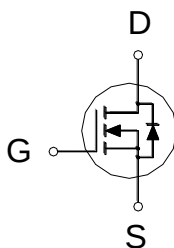




PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
80V	68m Ω	16A



1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	80	V
Gate-Source Voltage		V_{GS}	± 25	V
Continuous Drain Current	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	16	A
	$T_C = 100\text{ }^{\circ}\text{C}$		10	
Pulsed Drain Current ¹		I_{DM}	50	
Avalanche Current		I_{AS}	15	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	11	mJ
Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	P_D	39	W
	$T_C = 100\text{ }^{\circ}\text{C}$		15	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		3.2	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, Unless Otherwise Noted)

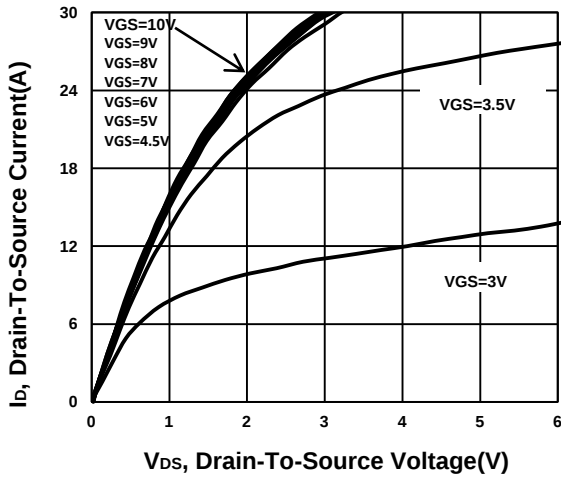
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	80			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 25V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 64V, V_{GS} = 0V$			1	μA
		$V_{DS} = 60V, V_{GS} = 0V, T_J = 125^{\circ}C$			10	

Drain-Source On-State Resistance ₁	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 10A		51	78	mΩ
		V _{GS} = 10V , I _D = 15A		48	68	
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 15A		30		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		558		pF
Output Capacitance	C _{oss}			68		
Reverse Transfer Capacitance	C _{rss}			46		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.2		Ω
Total Gate Charge ²	Q _g	V _{GS} = 10 V , V _{DS} = 40V , I _D = 15A		13.5		nC
Gate-Source Charge ²	Q _{gs}			1.8		
Gate-Drain Charge ²	Q _{gd}			5		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 40V I _D ≅ 15A, V _{GS} = 10V, R _{GEN} =6Ω		14		nS
Rise Time ²	t _r			12		
Turn-Off Delay Time ²	t _{d(off)}			38		
Fall Time ²	t _f			16		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current	I _S				16	A
Forward Voltage ¹	V _{SD}	I _F = 15A, V _{GS} = 0V			1.3	V
Reverse Recovery Time	t _{rr}	I _F = 15A, dI _F /dt = 100A / μS		21		nS
Reverse Recovery Charge	Q _{rr}			19		nC

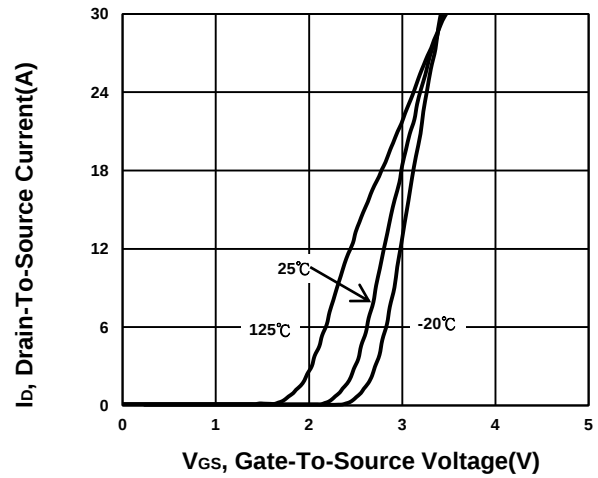
¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

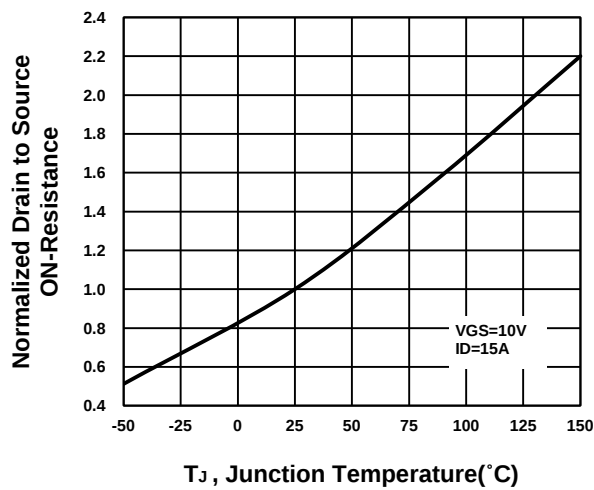
Output Characteristics



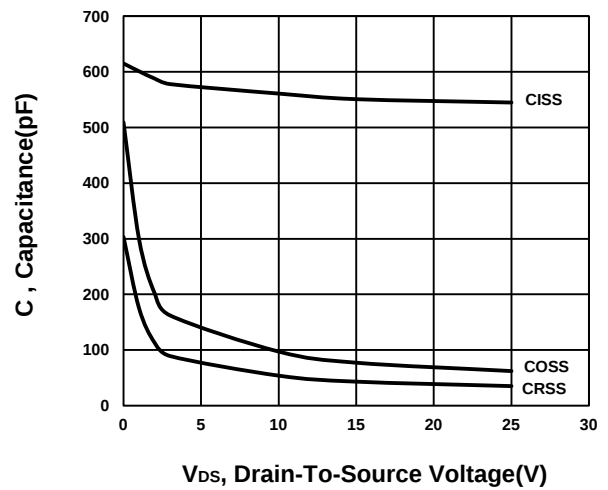
Transfer Characteristics



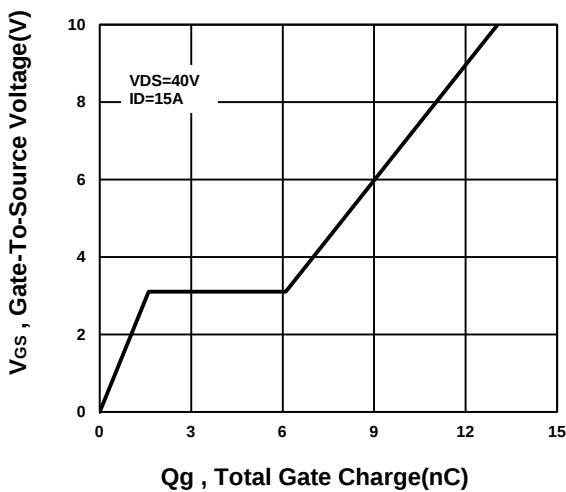
On-Resistance VS Temperature



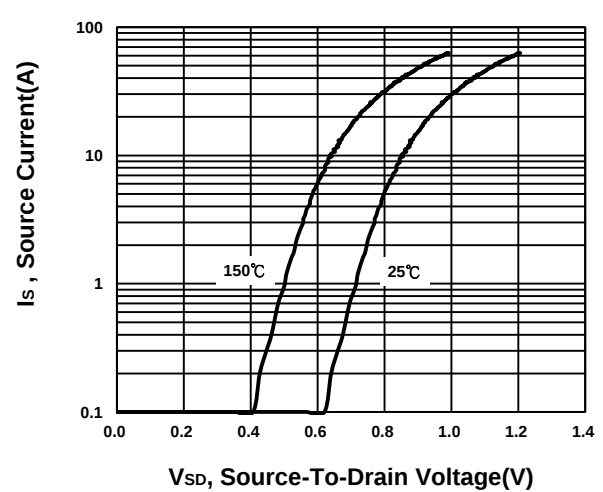
Capacitance Characteristic



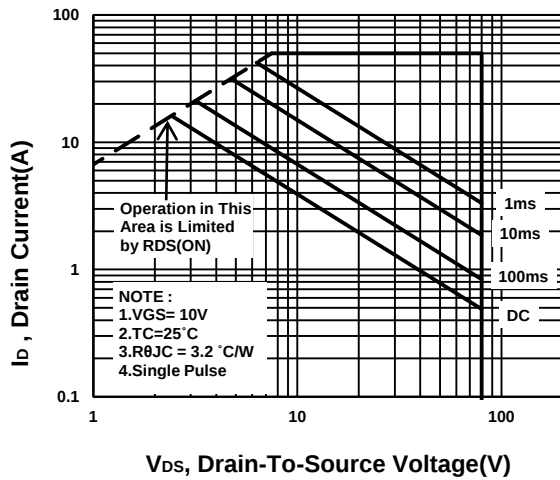
Gate charge Characteristics



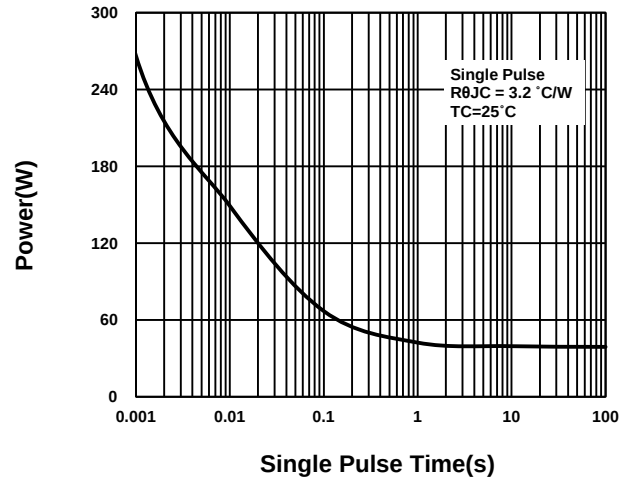
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

