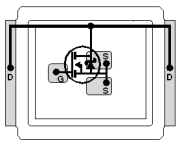
Features

- Latest MOSFET silicon technology
- Key parameters optimized for Class-D audio amplifier applications
- Low $R_{DS(on)}$ for improved efficiency
- Low Qg for better THD and improved efficiency
- Low Qrr for better THD and lower EMI
- Low package stray inductance for reduced ringing and lower EMI
- Can deliver up to 200 W per channel into 8Ω load in half-bridge configuration amplifier
- Dual sided cooling compatible
- Compatible with existing surface mount technologies
- RoHS compliant, halogen-free
- Lead-free (qualified up to 260°C reflow)

Key Parameters		
V_{DS}	150	V
$R_{DS(ON)}$ typ. @ V_{GS} = 10V	29	mΩ
Qg typ.	39	nC
$R_{G(int)}$ typ.	0.9	Ω



MZ


DirectFET® ISOMETRIC

Applicable DirectFET Outline and Substrate Outline (see p.6, 7 for details)

SH	SJ	ST	SH	MQ	MX	MT	MN	**MZ**		

Description

This Digital Audio MOSFET is specifically designed for Class-D audio amplifier applications. This MOSFET utilizes the latest processing techniques to achieve low on-resistance per silicon area. Furthermore, gate charge, body-diode reverse recovery and internal gate resistance are optimized to improve key Class-D audio amplifier performance factors such as efficiency, THD, and EMI.

The IRF6643PbF device utilizes DirectFET® packaging technology. DirectFET® packaging technology offers lower parasitic inductance and resistance when compared to conventional wirebonded SOIC packaging. Lower inductance improves EMI performance by reducing the voltage ringing that accompanies fast current transients. The DirectFET® package is compatible with existing layout geometries used in power applications, PCB assembly equipment and vapor phase, infra-red or convection soldering techniques, when application note AN-1035 is followed regarding the manufacturing method and processes. The DirectFET® package also allows dual sided cooling to maximize thermal transfer in power systems, improving thermal resistance and power dissipation. These features combine to make this MOSFET a highly efficient, robust and reliable device for Class-D audio amplifier applications.

Base part number	Package Type	Standard Pack		Orderable Part Number
		Form	Quantity	
IRF6643TRPbF	DirectFET Medium Can	Tape and Reel	4800	IRF6643TRPbF

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{GS}	Gate-to-Source Voltage	±20	V
I_D @ T_C = 25°C	Continuous Drain Current, V_{GS} @ 10V	35	A
I_D @ T_A = 25°C	Continuous Drain Current, V_{GS} @ 10V	6.2	
I_D @ T_A = 70°C	Continuous Drain Current, V_{GS} @ 10V	5.0	
I_{DM}	Pulsed Drain Current ①	76	
P_D @T_C = 25°C	Power Dissipation	89	W
P_D @T_A = 25°C	Power Dissipation ③	2.8	
P_D @T_A = 70°C	Power Dissipation ③	1.8	
E_{AS}	Single Pulse Avalanche Energy ②	50	mJ
I_{AR}	Avalanche Current ①	7.6	A
	Linear Derating Factor	0.022	W/°C
T_J T_{STG}	Operating Junction and Storage Temperature Range	-40 to + 150	°C

Notes ① through ⑨ are on page 9

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Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient ③	—	45	°C/W
$R_{\theta JA}$	Junction-to-Ambient ⑥	12.5	—	
$R_{\theta JA}$	Junction-to-Ambient ⑦	20	—	
$R_{\theta JC}$	Junction-to-Case ⑧⑨	—	1.4	
$R_{\theta J\text{-}PCB}$	Junction-to-PCB Mounted	1.0	—	

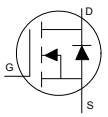
Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	150	—	—	V	V_{GS} = 0V, I_D = 250µA
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.18	—	V/°C	Reference to 25°C, I_D = 1.0mA
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	29	34.5	mΩ	V_{GS} = 10V, I_D = 7.6A ④
$V_{GS(th)}$	Gate Threshold Voltage	3.0	4.0	4.9	V	V_{DS} = V_{GS}, I_D = 150µA
$\Delta V_{GS(th)}$	Gate Threshold Voltage Coefficient	—	-11	—	mV/°C	
I_{DSS}	Drain-to-Source Leakage Current	—	—	20	µA	V_{DS} = 150V, V_{GS} = 0V
		—	—	250		V_{DS} = 120V, V_{GS} = 0V, T_J=125°C
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V_{GS} = 20V
	Gate-to-Source Reverse Leakage	—	—	-100		V_{GS} = -20V
R_G	Gate Resistance	—	0.8	—	Ω	

Dynamic @ T_J = 25°C (unless otherwise specified)

gfs	Forward Transconductance	16	—	—	S	V_{DS} = 10V, I_D = 7.6A
Q_g	Total Gate Charge	—	39	55	nC	V_{DS} = 75V V_{GS} = 10V I_D = 7.6A
Q_{gs1}	Pre-VthGate-to-Source Charge	—	9.6	—		
Q_{gs2}	Post-Vth Gate-to-Source Charge	—	2.2	—		
Q_{gd}	Gate-to-Drain Charge	—	11	17		
Q_{godr}	Gate Charge Overdrive	—	16	—		
Q_{sw}	Switch Charge (Q_{gs2} + Q_{gd})	—	13	—		
$t_{d(on)}$	Turn-On Delay Time	—	9.2	—	ns	V_{DD} = 75V, V_{GS} = 10V I_D = 7.6A
t_r	Rise Time	—	5.0	—		
$t_{d(off)}$	Turn-Off Delay Time	—	13	—		
t_f	Fall Time	—	4.4	—		
C_{iss}	Input Capacitance	—	2340	—	pF	V_{GS} = 0V V_{DS} = 25V f = 1.0MHz
C_{oss}	Output Capacitance	—	300	—		
C_{rss}	Reverse Transfer Capacitance	—	61	—		
C_{oss}	Output Capacitance	—	1950	—		V_{GS}=0V, V_{DS}=1.0V, f=1.0MHz
C_{oss}	Output Capacitance	—	140	—		V_{GS}=0V, V_{DS}=80V, f=1.0MHz

Diode Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	58	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	76		
V_{SD}	Diode Forward Voltage	—	—	1.3	V	T_J = 25°C, I_S = 7.6A, V_{GS} = 0V ④
t_{rr}	Reverse Recovery Time	—	67	100	ns	T_J = 25°C, I_F = 7.6A, V_{DD} = 50V
Q_{rr}	Reverse Recovery Charge	—	190	280	nC	di/dt = 100A/µs ④

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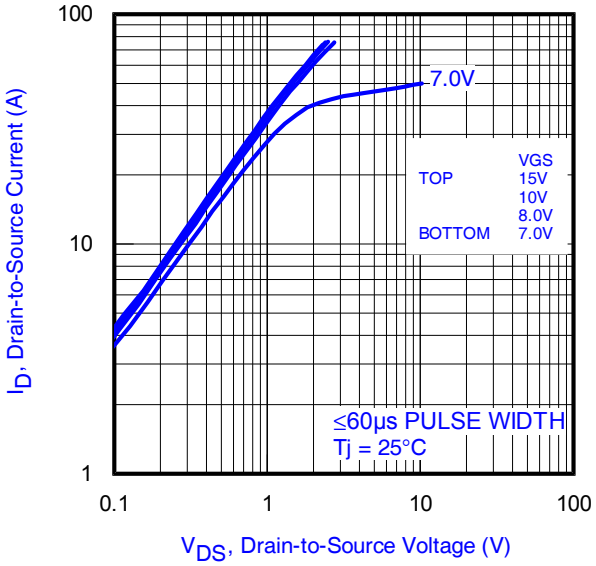


Fig 1. Typical Output Characteristics

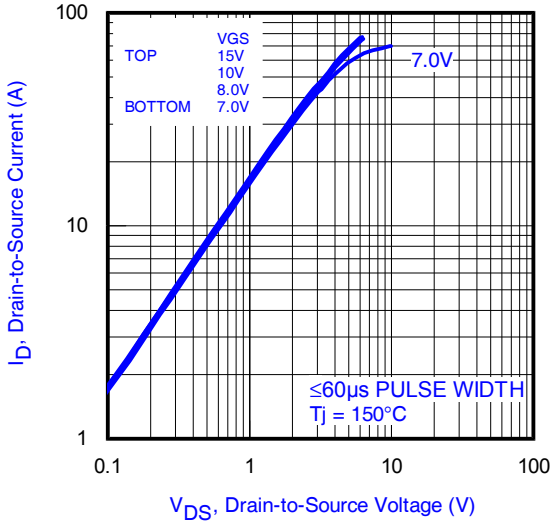


Fig 2. Typical Output Characteristics

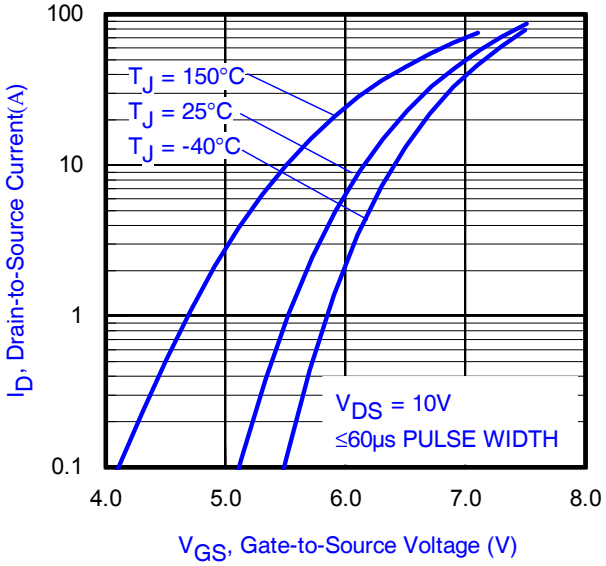


Fig 3. Typical Transfer Characteristics

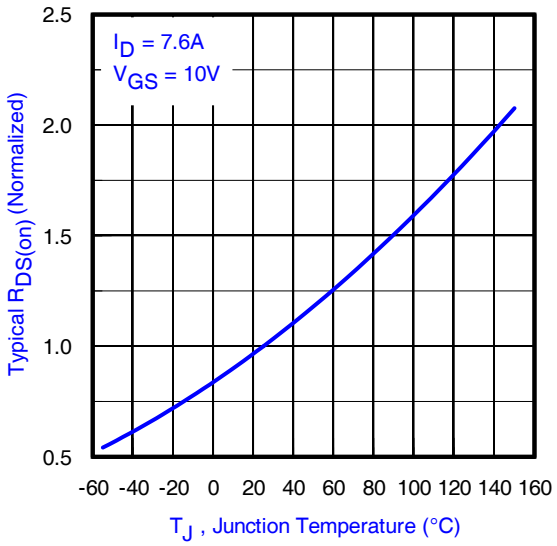


Fig 4. Normalized On-Resistance vs. Temperature

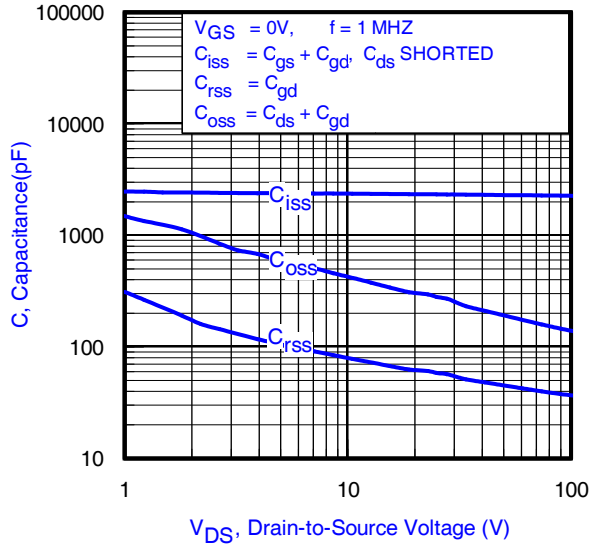


Fig 5. Typical Capacitance vs. Drain-to-Source Voltage

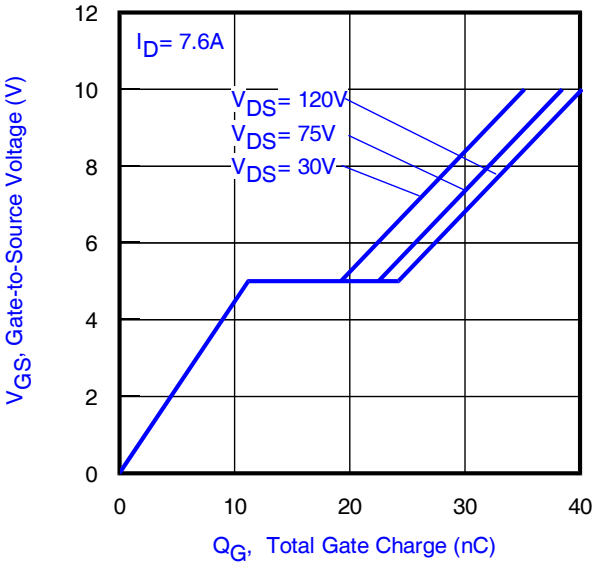


Fig 6. Typical Gate Charge vs Gate-to-Source Voltage

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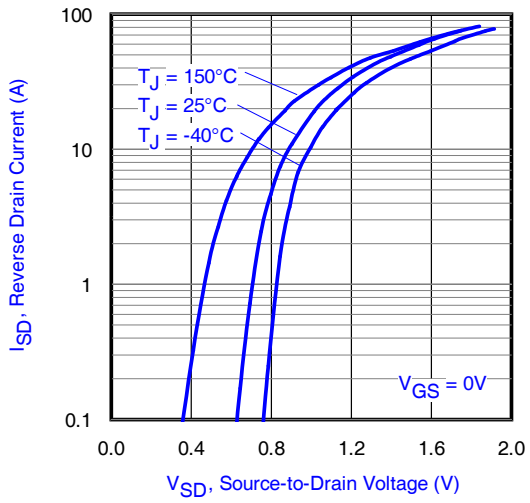


Fig 7. Typical Source-Drain Diode Forward Voltage

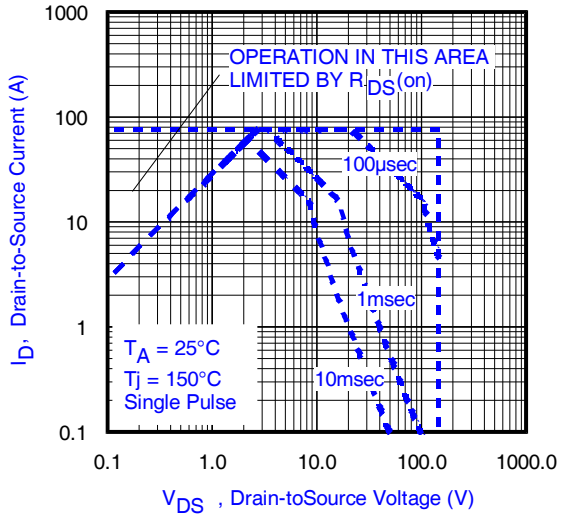


Fig 8. Maximum Safe Operating Area

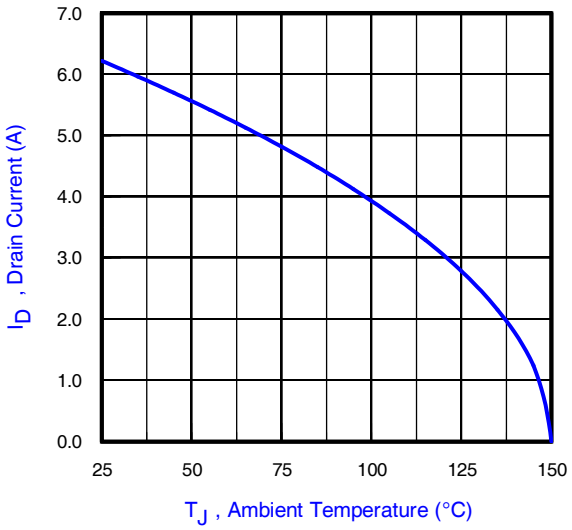


Fig 9. Maximum Drain Current vs. Ambient Temperature

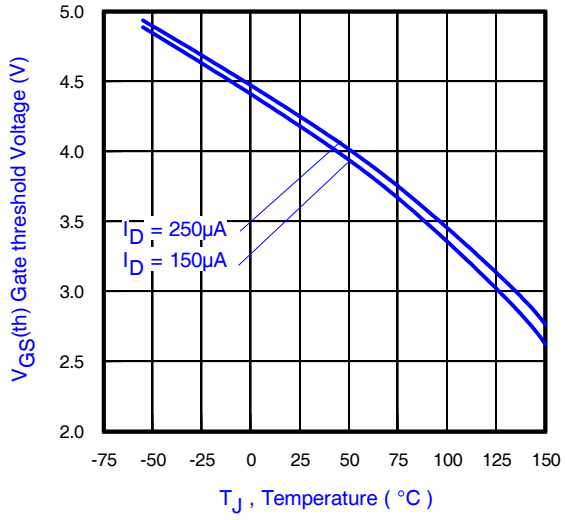


Fig 10. Typical Threshold Voltage vs. Junction Temperature

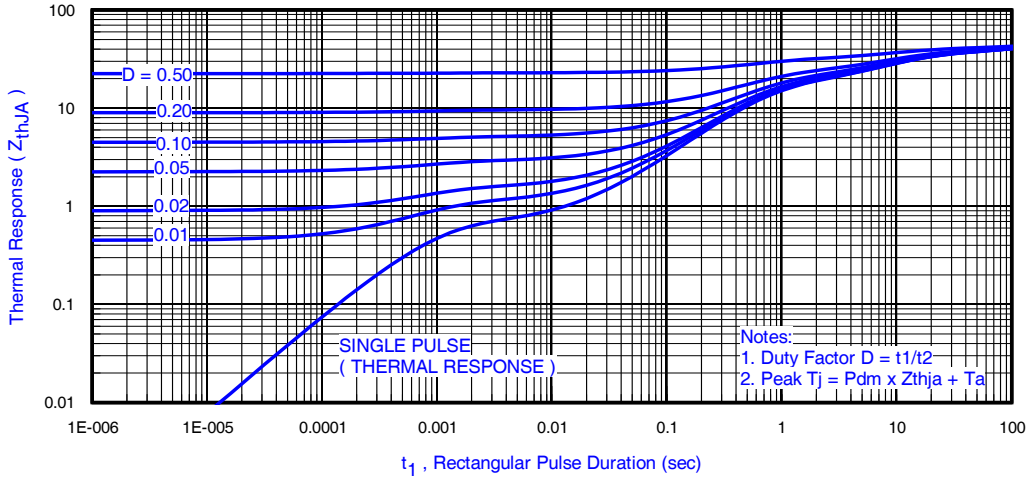


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient ③

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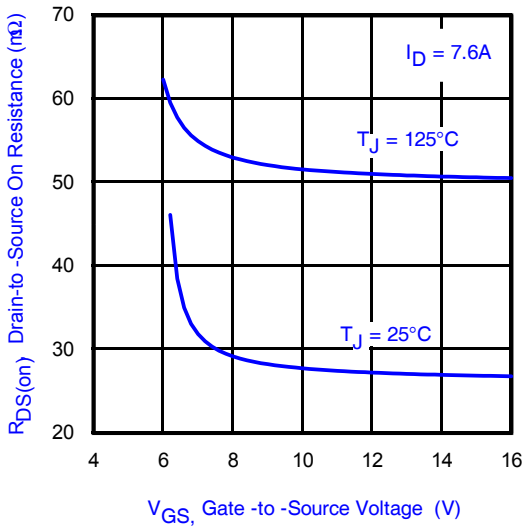


Fig 12. Typical On-Resistance vs. Gate Voltage

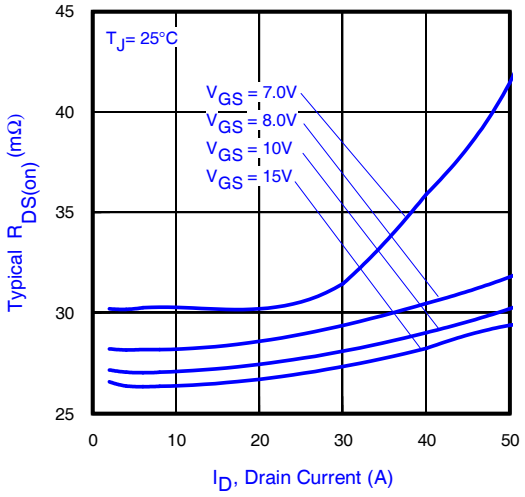


Fig 13. Typical On-Resistance vs. Drain Current

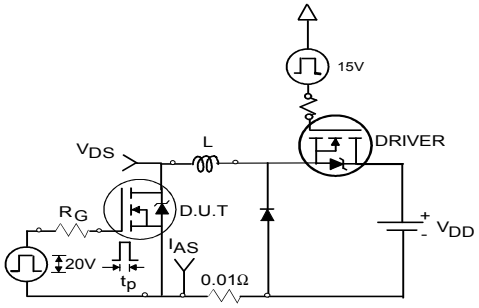


Fig 15a. Unclamped Inductive Test Circuit

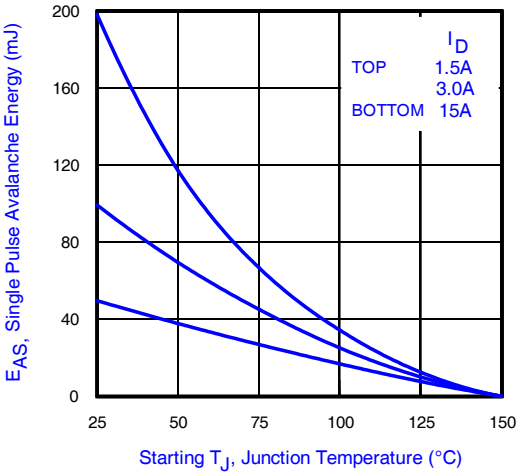


Fig 14. Maximum Avalanche Energy vs. Drain Current

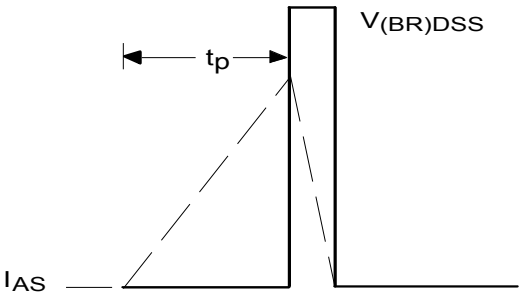


Fig 15b. Unclamped Inductive Waveforms

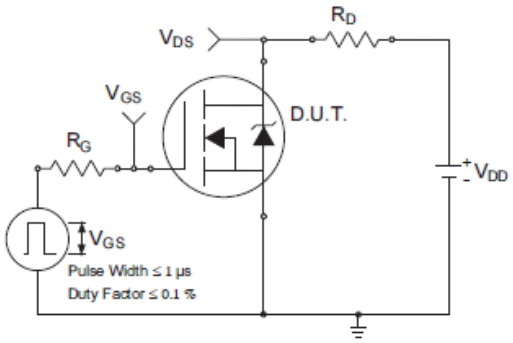


Fig 16a. Switching Time Test Circuit

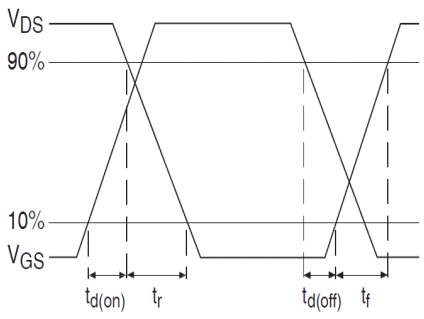


Fig 16b. Switching Time Waveforms

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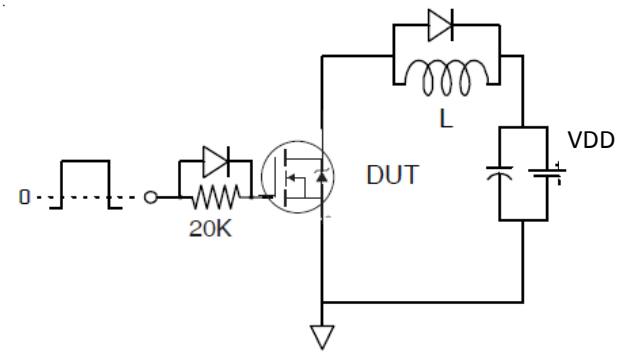


Fig 17a. Gate Charge Test Circuit

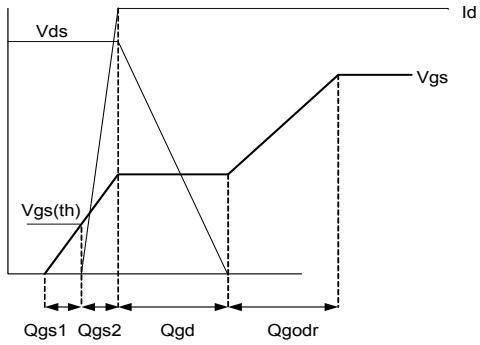


Fig 17b. Gate Charge Waveform

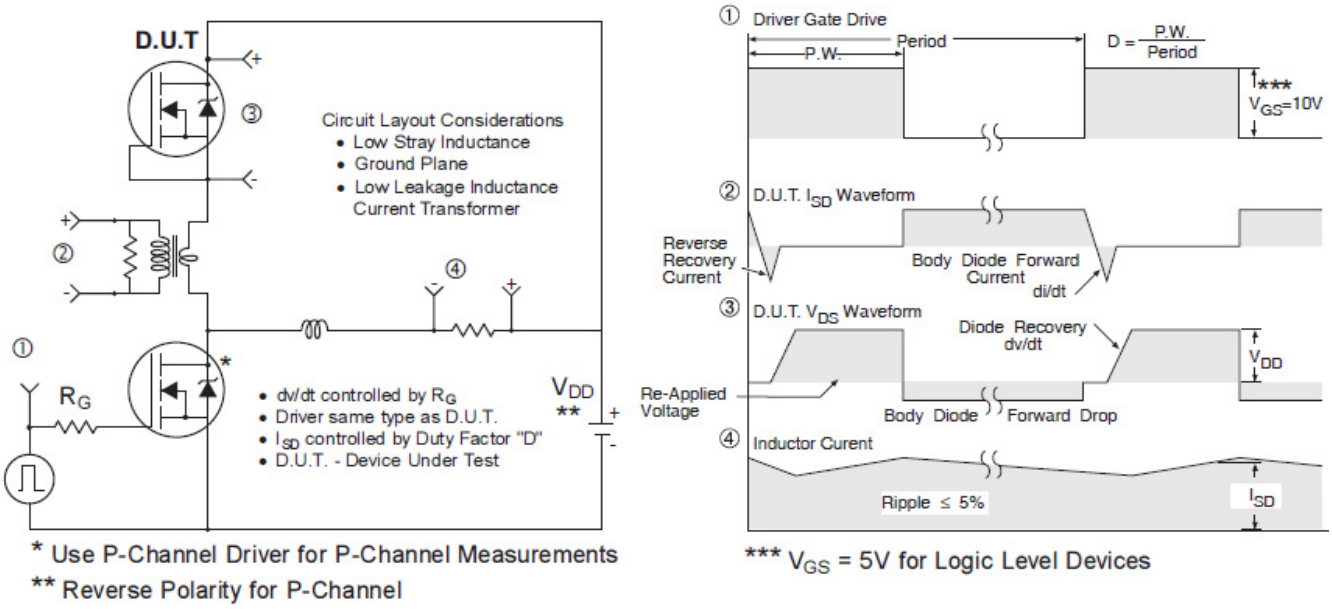
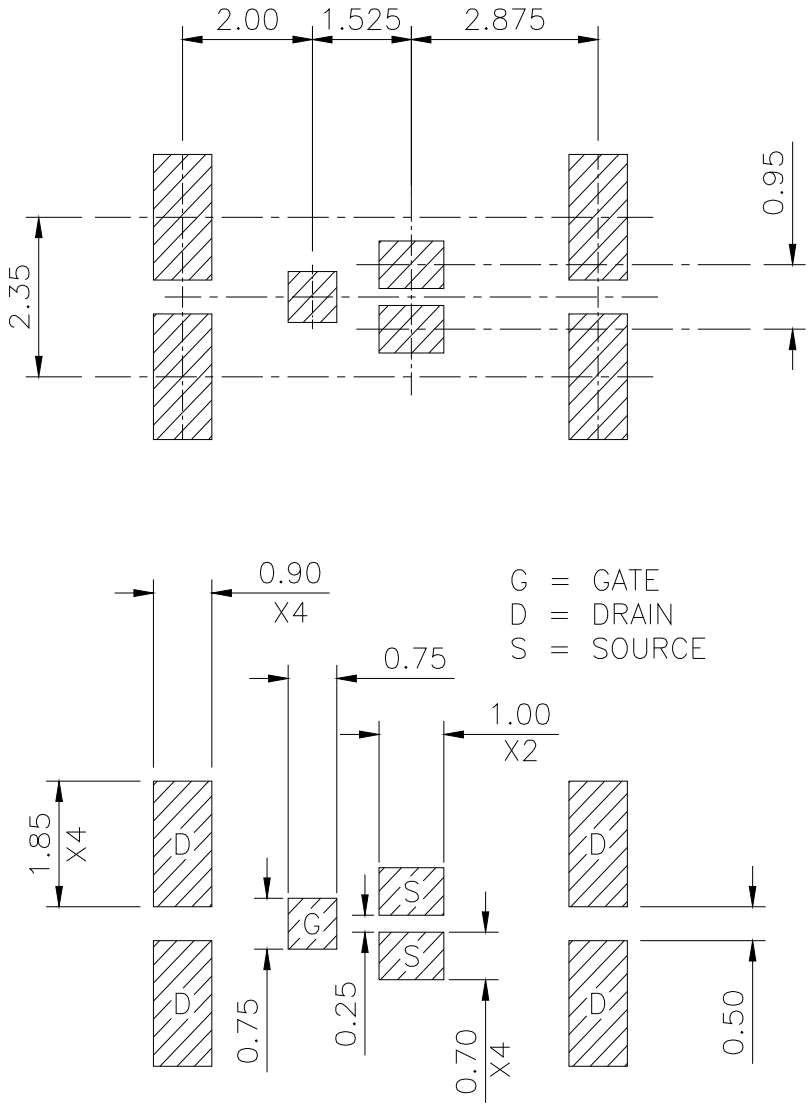


Fig 18. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs

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DirectFET® Substrate and PCB Layout, MZ Outline
(Medium Size Can, Z-Designation).

Please see DirectFET application note AN-1035 for all details regarding the assembly of DirectFET.
This includes all recommendations for stencil and substrate designs.

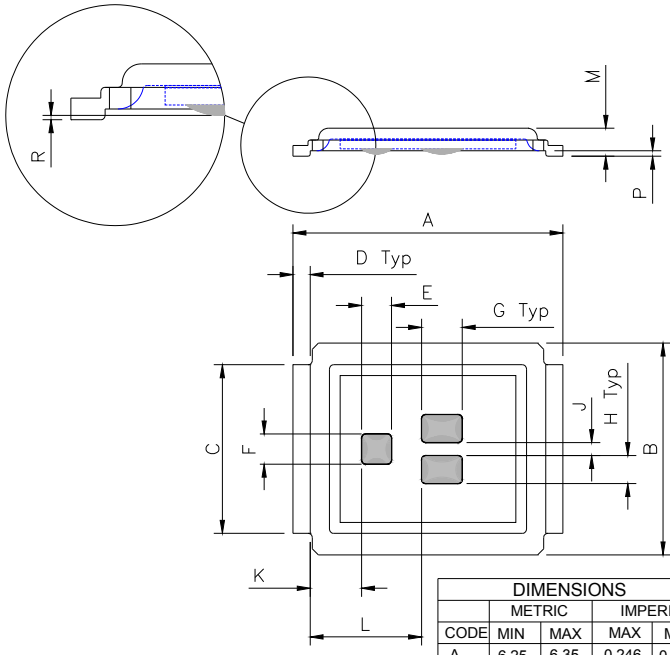


Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

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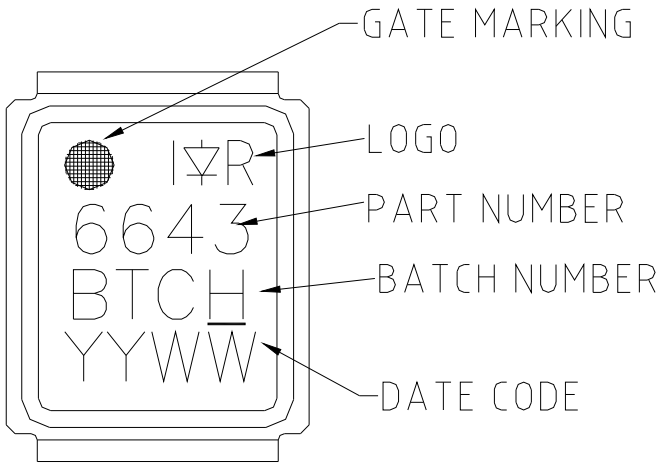
DirectFET® Outline Dimension, MZ Outline
(Medium Size Can, D-Designation).

Please see DirectFET application note AN-1035 for all details regarding the assembly of DirectFET. This includes all recommendations for stencil and substrate designs.



DIMENSIONS				
	METRIC		IMPERIAL	
CODE	MIN	MAX	MAX	MAX
A	6.25	6.35	0.246	0.250
B	4.80	5.05	0.189	0.201
C	3.85	3.95	0.152	0.156
D	0.35	0.45	0.014	0.018
E	0.68	0.72	0.027	0.028
F	0.68	0.72	0.027	0.028
G	0.93	0.97	0.037	0.038
H	0.63	0.67	0.025	0.026
J	0.28	0.32	0.011	0.013
K	1.13	1.26	0.044	0.050
L	2.53	2.66	0.100	0.105
M	0.616	0.676	0.0235	0.0274
R	0.020	0.080	0.0008	0.0031
P	0.08	0.17	0.003	0.007

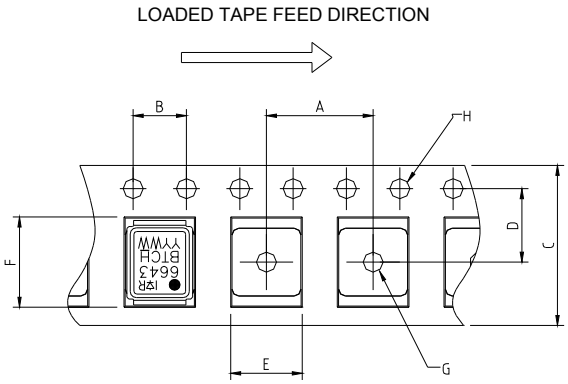
DirectFET® Part Marking



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

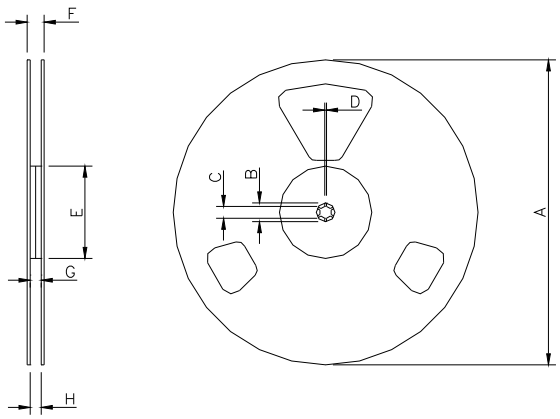
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DirectFET® Tape & Reel Dimension (Showing component orientation).



NOTE: CONTROLLING DIMENSIONS IN MM

DIMENSIONS				
	METRIC		IMPERIAL	
CODE	MIN	MAX	MIN	MAX
A	7.90	8.10	0.311	0.319
B	3.90	4.10	0.154	0.161
C	11.90	12.30	0.469	0.484
D	5.45	5.55	0.215	0.219
E	5.10	5.30	0.201	0.209
F	6.50	6.70	0.256	0.264
G	1.50	N.C	0.059	N.C
H	1.50	1.60	0.059	0.063



NOTE: Controlling dimensions in mm
Std reel quantity is 4800 parts. (ordered as IRF6643TRPBF). For 1000 parts on 7" reel, order IRF6643TR1PBF

REEL DIMENSIONS								
STANDARD OPTION (QTY 4800)					TR1 OPTION (QTY 1000)			
	METRIC		IMPERIAL		METRIC		IMPERIAL	
CODE	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX
A	330.0	N.C	12.992	N.C	177.77	N.C	6.9	N.C
B	20.2	N.C	0.795	N.C	19.06	N.C	0.75	N.C
C	12.8	13.2	0.504	0.520	13.5	12.8	0.53	0.50
D	1.5	N.C	0.059	N.C	1.5	N.C	0.059	N.C
E	100.0	N.C	3.937	N.C	58.72	N.C	2.31	N.C
F	N.C	18.4	N.C	0.724	N.C	13.50	N.C	0.53
G	12.4	14.4	0.488	0.567	11.9	12.01	0.47	N.C
H	11.9	15.4	0.469	0.606	11.9	12.01	0.47	N.C

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

Qualification Information†

Moisture Sensitivity Level	DirectFET	MSL1 (per JEDEC J-STD-020D††)
RoHS Compliant	Yes	

† Qualification standards can be found at International Rectifier's web site http://www.irf.com/product-info/reliability/

†† Applicable version of JEDEC standard at the time of product release.

Notes:

① Repetitive rating; pulse width limited by max. junction temperature.

② Starting T_J = 25°C, L = 0.43mH, R_G = 25Ω, I_{AS} = 7.6A.

③ Surface mounted on 1 in. square Cu board.

④ Pulse width ≤ 400µs; duty cycle ≤ 2%.

⑤ Coss eff. is a fixed capacitance that gives the same charging time as Coss while V_{DS} is rising from 0 to 80% V_{DSS}.

⑥ Used double sided cooling , mounting pad with large heatsink.

⑦ Mounted on minimum footprint full size board with metalized back and with small clip heatsink.

⑧ T_C measured with thermal couple mounted to top (Drain) of part.

⑨ R_θ is measured at T_J of approximately 90°C.

Revision History

Date	Comments
05/30/2013	Converted the data sheet to Class-D Audio formatting template. No change in electrical parameters.

International
IOR Rectifier
IR WORLD HEADQUARTERS: 101 N. Sepulveda Blvd., El Segundo, California 90245, USA
To contact International Rectifier, please visit http://www.irf.com/whoto-call/

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