- **GENERAL PURPOSE SILICON DIODES**
- **METALLURGICALLY BONDED**

CDLL483B
CDLL485B
CDLL486B

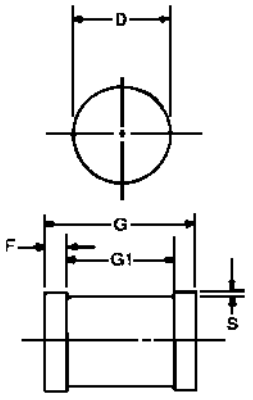
MAXIMUM RATINGS

Operating Temperature: -65°C to +175°C
Storage Temperature: -65°C to +175°C
Operating Current: 200 mA
Derating: 1.2mA/°C from 25ºC to 150ºC
1.0mA/°C from 150ºC to 175ºC
Forward Current: 650mA

ELECTRICAL CHARACTERISTICS @ 25°C, unless otherwise specified

TYPE	V_{RM}	V_{RWM}	I_O	I_O T_A = +150°C	I_{FSM} T_P=1/120 S T_A=25ºC
	$V_{(pk)}$	$V_{(pk)}$	mA	mA	A
CDLL483B	80	70	200	50	2
CDLL485B	180	180	200	50	2
CDLL486B	250	225	200	50	2

TYPE	V_F @100mA	I_{R1} at V_{RWM} T_A=25ºC	I_{R2} at V_{RM} T_A=25ºC	I_{R3} at V_{RWM} T_A = 150ºC
	V dc	nA dc	μA	μA dc
CDLL483B	0.8 - 1.0	25	100	5
CDLL485B	0.8 - 1.0	25	100	5
CDLL486B	0.8 - 1.0	25	100	5



	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
D	1.60	1.70	0.063	0.067
F	0.41	0.55	0.016	0.022
G	3.30	3.70	.130	.146
G1	2.54 REF.		.100 REF.	
S	0.03 MIN.		.001 MIN.	

FIGURE 1

DESIGN DATA

CASE: DO-213AA**,** Hermetically sealed glass case. (MELF, SOD-80, LL34)

LEAD FINISH: Tin / Lead

THERMAL RESISTANCE: ($R_{\Theta JEC}$): 100 ˚C/W maximum

THERMAL IMPEDANCE: ($Z_{\Theta JX}$): 35 ˚C/W maximum

POLARITY: Cathode end is banded.

MOUNTING POSITION: Any.

MOUNTING SURFACE SELECTION: The Axial Coefficient of Expansion (COE) Of this Device is Approximately +6PPM/°C. The COE of the Mounting Surface System Should Be Selected To Provide A Suitable Match With This Device.

COMPENSATED DEVICES INCORPORATED
22 COREY STREET, MELROSE, MASSACHUSETTS 02176
PHONE (781) 665-1071 FAX (781) 665-7379
WEBSITE: http://www.cdi-diodes.com E-mail: mail@cdi-diodes.com

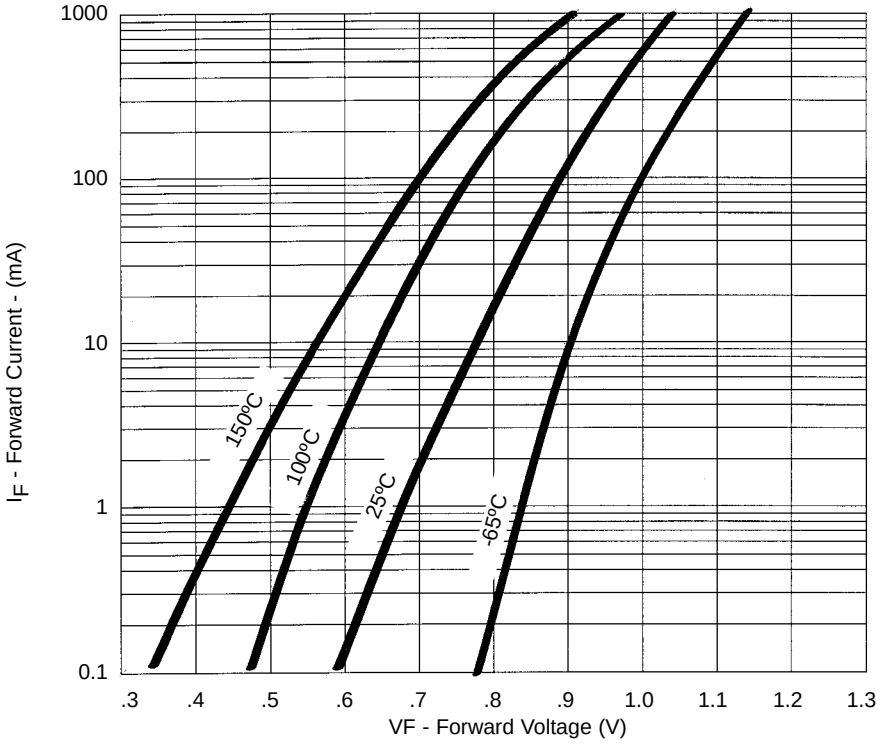
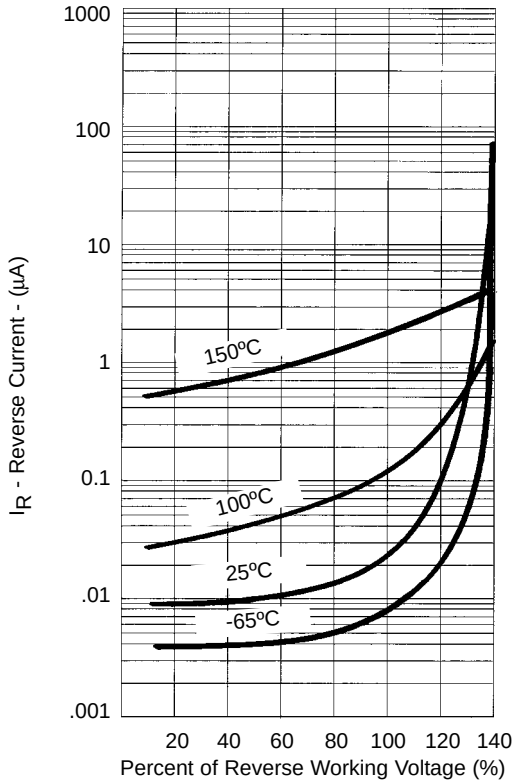


FIGURE 2
Typical Forward Current
vs Forward Voltage



NOTE : All temperatures shown on graphs are junction temperatures

FIGURE 3
Typical Reverse Current
vs Reverse Voltage