

OPA348
OPA2348
OPA4348

SBOS213C – NOVEMBER 2001 – REVISED MAY 2002

1MHz, 45μA, CMOS, Rail-to-Rail OPERATIONAL AMPLIFIERS

FEATURES

- **LOW I_Q: 45μA typical**
- **LOW COST**
- **RAIL-TO-RAIL INPUT AND OUTPUT**
- **SINGLE SUPPLY: +2.1V to +5.5V**
- **INPUT BIAS CURRENT: 0.5pA**
- ***Micro*SIZE PACKAGES: SC70-5, SOT23-8 and TSSOP-14**
- **HIGH SPEED:POWER WITH BANDWIDTH: 1MHz**

APPLICATIONS

- **PORTABLE EQUIPMENT**
- **BATTERY-POWERED EQUIPMENT**
- **SMOKE ALARMS**
- **CO DETECTORS**
- **MEDICAL INSTRUMENTATION**

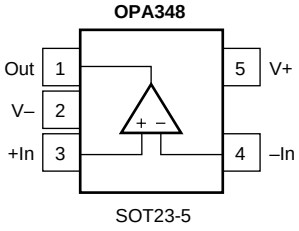
DESCRIPTION

The OPA348 series amplifiers are single supply, low-power, CMOS op amps in micro packaging. Featuring an extended bandwidth of 1MHz, and a supply current of 45μA, the OPA348 series is useful for low-power applications on single supplies of 2.1V to 5.5V.

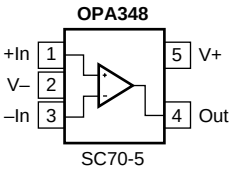
Low supply current of 45μA, and an input bias current of 0.5pA, make the OPA348 series an optimal candidate for low-power, high-impedance applications such as smoke detectors and other sensors.

The OPA348 is available in the miniature SC70-5, SOT23-5 and SO-8 packages. The OPA2348 is available in SOT23-8 and SO-8 packages, and the OPA4348 is offered in space-saving TSSOP-14 and SO-14 packages. The extended temperature range of –40°C to +125°C over all supply voltages offers additional design flexibility.

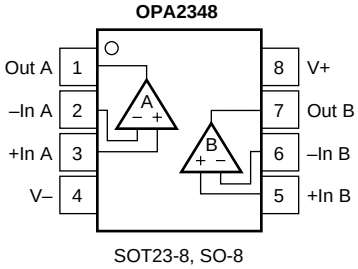
PACKAGES	OPA348	OPA2348	OPA4348
SOT23-5	X		
SOT23-8		X	
SO-8	X	X	
TSSOP-14			X
SO-14			X
SC70-5	X		



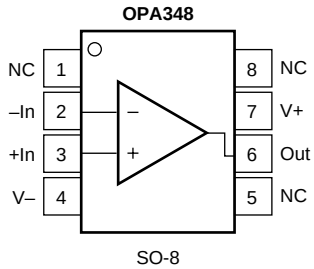
SOT23-5



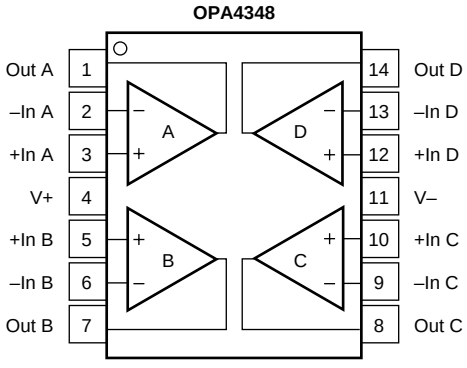
SC70-5



SOT23-8, SO-8



SO-8



TSSOP-14, SO-14

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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

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ABSOLUTE MAXIMUM RATINGS[1]

Supply Voltage, V– to V+	7.5V
Signal Input Terminals, Voltage[2]	(V–) – 0.5V to (V+) + 0.5V
Current[2]	10mA
Output Short-Circuit[3]	Continuous
Operating Temperature	–65°C to +150°C
Storage Temperature	–65°C to +150°C
Junction Temperature	150°C
Lead Temperature (soldering, 10s)	300°C

NOTES: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only. Functional operation of the device at these conditions, or beyond the specified operating conditions, is not implied. (2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less. (3) Short-circuit to ground, one amplifier per package.

ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE-LEAD	PACKAGE DESIGNATOR[1]	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER[2]	TRANSPORT MEDIA, QUANTITY
Single						
OPA348AI	SOT23-5	DBV	–40°C to +125°C	A48	OPA348AIDBVT	Tape and Reel, 250
"	"	"	"	"	OPA348AIDBVR	Tape and Reel, 3000
OPA348AI	SO-8	D	–40°C to +125°C	348A	OPA348AID	Tubes, 100
"	"	"	"	"	OPA348AIDR	Tape and Reel, 2500
OPA348AI	SC70-5	DCK	–40°C to 125°C	S48	OPA348AIDCKT	Tape and Reel, 250
"	"	"	"	"	OPA348AIDCKR	Tape and Reel, 3000
Dual						
OPA2348AI	SOT23-8	DCN	–40°C to +125°C	B48	OPA2348AIDCNT	Tape and Reel, 250
"	"	"	"	"	OPA2348AIDCNR	Tape and Reel, 3000
OPA2348AI	SO-8	D	–40°C to +125°C	2348A	OPA2348AID	Tubes, 100
"	"	"	"	"	OPA2348AIDR	Tape and Reel, 2500
Quad						
OPA4348AI	SO-14	D	–40°C to +125°C	OPA4348	OPA4348AID	Tubes, 58
"	"	"	"	"	OPA4348AIDR	Tape and Reel, 2500
OPA4348AI	TSSOP-14	PW	–40°C to +125°C	4348A	OPA4348AIPWT	Tubes, 250
"	"	"	"	"	OPA4348AIPWR	Tape and Reel, 2500

NOTES: (1) For the most current specifications and package information, refer to our web site at www.ti.com. (2) Models labeled with “T” indicate smaller quantity tape and reel, “R” indicates large quantity tape and reel and “D” indicates tubes of specified quantity.

ELECTRICAL CHARACTERISTICS: V_S = 2.5V to 5.5V

Boldface limits apply over the specified temperature range, T_A = –40°C to +125°C

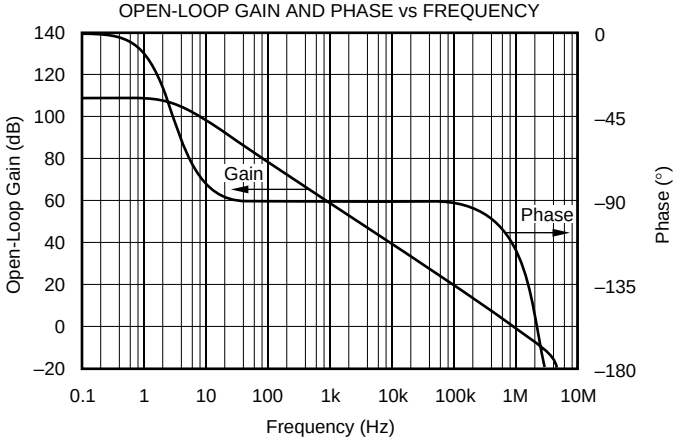
At T_A = +25°C, R_L = 100kΩ connected to V_S/2 and V_{OUT} = V_S/2, unless otherwise noted.

PARAMETER		CONDITION	OPA348 OPA2348 OPA4348 MIN	TYP	MAX	UNITS
OFFSET VOLTAGE						
Input Offset Voltage	V_{OS}	V_S = 5V, V_{CM} = (V–) + 0.8V		1	5	mV
Over Temperature					**6**	**mV**
Drift	**dV_{OS}/dT**			**4**		**µV/°C**
vs Power Supply	PSRR	V_S = 2.5V to 5.5V, V_{CM} < (V+) – 1.7V		60	175	µV/V
Over Temperature		**V_S = 2.5V to 5.5V, V_{CM} < (V+) – 1.7V**			**300**	**µV/V**
Channel Separation, dc				0.2		µV/V
f = 1kHz				134		dB
INPUT VOLTAGE RANGE						
Common-Mode Voltage Range	V_{CM}		(V–) – 0.2		(V+) + 0.2	V
Common-Mode Rejection Ratio	CMRR	(V–) – 0.2V < V_{CM} < (V+) – 1.7V	70	82		dB
over Temperature		**(V–) < V_{CM} < (V+) – 1.7V**	**66**			**dB**
		V_S = 5.5V, (V–) – 0.2V < V_{CM} < (V+) + 0.2V	60	71		dB
over Temperature		**V_S = 5.5V, (V–) < V_{CM} < (V+)**	**56**			**dB**
INPUT BIAS CURRENT						
Input Bias Current	I_B			±0.5	±10	pA
Input Offset Current	I_{OS}			±0.5	±10	pA
INPUT IMPEDANCE						
Differential				10^{13} || 3		Ω || pF
Common-Mode				10^{13} || 6		Ω || pF
NOISE		V_{CM} < (V+) – 1.7V				
Input Voltage Noise, f = 0.1Hz to 10Hz				10		µVp-p
Input Voltage Noise Density, f = 1kHz	e_n			35		nV/√Hz
Input Current Noise Density, f = 1kHz	i_n			4		fA/√Hz
OPEN-LOOP GAIN						
Open-Loop Voltage Gain	A_{OL}	V_S = 5V, R_L = 100kΩ, 0.025V < V_O < 4.975V	94	108		dB
over Temperature		**V_S = 5V, R_L = 100kΩ, 0.025V < V_O < 4.975V**	**90**			**dB**
		V_S = 5V, R_L = 5kΩ, 0.125V < V_O < 4.875V	90	98		dB
over Temperature		**V_S = 5V, R_L = 5kΩ, 0.125V < V_O < 4.875V**	**88**			**dB**
OUTPUT						
Voltage Output Swing from Rail		R_L = 100kΩ, A_{OL} > 94dB		18	25	mV
over Temperature		**R_L = 100kΩ, A_{OL} > 90dB**			**25**	**mV**
		R_L = 5kΩ, A_{OL} > 90dB		100	125	mV
over Temperature		**R_L = 5kΩ, A_{OL} > 88dB**			**125**	**mV**
Short-Circuit Current	I_{SC}			±10		mA
Capacitive Load Drive	C_{LOAD}		See Typical Characteristics			
FREQUENCY RESPONSE		C_L = 100pF				
Gain-Bandwidth Product	GBW			1		MHz
Slew Rate	SR	G = +1		0.5		V/µs
Settling Time, 0.1%	t_S	V_S = 5.5V, 2V Step, G = +1		5		µs
0.01%		V_S = 5.5V, 2V Step, G = +1		7		µs
Overload Recovery Time		V_{IN} • Gain > V_S		1.6		µs
Total Harmonic Distortion + Noise	THD+N	V_S = 5.5V, V_O = 3Vp-p, G = +1, f = 1kHz		0.0023		%
POWER SUPPLY						
Specified Voltage Range	V_S		2.5		5.5	V
Minimum Operating Voltage				2.1 to 5.5		V
Quiescent Current (per amplifier)	I_Q	I_O = 0		45	65	µA
over Temperature					**75**	**µA**
TEMPERATURE RANGE						
Specified Range			–40		125	°C
Operating Range			–65		150	°C
Storage Range			–65		150	°C
Thermal Resistance	θ_{JA}					
SOT23-5 Surface-Mount				200		°C/W
SOT23-8 Surface-Mount				150		°C/W
MSOP-8 Surface-Mount				150		°C/W
SO-8 Surface-Mount				150		°C/W
SO-14 Surface-Mount				100		°C/W
TSSOP-14 Surface-Mount				100		°C/W
SC70-5 Surface-Mount				250		°C/W

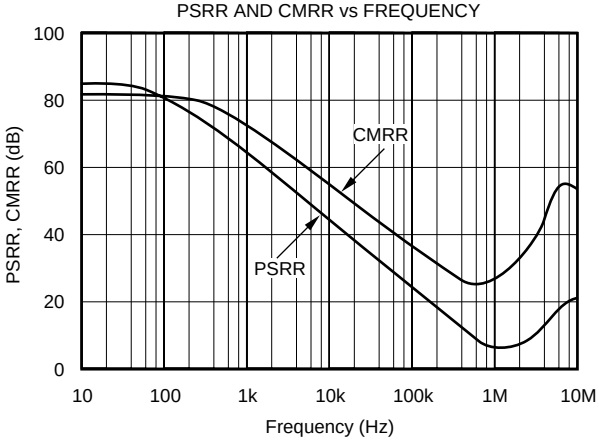
TYPICAL CHARACTERISTICS

At T_A = +25°C, R_L = 100kΩ connected to V_S / 2 and V_{OUT} = V_S / 2, unless otherwise noted.

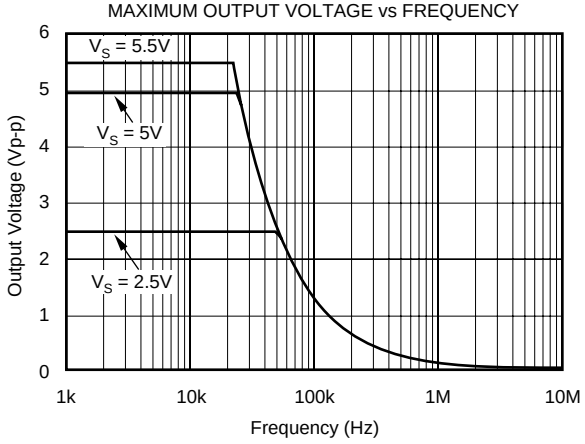
OPEN-LOOP GAIN AND PHASE vs FREQUENCY



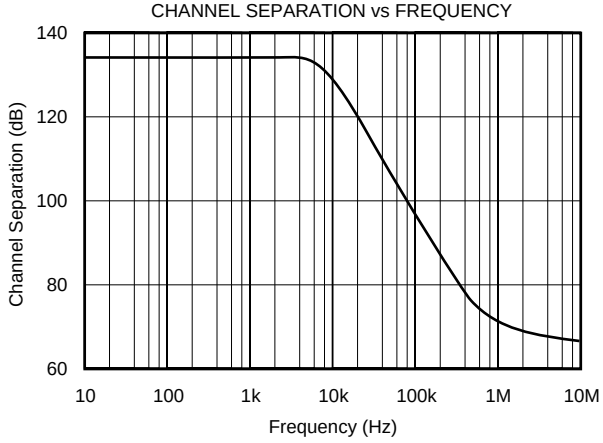
PSRR AND CMRR vs FREQUENCY



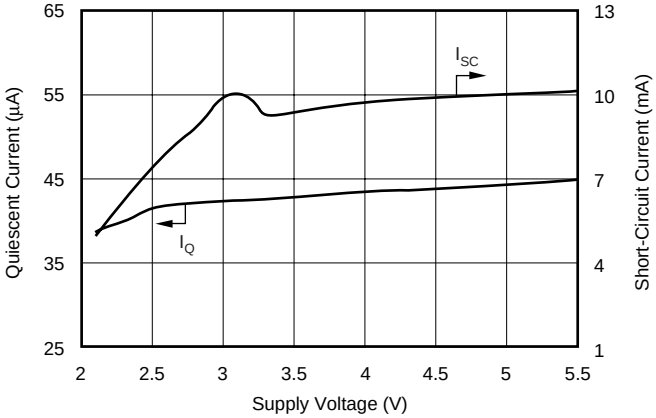
MAXIMUM OUTPUT VOLTAGE vs FREQUENCY



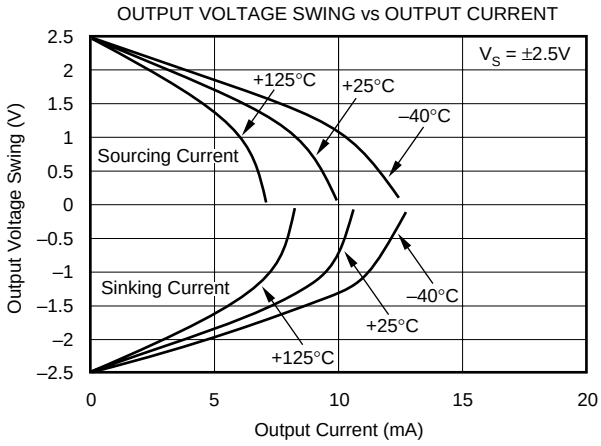
CHANNEL SEPARATION vs FREQUENCY



QUIESCENT AND SHORT-CIRCUIT CURRENT vs SUPPLY VOLTAGE



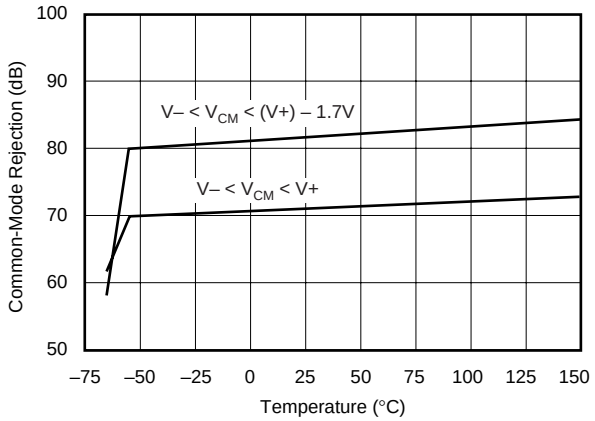
OUTPUT VOLTAGE SWING vs OUTPUT CURRENT



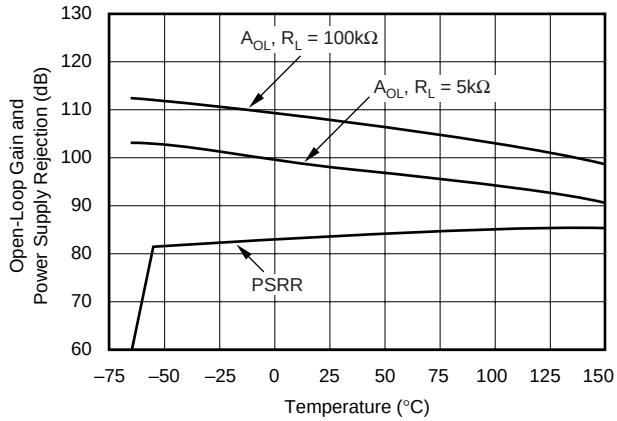
TYPICAL CHARACTERISTICS (Cont.)

At T_A = +25°C, R_L = 100kΩ connected to V_S / 2 and V_{OUT} = V_S / 2, unless otherwise noted.

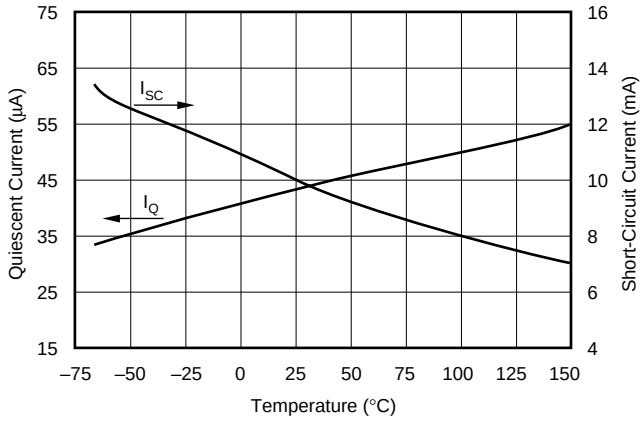
COMMON-MODE REJECTION vs TEMPERATURE



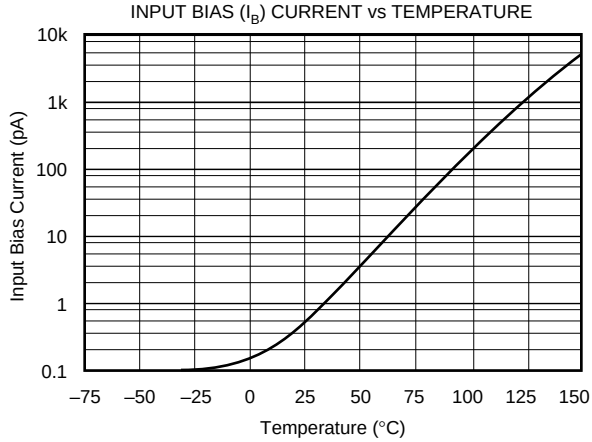
OPEN-LOOP GAIN AND PSRR vs TEMPERATURE



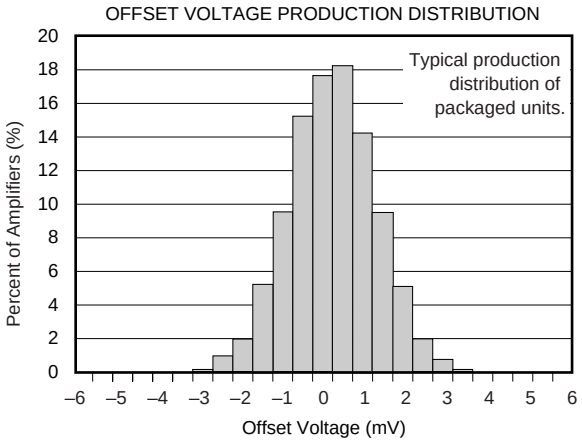
QUIESCENT AND SHORT-CIRCUIT CURRENT vs TEMPERATURE



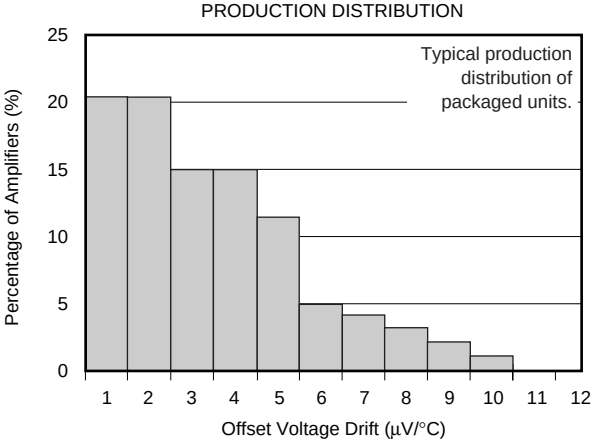
INPUT BIAS (I_B) CURRENT vs TEMPERATURE



OFFSET VOLTAGE PRODUCTION DISTRIBUTION



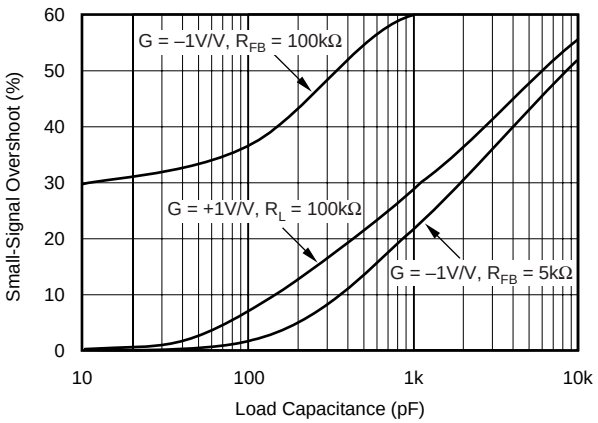
OFFSET VOLTAGE DRIFT MAGNITUDE PRODUCTION DISTRIBUTION



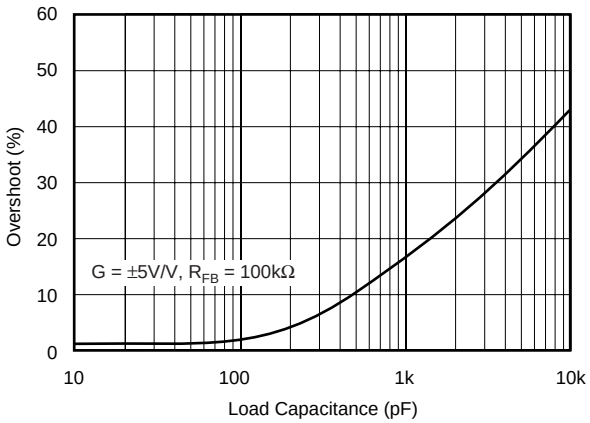
TYPICAL CHARACTERISTICS (Cont.)

At T_A = +25°C, R_L = 100kΩ connected to V_S / 2 and V_{OUT} = V_S / 2, unless otherwise noted.

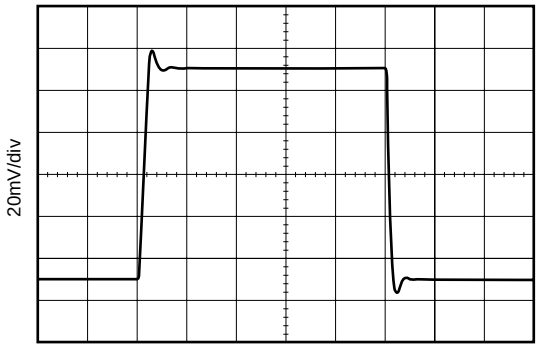
SMALL-SIGNAL OVERSHOOT
vs LOAD CAPACITANCE



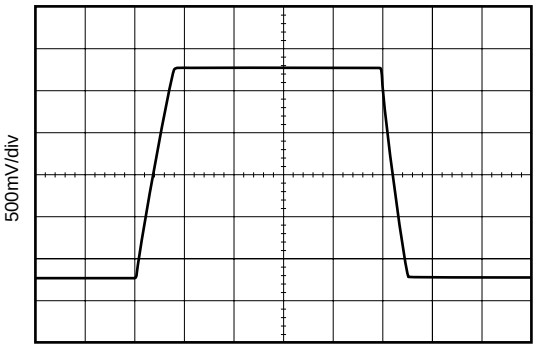
PERCENT OVERSHOOT vs LOAD CAPACITANCE



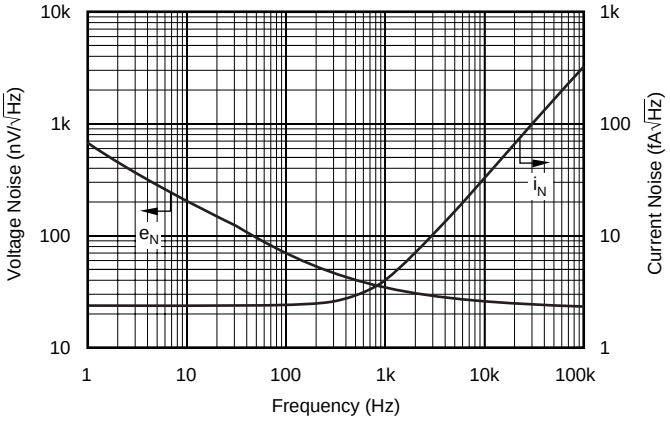
SMALL-SIGNAL STEP RESPONSE
G = +1V/V, R_L = 100kΩ, C_L = 100pF



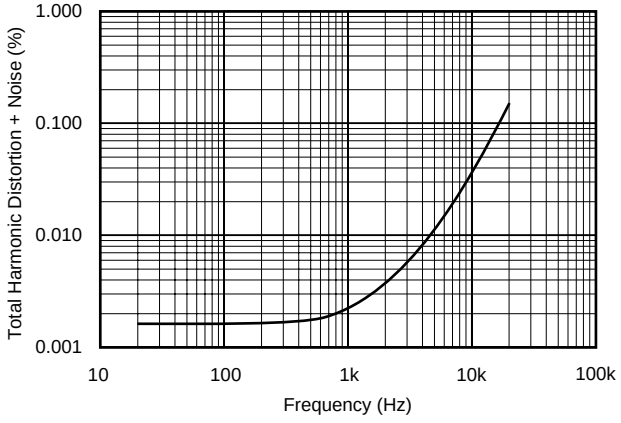
LARGE-SIGNAL STEP RESPONSE
G = +1V/V, R_L = 100kΩ, C_L = 100pF



INPUT CURRENT AND VOLTAGE NOISE
SPECTRAL DENSITY vs FREQUENCY



TOTAL HARMONIC DISTORTION + NOISE
vs FREQUENCY



APPLICATIONS INFORMATION

OPA348 series op amps are unity-gain stable and suitable for a wide range of general-purpose applications.

The OPA348 series features wide bandwidth and unity-gain stability with rail-to-rail input and output for increased dynamic range. Figure 1 shows the input and output waveforms for the OPA348 in unity-gain configuration. Operation is from a single +5V supply with a 100kΩ load connected to $V_S/2$. The input is a 5Vp-p sinusoid. Output voltage is approximately 4.98Vp-p.

Power-supply pins should be bypassed with 0.01µF ceramic capacitors.

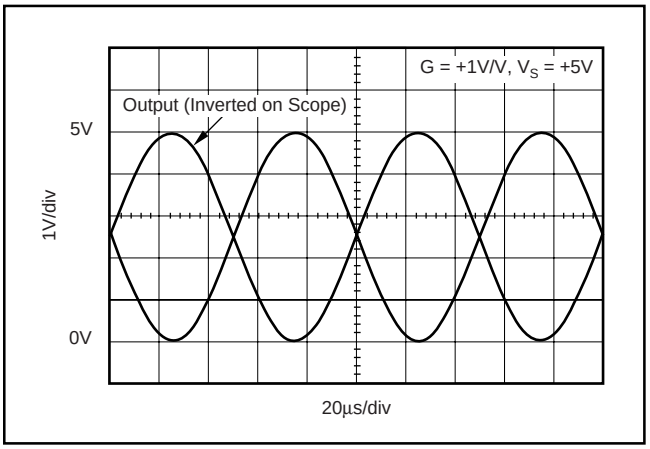


FIGURE 1. The OPA348 Features Rail-to-Rail Input/Output.

OPERATING VOLTAGE

OPA348 series op amps are fully specified and tested from +2.5V to +5.5V. However, supply voltage may range from +2.1V to +5.5V. Parameters are tested over the specified supply range—a unique feature of the OPA348 series. In addition, all temperature specifications apply from –40°C to +125°C. Most behavior remains virtually unchanged throughout the full operating voltage range. Parameters that vary significantly with operating voltages or temperature are shown in the Typical Characteristics.

COMMON-MODE VOLTAGE RANGE

The input common-mode voltage range of the OPA348 series extends 200mV beyond the supply rails. This is achieved with a complementary input stage—an N-channel input differential pair in parallel with a P-channel differential pair. The N-channel pair is active for input voltages close to the positive rail, typically (V+) – 1.2V to 300mV above the positive supply, while the P-channel pair is on for inputs from 300mV below the negative supply to approximately (V+) – 1.4V. There is a small transition region, typically (V+) – 1.4V to (V+) – 1.2V, in which both pairs are on. This 200mV transition region, shown in Figure 2, can vary ±300mV with process variation. Thus, the transition region (both stages on) can range from (V+) – 1.7V to (V+) – 1.5V on the low end, up to (V+) – 1.1V to (V+) – 0.9V on the high end. Within the 200mV transition region PSRR, CMRR, offset voltage, offset drift, and THD may be degraded compared to operation outside this region.

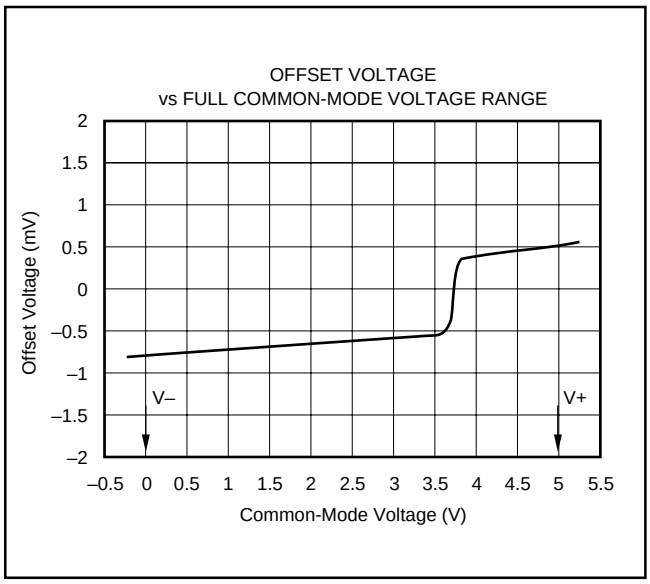


FIGURE 2. Behavior of Typical Transition Region at Room Temperature.

RAIL-TO-RAIL INPUT

The input common-mode range extends from (V–) – 0.2V to (V+) + 0.2V. For normal operation, inputs should be limited to this range. The absolute maximum input voltage is 500mV beyond the supplies. Inputs greater than the input common-mode range but less than the maximum input voltage, while not valid, will not cause any damage to the op amp. Unlike some other op amps, if input current is limited the inputs may go beyond the power supplies without phase inversion, as shown in Figure 3.

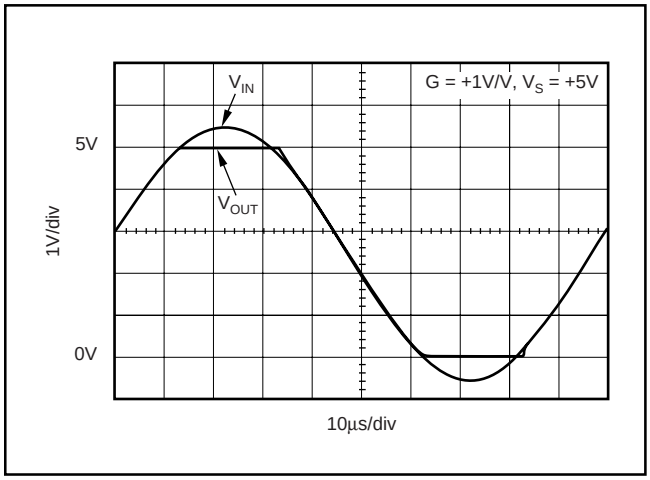


FIGURE 3. OPA348—No Phase Inversion with Inputs Greater than the Power-Supply Voltage.

Normally, input currents are 0.5pA. However, large inputs (greater than 500mV beyond the supply rails) can cause excessive current to flow in or out of the input pins. Therefore, as well as keeping the input voltage below the maximum rating, it is also important to limit the input current to less than 10mA. This is easily accomplished with an input voltage resistor, as shown in Figure 4.

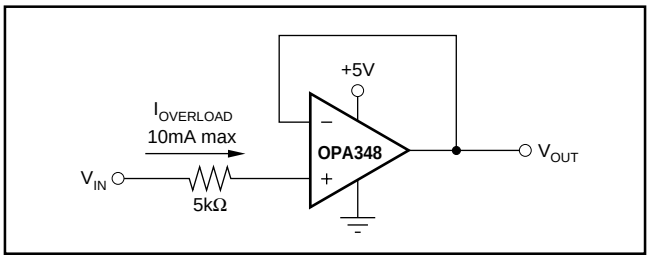


FIGURE 4. Input Current Protection for Voltages Exceeding the Supply Voltage.

RAIL-TO-RAIL OUTPUT

A class AB output stage with common-source transistors is used to achieve rail-to-rail output. This output stage is capable of driving 5kΩ loads connected to any potential between V+ and ground. For light resistive loads (> 100kΩ), the output voltage can typically swing to within 18mV from supply rail. With moderate resistive loads (10kΩ to 50kΩ), the output voltage can typically swing to within 100mV of the supply rails while maintaining high open-loop gain (see the typical characteristic “Output Voltage Swing vs Output Current”).

CAPACITIVE LOAD AND STABILITY

The OPA348 in a unity-gain configuration can directly drive up to 250pF pure capacitive load. Increasing the gain enhances the amplifier’s ability to drive greater capacitive loads (see the typical characteristic “Small-Signal Overshoot vs Capacitive Load”). In unity-gain configurations, capacitive load drive can be improved by inserting a small (10Ω to 20Ω) resistor, R_S, in series with the output, as shown in Figure 5. This significantly reduces ringing while maintaining DC performance for purely capacitive loads. However, if there is a resistive load in parallel with the capacitive load, a voltage divider is created, introducing a Direct Current (DC) error at the output and slightly reducing the output swing. The error introduced is proportional to the ratio R_S/R_L, and is generally negligible.

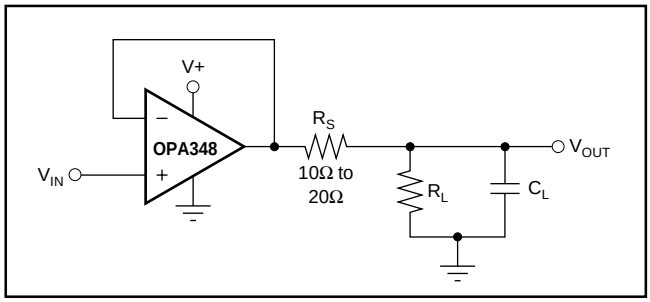


FIGURE 5. Series Resistor in Unity-Gain Buffer Configuration Improves Capacitive Load Drive.

In unity-gain inverter configuration, phase margin can be reduced by the reaction between the capacitance at the op amp input, and the gain setting resistors, thus degrading capacitive load drive. Best performance is achieved by using small valued resistors. For example, when driving a 500pF load, reducing the resistor values from 100kΩ to 5kΩ decreases overshoot from 55% to 13% (see the typical characteristic “Small-Signal Overshoot vs. Load Capacitance”). However, when large valued resistors cannot be avoided, a small (4pF to 6pF) capacitor, C_{FB}, can be inserted in the feedback, as shown in Figure 6. This significantly reduces overshoot by compensating the effect of capacitance, C_{IN}, which includes the amplifier's input capacitance and PC board parasitic capacitance.

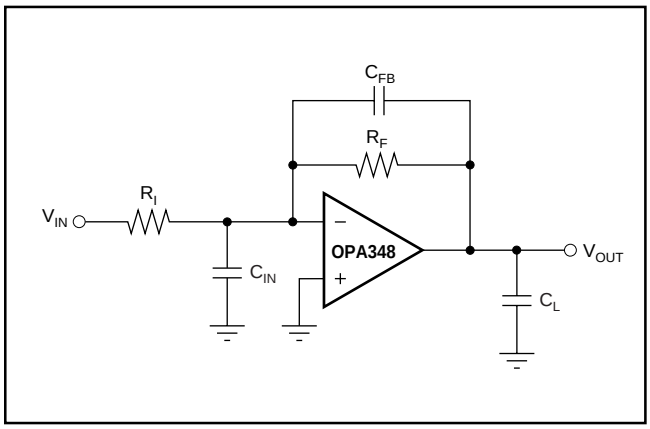


FIGURE 6. Improving Capacitive Load Drive.

DRIVING A/D CONVERTERS

The OPA348 series op amps are optimized for driving medium-speed sampling Analog-to-Digital Converters (ADCs). The OPA348 op amps buffer the ADCs input capacitance and resulting charge injection while providing signal gain.

The OPA348 in a basic noninverting configuration driving the ADS7822, see Figure 7. The ADS7822 is a 12-bit, *micro*POWER sampling converter in the MSOP-8 package. When used with the low-power, miniature packages of the OPA348, the combination is ideal for space-limited, low-power applications. In this configuration, an RC network at the ADC’s input can be used to provide for anti-aliasing filter and charge injection current.

The OPA348 in noninverting configuration driving ADS7822 limited, low-power applications. In this configuration, an RC network at the ADC’s input can be used to provide for anti-aliasing filter and charge injection current. See Figure 8 for the OPA2348 driving an ADS7822 in a speech bandpass filtered data acquisition system. This small, low-cost solution provides the necessary amplification and signal conditioning to interface directly with an electret microphone. This circuit will operate with V_S = 2.7V to 5V with less than 250µA typical quiescent current.

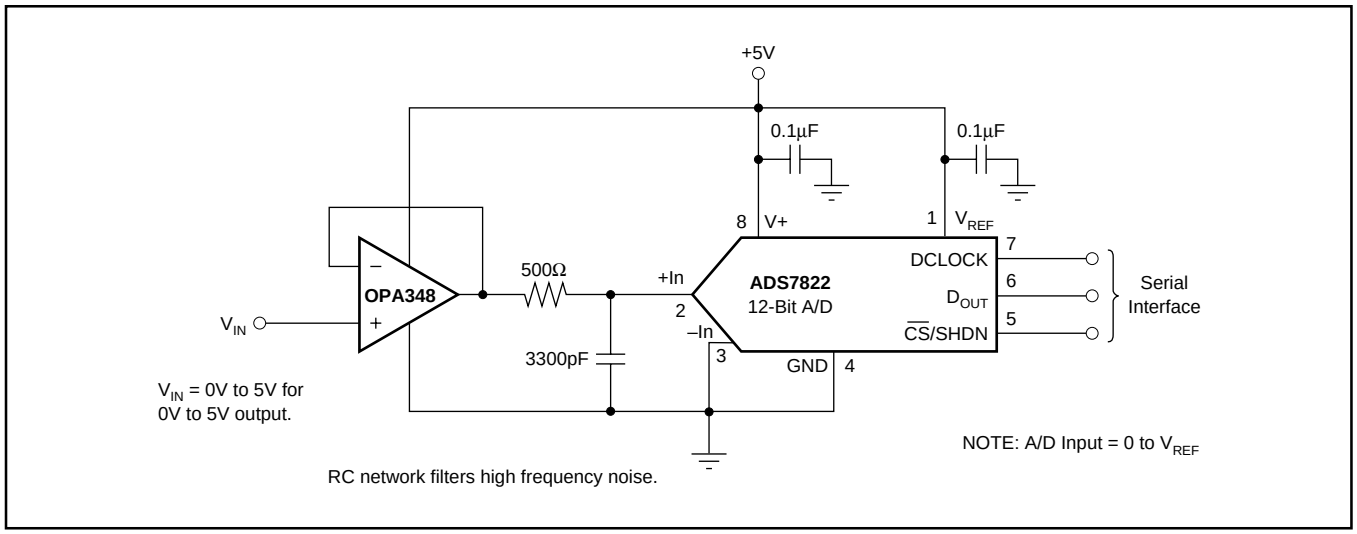


FIGURE 7. OPA348 in Noninverting Configuration Driving ADS7822.

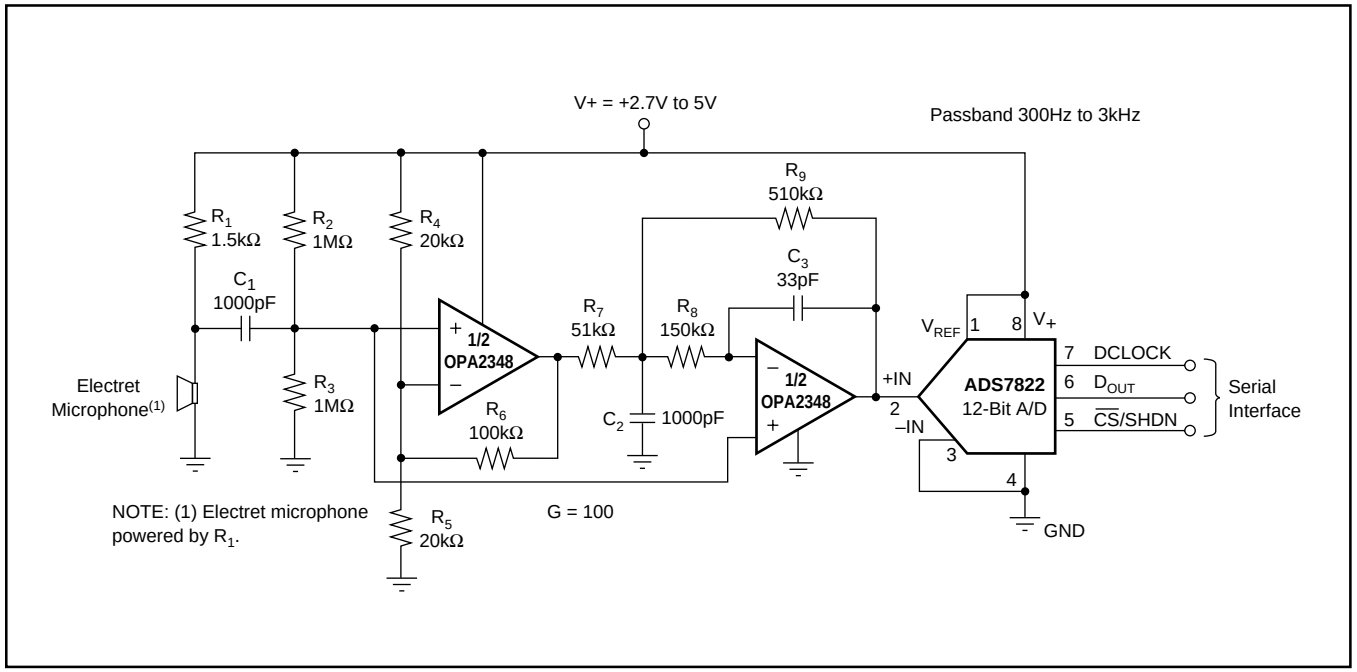
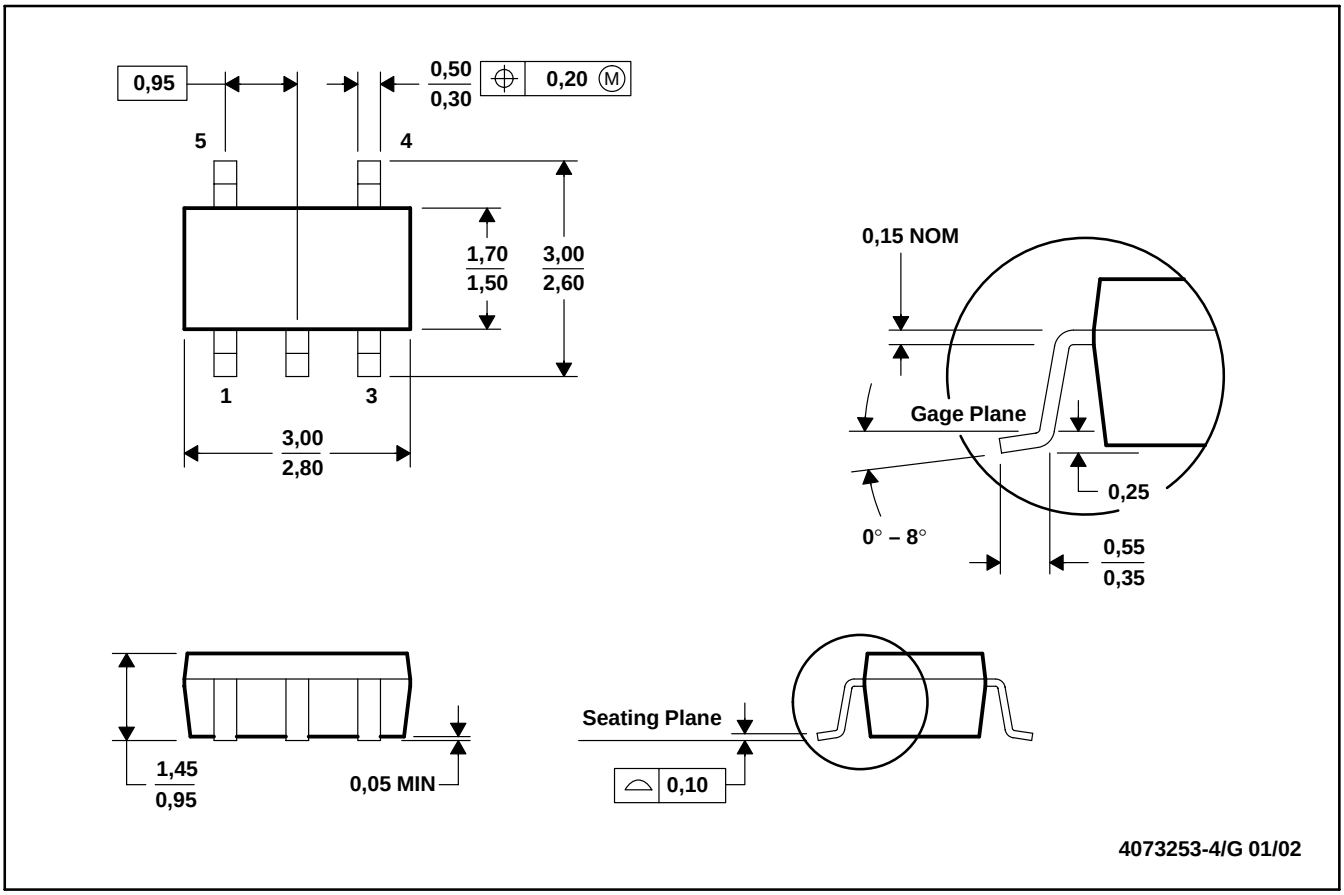


FIGURE 8. OPA2348 as a Speech Bandpass Filtered Data Acquisition System.

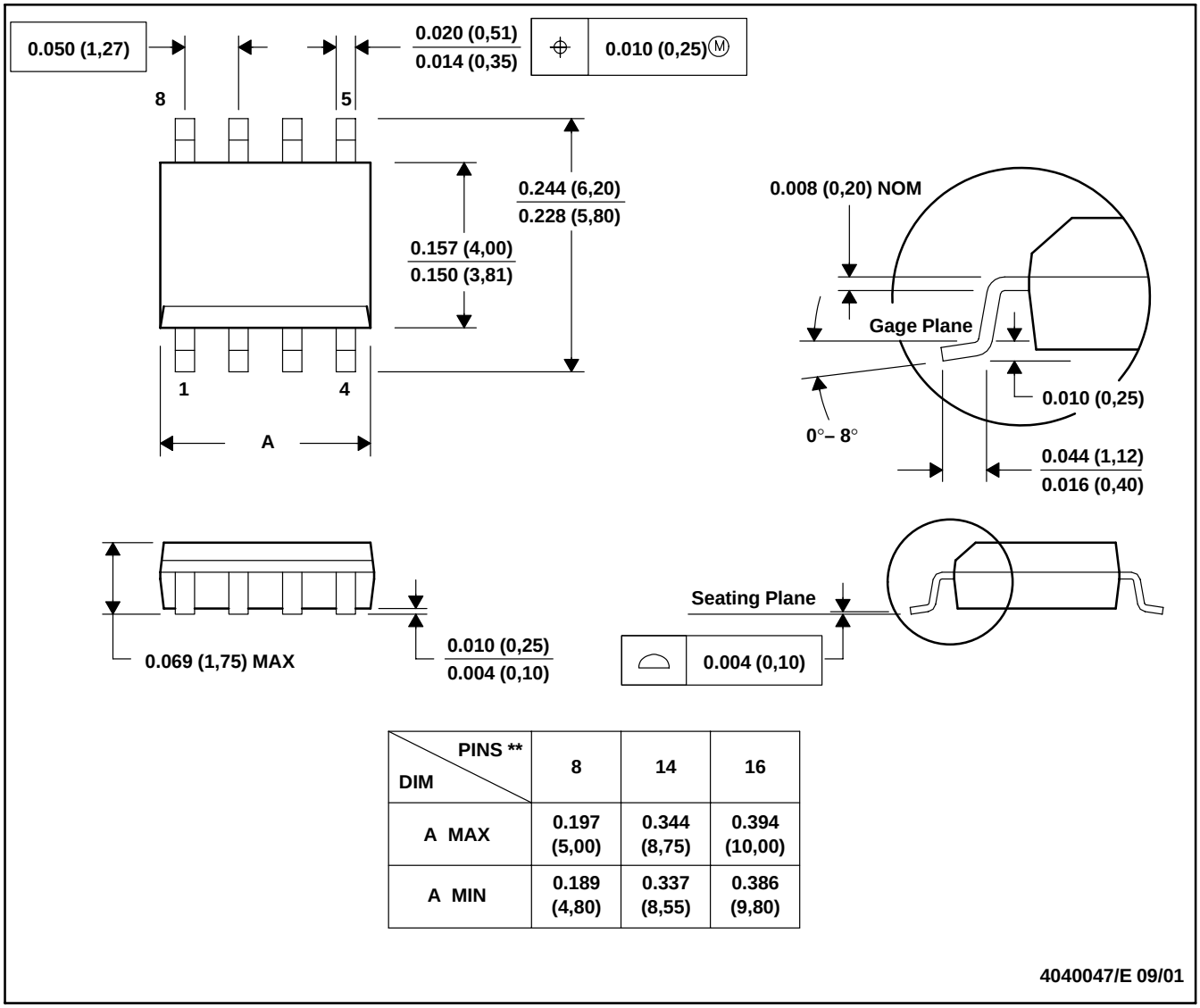
DBV (R-PDSO-G5) **PLASTIC SMALL-OUTLINE**



NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion.
D. Falls within JEDEC MO-178

D (R-PDSO-G)** **PLASTIC SMALL-OUTLINE PACKAGE**

8 PINS SHOWN

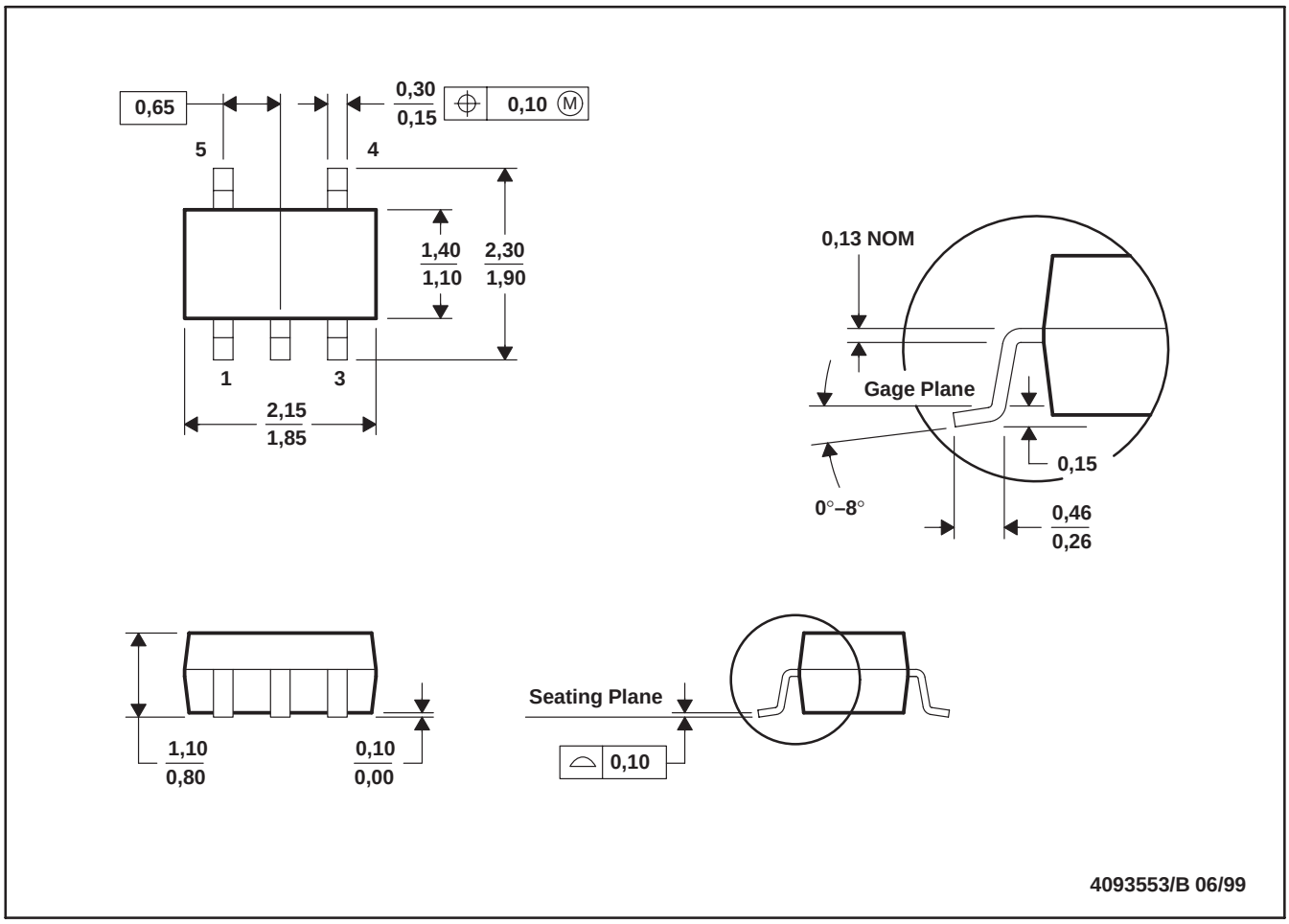


DIM \ PINS **	8	14	16
A MAX	0.197 (5,00)	0.344 (8,75)	0.394 (10,00)
A MIN	0.189 (4,80)	0.337 (8,55)	0.386 (9,80)

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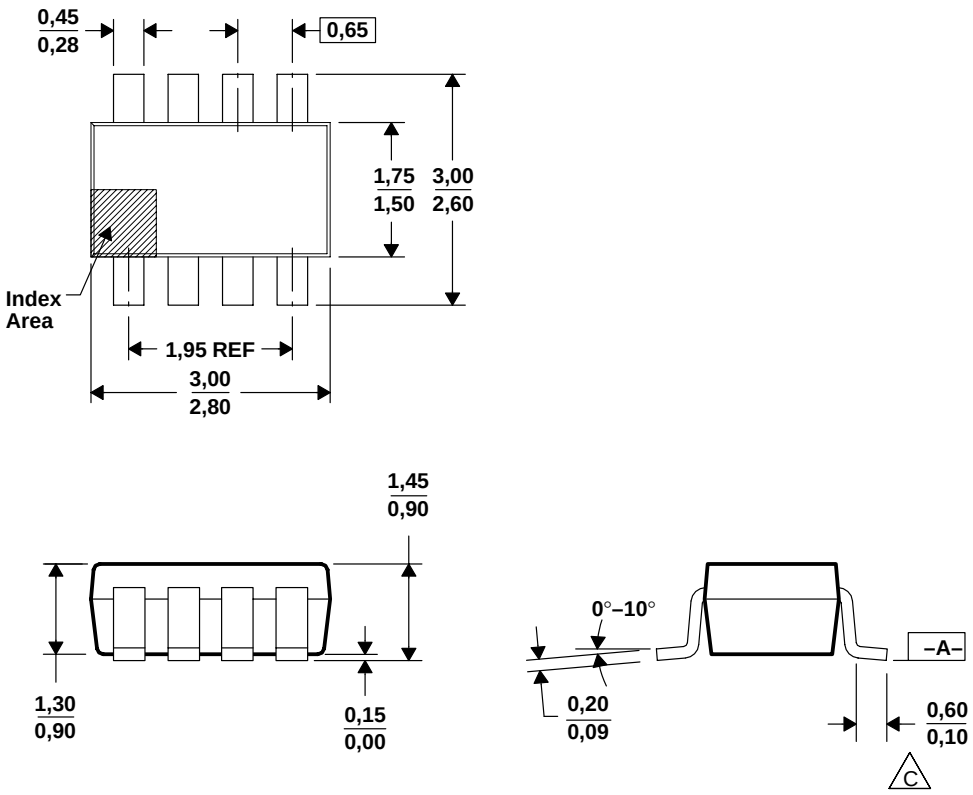
NOTES: A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion, not to exceed 0.006 (0,15).
D. Falls within JEDEC MS-012

DCK (R-PDSO-G5) **PLASTIC SMALL-OUTLINE**



NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion.
D. Falls within JEDEC MO-203

DCN (R-PDSO-G8) **PLASTIC SMALL-OUTLINE**

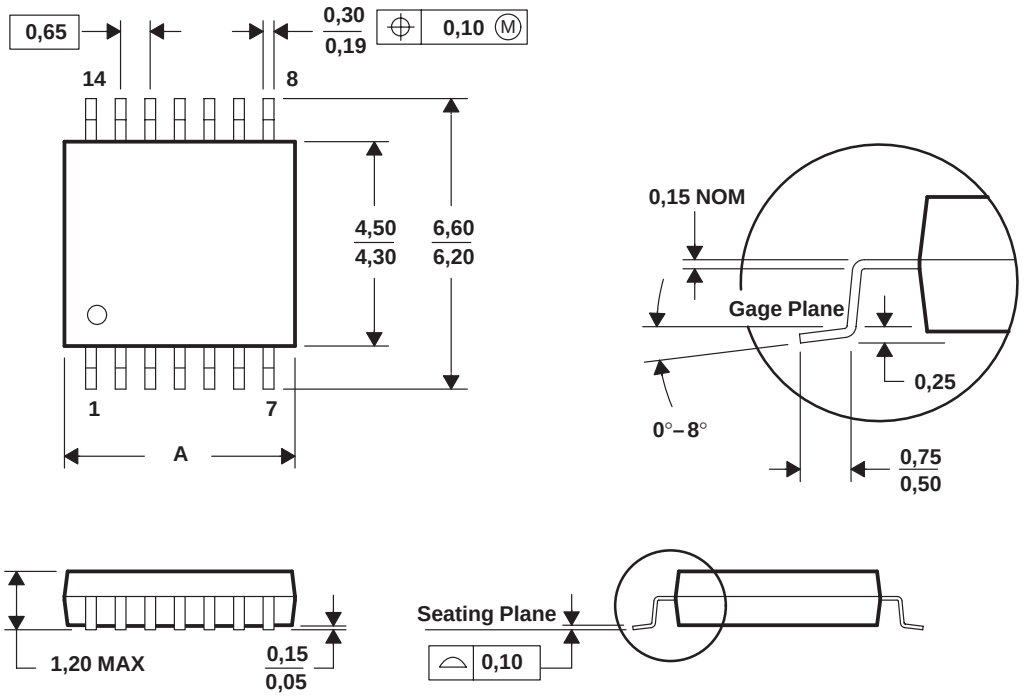


4202106/A 03/01

NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Foot length measured reference to flat foot surface parallel to Datum A.
D. Package outline exclusive of mold flash, metal burr and dambar protrusion/intrusion.
E. Package outline inclusive of solder plating.
F. A visual index feature must be located within the cross-hatched area.

PW (R-PDSO-G)** **PLASTIC SMALL-OUTLINE PACKAGE**

14 PINS SHOWN



DIM \ PINS **	8	14	16	20	24	28
A MAX	3,10	5,10	5,10	6,60	7,90	9,80
A MIN	2,90	4,90	4,90	6,40	7,70	9,60

4040064/F 01/97

NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
D. Falls within JEDEC MO-153

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)
OPA2348AID	ACTIVE	SOIC	D	8	100	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA2348AIDCNR	ACTIVE	SOT-23	DCN	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA2348AIDCNRG4	ACTIVE	SOT-23	DCN	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA2348AIDCNT	ACTIVE	SOT-23	DCN	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA2348AIDCNTG4	ACTIVE	SOT-23	DCN	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA2348AIDG4	ACTIVE	SOIC	D	8	100	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA2348AIDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA2348AIDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA348AID	ACTIVE	SOIC	D	8	100	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA348AIDBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA348AIDBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA348AIDBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA348AIDBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA348AIDCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA348AIDCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA348AIDCKT	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA348AIDCKTG4	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
OPA348AIDG4	ACTIVE	SOIC	D	8	100	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA348AIDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA348AIDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA4348AID	ACTIVE	SOIC	D	14	58	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA4348AIDG4	ACTIVE	SOIC	D	14	58	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA4348AIDR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA4348AIDRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA4348AIPWR	ACTIVE	TSSOP	PW	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)
OPA4348AIPWRG4	ACTIVE	TSSOP	PW	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA4348AIPWT	ACTIVE	TSSOP	PW	14	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
OPA4348AIPWTG4	ACTIVE	TSSOP	PW	14	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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