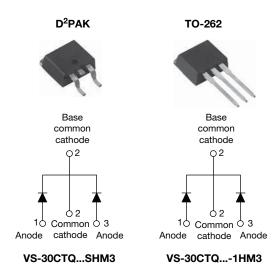
VS-30CTQ...SHM3, VS-30CTQ...-1HM3 Series

Vishay Semiconductors

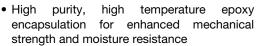
High Performance Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY							
I _{F(AV)}	2 x 15 A						
V_{R}	35 V to 45 V						
V _F at I _F	0.56 V						
I _{RM}	15 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	20 mJ						
Package	TO-263AB (D ² PAK), TO-262AA						
Diode variation	Common cathode						

FEATURES

- 175 °C T_J operation
- Center tap configuration
- · Very low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD-201 class 1A whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-30CTQ... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL CHARACTERISTICS VALUES U										
I _{F(AV)}	Rectangular waveform	30	А							
V_{RRM}		35 to 45	V							
I _{FSM}	t _p = 5 μs sine	1060	Α							
V _F	15 A _{pk} , T _J = 125 °C (per leg)	0.56	V							
T _J	Range	-55 to +175	°C							

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS-30CTQ035SHM3 VS-30CTQ035-1HM3	VS-30CTQ040SHM3 VS-30CTQ040-1HM3	VS-30CTQ045SHM3 VS-30CTQ045-1HM3	UNITS					
Maximum DC reverse voltage	V_{R}	35	40	45	V					
Maximum working peak reverse voltage	V_{RWM}	33	40	43	V					



VS-30CTQ...SHM3, VS-30CTQ...-1HM3 Series

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ABSOLUTE MAXIMUM RATINGS											
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS						
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 127 °C	30								
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load	1060	Α						
non-repetitive surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	265							
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3.0 \text{A}, L = 4.40 \text{C}$	20	mJ							
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim		3.0	Α						

ELECTRICAL SPECIFICATIONS											
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS						
		15 A	T _{.1} = 25 °C	0.62							
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	1J=25 C	0.76	V						
See fig. 1	VFM (1)	15 A	T _J = 125 °C	0.56							
		30 A	1J = 125 C	0.70							
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	2	mA						
See fig. 2	IRM (")	$T_J = 125 ^{\circ}\text{C}$		15	MA						
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range	900	pF							
Typical series inductance per leg	L _S	Measured lead to lead 5 r	8.0	nΗ							
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs							

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHAN	ICAL SPE	CIFICAT	IONS		
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	•	T _J , T _{Stg}		-55 to +175	°C
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4	3.25	
Maximum thermal resistance, junction to case per package		- R _{thJC}	DC operation	1.63	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
Mounting torque	maximum			12 (10)	(lbf \cdot in)
Marking device			Case style D ² PAK		035SH 040SH 045SH
			Case style TO-262	30CTQ 30CTQ 30CTQ	040-1H

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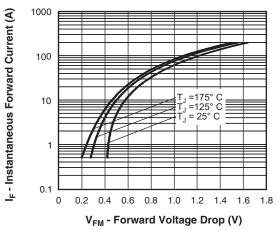


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

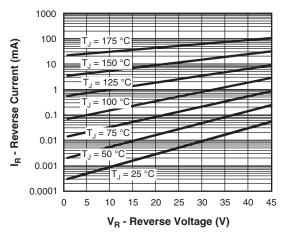


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

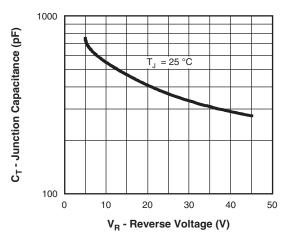


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

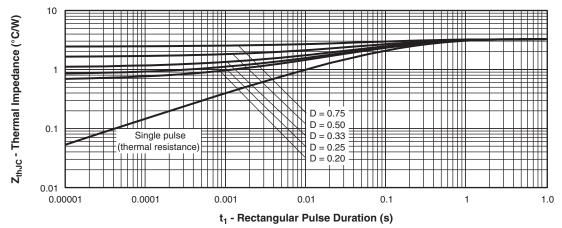


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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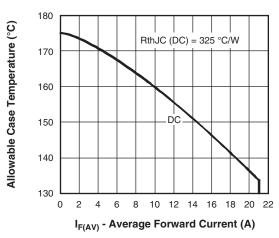


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

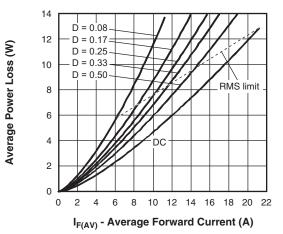


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

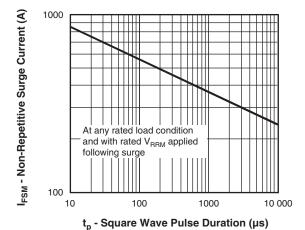


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

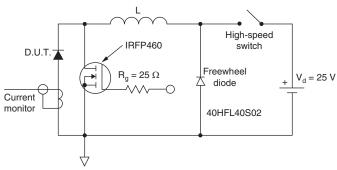


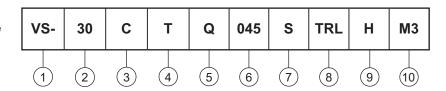
Fig. 8 - Unclamped Inductive Test Circuit

VS-30CTQ...SHM3, VS-30CTQ...-1HM3 Series

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 A)

Circuit configuration: C = common cathode

4 - T = TO-220

- Schottky "Q" series
- Voltage ratings

035 = 35 V040 = 40 V

- O D2DA16

045 = 45 V

• -1 = TO-262

8 - • None = tube

• TRL = tape and reel (left oriented - for D²PAK only)

• TRR = tape and reel (right oriented - for D²PAK only)

9 - H = AEC-Q101 qualified

- M3 = halogen-free, RoHS-compliant and termination lead (Pb)-free

ORDERING INFORMATION										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-30CTQ035SHM3	50	1000	Antistatic plastic tubes							
VS-30CTQ035STRRHM3	800	800	13" diameter reel							
VS-30CTQ035STRLHM3	800	800	13" diameter reel							
VS-30CTQ035-1HM3	50	1000	Antistatic plastic tubes							
VS-30CTQ040SHM3	50	1000	Antistatic plastic tubes							
VS-30CTQ040STRRHM3	800	800	13" diameter reel							
VS-30CTQ040STRLHM3	800	800	13" diameter reel							
VS-30CTQ040-1HM3	50	1000	Antistatic plastic tubes							
VS-30CTQ045SHM3	50	1000	Antistatic plastic tubes							
VS-30CTQ045STRRHM3	800	800	13" diameter reel							
VS-30CTQ045STRLHM3	800	800	13" diameter reel							
VS-30CTQ045-1HM3	50	1000	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS								
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046						
Dimensions	TO-262AA	www.vishay.com/doc?95419						
Part marking information	TO-263AB (D ² PAK)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?95032						



D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	ETERS	INC	HES	NOTES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

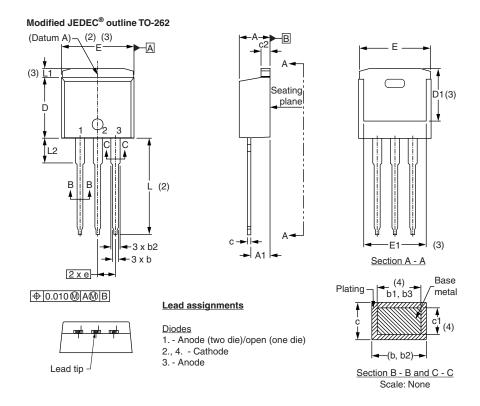
Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



TO-262

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.10	D BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.36	3.71	0.132	0.146		

Notes

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- 5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019 1 Document Number: 95419



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D²PAK

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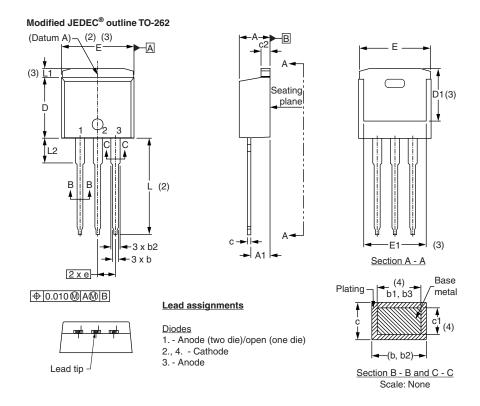
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TO-262

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