

Ultralow Power Single/Dual Comparator with Reference

FEATURES

- **Ultralow Quiescent Current: 2.1 μ A Typ (LTC1440)**
- **Reference Output Drives 0.01 μ F Capacitor**
- **Adjustable Hysteresis (LTC1440/LTC1442)**
- **Wide Supply Range:**
 Single: 2V to 11V
 Dual: ± 1 V to ± 5.5 V
- **Input Voltage Range Includes the Negative Supply**
- **TTL/CMOS Compatible Outputs**
- **12 μ s Propagation Delay with 10mV Overdrive**
- **No Crowbar Current**
- **40mA Continuous Source Current**
- **Pin Compatible Upgrades for MAX921/922/923**
- **3mm x 3mm x 0.75mm DFN Package (LTC1440)**

APPLICATIONS

- Battery-Powered System Monitoring
- Threshold Detectors
- Window Comparators
- Oscillator Circuits

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DESCRIPTION

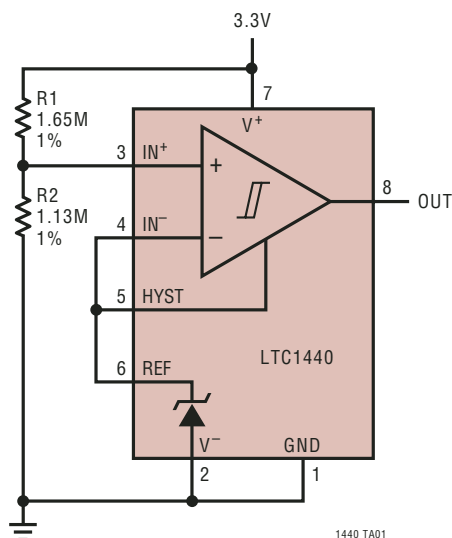
The LTC[®]1440/LTC1441/LTC1442 are ultralow power single and dual comparators with built-in references. The comparators feature less than 3.7 μ A supply current over temperature (LTC1440), a 1.182V $\pm 1\%$ reference, programmable hysteresis (LTC1440/LTC1442) and TTL/CMOS outputs that sink and source current. The reference output can drive a bypass capacitor of up to 0.01 μ F without oscillation.

The comparators operate from a single 2V to 11V supply or a dual ± 1 V to ± 5.5 V supply (LTC1440). Comparator hysteresis is easily programmed by using two resistors and the HYST pin (LTC1440/LTC1442). Each comparator's input operates from the negative supply to within 1.3V of the positive supply. The comparator output stage can continuously source up to 40mA. By eliminating the cross-conducting current that normally happens when the comparator changes logic states, the power supply glitches are eliminated.

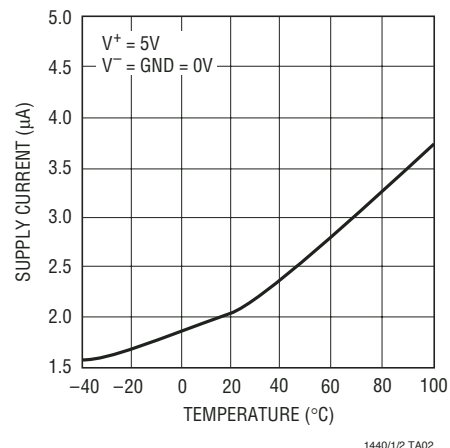
The LTC1440 is available in 8-pin PDIP, SO, MSOP and DFN packages. The LTC1441/LTC1442 are available in 8-pin PDIP and SO packages.

TYPICAL APPLICATION

Micropower 2.9V V_{CC} Threshold Detector



LTC1440 Supply Current vs Temperature



1440/1/2 TA02

ABSOLUTE MAXIMUM RATINGS

(Note 1)

Voltage

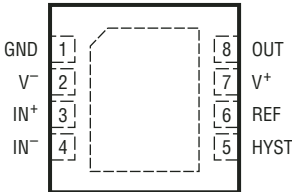
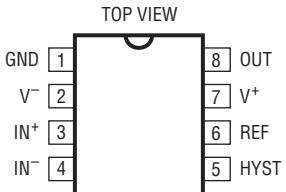
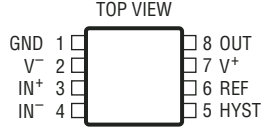
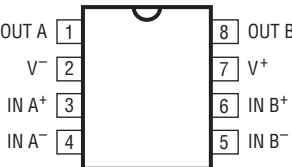
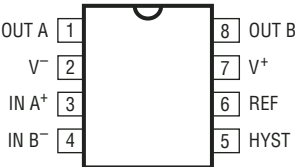
V^+ to V^- , V^+ to GND, GND to V^- 12V to $-0.3V$
 IN^+ , IN^- , HYST ($V^+ + 0.3V$) to ($V^- - 0.3V$)
 REF ($V^+ + 0.3V$) to ($V^- - 0.3V$)
 OUT (LTC1440) ($V^+ + 0.3V$) to (GND $- 0.3V$)
 OUT (LTC1441/LTC1442) ... ($V^+ + 0.3V$) to ($V^- - 0.3V$)

Current

IN^+ , IN^- , HYST 20mA
 REF 20mA
 OUT 50mA

OUT Short-Circuit Duration ($V^+ \leq 5.5V$) Continuous
 Power Dissipation 500mW
 Operating Temperature Range
 LTC144XC $0^\circ C$ to $70^\circ C$
 LTC144XI $-40^\circ C$ to $85^\circ C$
 Storage Temperature Range $-65^\circ C$ to $150^\circ C$
 Storage Temperature Range
 (DD Package) $-65^\circ C$ to $125^\circ C$
 Junction Temperature $150^\circ C$
 Junction Temperature (DD Package) $125^\circ C$
 Lead Temperature (Soldering, 10 sec) $300^\circ C$

PACKAGE/ORDER INFORMATION

TOP VIEW  DD PACKAGE 8-LEAD (3mm x 3mm) PLASTIC DFN $T_{JMAX} = 125^{\circ}C$, $\theta_{JA} = 160^{\circ}C/W$ (DD) UNDERSIDE METAL CONNECTED TO V^{-} (PCB CONNECTION OPTIONAL)		TOP VIEW  N8 PACKAGE 8-LEAD PDIP S8 PACKAGE 8-LEAD PLASTIC SO $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 130^{\circ}C/W$ (N8) $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 175^{\circ}C/W$ (S8)		TOP VIEW  MS8 PACKAGE 8-LEAD PLASTIC MSOP $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 250^{\circ}C/W$	
ORDER PART NUMBER	DD8 PART MARKING*	ORDER PART NUMBER	S8 PART MARKING	ORDER PART NUMBER	MS8 PART MARKING*
LTC1440CDD LTC1440IDD	LBTH	LTC1440CN8 LTC1440CS8 LTC1440IN8 LTC1440IS8	1440 1440 1440I 1440I	LTC1440CMS8 LTC1440IMS8	LTBX
TOP VIEW  N8 PACKAGE 8-LEAD PDIP S8 PACKAGE 8-LEAD PLASTIC SO $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 130^{\circ}C/W$ (N8) $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 175^{\circ}C/W$ (S8)		ORDER PART NUMBER	TOP VIEW  N8 PACKAGE 8-LEAD PDIP S8 PACKAGE 8-LEAD PLASTIC SO $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 130^{\circ}C/W$ (N8) $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 175^{\circ}C/W$ (S8)		ORDER PART NUMBER
		LTC1441CN8 LTC1441CS8 LTC1441IN8 LTC1441IS8			LTC1442CN8 LTC1442CS8 LTC1442IN8 LTC1442IS8
		S8 PART MARKING			S8 PART MARKING
		1441 1441I			1442 1442I

Order Options

Tape and Reel: Add #TR

Lead Free: Add #PBF

Lead Free Tape and Reel: Add #TRPBF

Lead Free Part Marking: <http://www.linear.com/leadfree/>

Consult LTC Marketing for parts specified with wider operating temperature ranges.

* The temperature grade is identified by a label on the shipping container.

ELECTRICAL CHARACTERISTICS The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V^+ = 5\text{V}$ and $V^- = \text{GND} = 0\text{V}$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS			MIN	TYP	MAX	UNITS
Power Supply								
V ⁺	Supply Voltage Range			●	2.0		11.0	V
I _{CC}	Supply Current	IN ⁺ = IN [−] + 80mV HYST = REF (LTC1440/LTC1442)	LTC1440 0°C ≤ T _A ≤ 70°C −40°C ≤ T _A ≤ 85°C	● ●		2.1	4.0 4.4	μA μA
			LTC1441 LTC1442	● ●		3.5 3.5	5.7 5.7	μA μA
Comparator								
V _{OS}	Comparator Input Offset Voltage	V _{CM} = 2.5V		●		±3	±10	mV
I _{IN}	Input Leakage Current (IN ⁺ , IN [−]) Input Leakage Current (HYST)	V _{IN} ⁺ = V _{IN} [−] = 2.5V		● ●		±0.01 ±0.02	±1.0 ±1.0	nA nA
V _{CM}	Comparator Input Common Mode Range			●	V [−]		V ⁺ − 1.3V	V
CMRR	Common Mode Rejection Ratio	V [−] to V ⁺ − 1.3V				0.1	1	mV/V
PSRR	Power Supply Rejection Ratio	V ⁺ = 2V to 11V (LTC1441) V ⁺ = 2.5V to 11V (LTC1440/LTC1442)				0.1 0.1	1 1	mV/V mV/V
NOISE	Voltage Noise	100Hz to 100kHz				100		μV _{RMS}
V _{HYST}	Hysteresis Input Voltage Range	LTC1440/LTC1442		●	REF − 50mV		REF	V
t _{PD}	Propagation Delay	C _{OUT} = 100pF Overdrive = 10mV Overdrive = 100mV				15 8		μs μs
V _{OH}	Output High Voltage	I _O = −13mA		●	V ⁺ − 0.4V			V
V _{OL}	Output Low Voltage	I _O = 1.8mA LTC1440 LTC1441/LTC1442		● ●			GND + 0.4V V [−] + 0.4V	V V
Reference								
V _{REF}	Reference Voltage	No Load	LTC1440/LTC1442 0°C ≤ T _A ≤ 70°C −40°C ≤ T _A ≤ 85°C LTC1440 (MSOP, DFN)	● ● ●	1.170 1.164		1.194 1.200	V V V
I _{SOURCE}	Reference Output Source Current	ΔV _{REF} ≤ 1mV (LTC1442)		●	100			μA
I _{SINK}	Reference Output Sink Current	ΔV _{REF} ≤ 2.5mV (LTC1442)			10	20		μA
ΔV _{REF}	Reference Source Current	0 ≤ I _{SOURCE} ≤ 2mA (LTC1440)		●		0.8	5	mV
	Reference Sink Current	0 ≤ I _{SINK} ≤ 10μA (LTC1440)		●		0.5	1.5 5	mV mV
NOISE	Voltage Noise	100Hz to 100kHz				100		μV _{RMS}

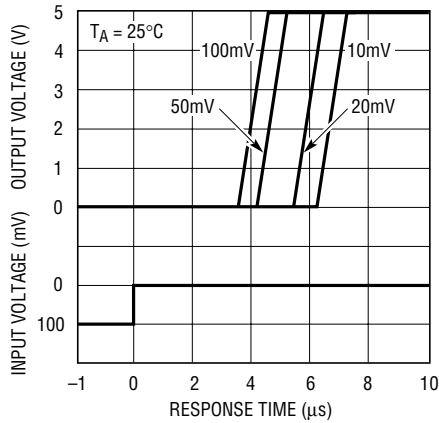
LTC1440/LTC1441/LTC1442

ELECTRICAL CHARACTERISTICS The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V^+ = 3\text{V}$ and $V^- = \text{GND} = 0\text{V}$ unless otherwise noted.

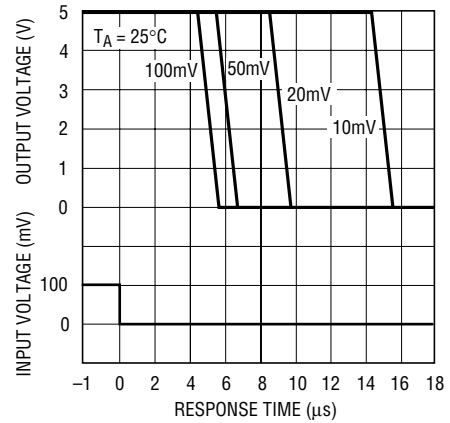
SYMBOL	PARAMETER	CONDITIONS			MIN	TYP	MAX	UNITS
Power Supply								
V ⁺	Supply Voltage Range			●	2		11	V
I _{CC}	Supply Current	IN ⁺ = IN [−] + 80mV HYST = REF (LTC1440/LTC1442)	LTC1440 0°C ≤ T _A ≤ 70°C −40°C ≤ T _A ≤ 85°C	● ●		2	3.9 4.3	μA μA
			LTC1441 LTC1442	● ●		3.5 3.5	5.7 5.7	μA μA
Comparator								
V _{OS}	Comparator Input Offset Voltage	V _{CM} = 1.5V		●		±3	±10	mV
I _{IN}	Input Leakage Current (IN ⁺ , IN [−]) Input Leakage Current (HYST)	V _{IN} ⁺ = V _{IN} [−] = 1.5V		● ●		±0.01 ±0.02	±1 ±1	nA nA
V _{CM}	Comparator Input Common Mode Range			●	V [−]		V ⁺ − 1.3V	V
CMRR	Common Mode Rejection Ratio	V [−] to V ⁺ − 1.3V				0.1	1	mV/V
PSRR	Power Supply Rejection Ratio	V ⁺ = 2V to 11V (LTC1441) V ⁺ = 2.5V to 11V (LTC1440/LTC1442)				0.1 0.1	1 1	mV/V mV/V
NOISE	Voltage Noise	100Hz to 100kHz				100		μV _{RMS}
V _{HYST}	Hysteresis Input Voltage Range	LTC1440/LTC1442		●	REF − 50mV		REF	V
t _{PD}	Propagation Delay	C _{OUT} = 100pF Overdrive = 10mV Overdrive = 100mV				14 5		μs μs
V _{OH}	Output High Voltage	I _O = −8mA		●	V ⁺ − 0.4V			V
Comparator								
V _{OL}	Output Low Voltage	I _O = 0.8mA LTC1440 LTC1441/LTC1442		● ●			GND + 0.4V V [−] + 0.4V	V V
Reference								
V _{REF}	Reference Voltage	No Load	LTC1440/LTC1442 0°C ≤ T _A ≤ 70°C −40°C ≤ T _A ≤ 85°C LTC1440 (MSOP, DFN)	● ● ●	1.170 1.164 1.164	1.182	1.194 1.200 1.200	V V V
I _{SOURCE}	Reference Output Source Current	ΔV _{REF} ≤ 1mV (LTC1442)		●	60	120		μA
I _{SINK}	Reference Output Sink Current	ΔV _{REF} ≤ 2.5mV (LTC1442)			10	20		μA
ΔV _{REF}	Reference Source Current	0 ≤ I _{SOURCE} ≤ 1mA (LTC1440)		●		0.8	5.5	mV
	Reference Sink Current	0 ≤ I _{SINK} ≤ 10μA (LTC1440)		●		0.5	1.5 5	mV mV
NOISE	Voltage Noise	100Hz to 100kHz				100		μV _{RMS}

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

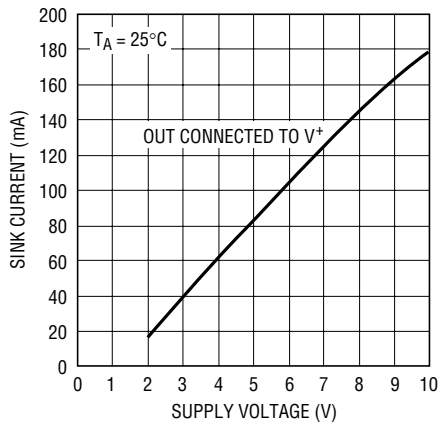
TYPICAL PERFORMANCE CHARACTERISTICS

Comparator Response Time vs
Input Overdrive

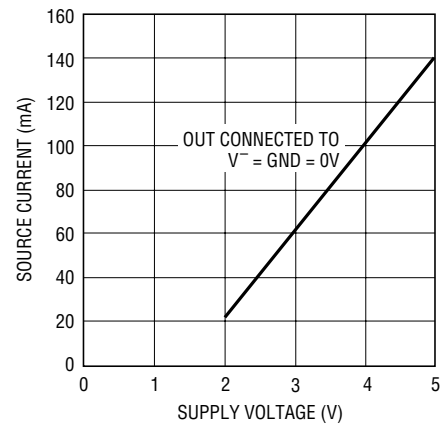
1440/1/2 G01

Comparator Response Time vs
Input Overdrive

1440/1/2 G02

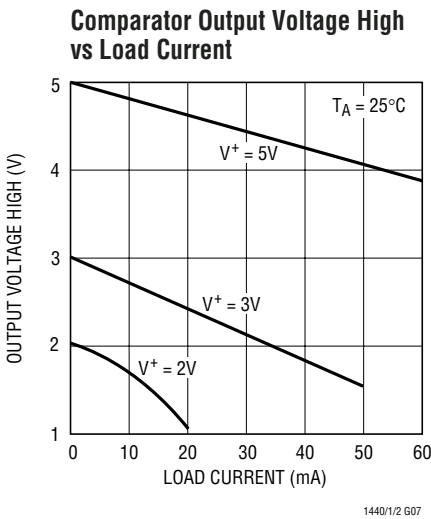
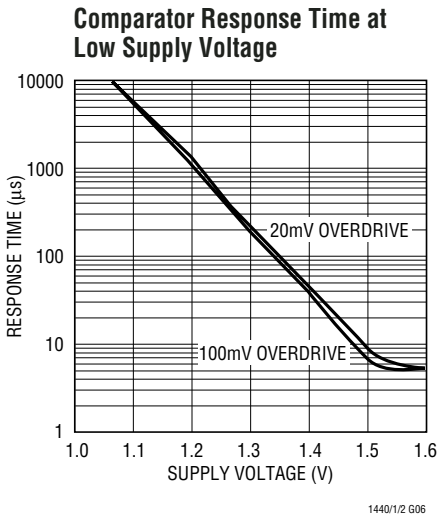
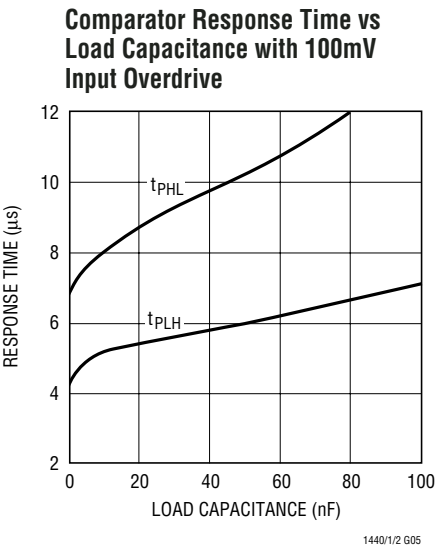
Comparator Short-Circuit Sink
Current vs Supply Voltage

1440/1/2 G03

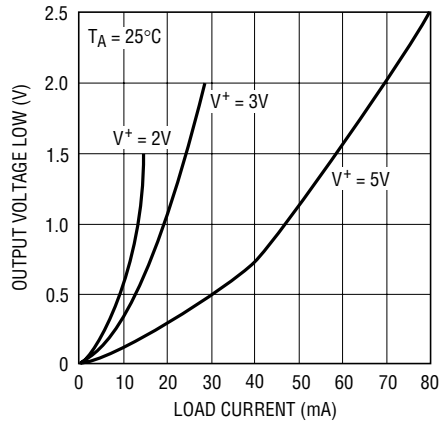
Comparator Short-Circuit Source
Current vs Supply Voltage

1440/1/2 G04

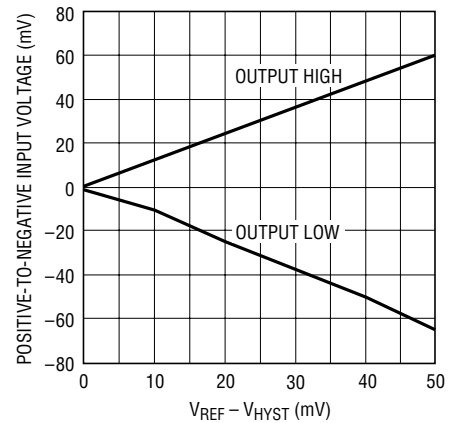
TYPICAL PERFORMANCE CHARACTERISTICS



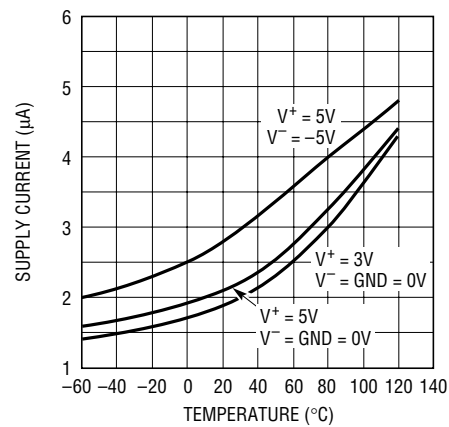
TYPICAL PERFORMANCE CHARACTERISTICS

Comparator Output Voltage Low
vs Load Current

1440/1/2 G08

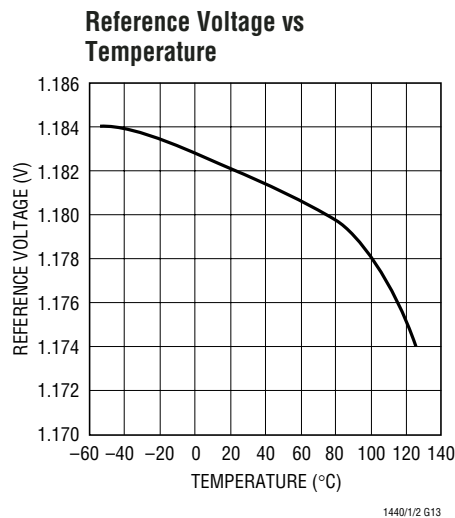
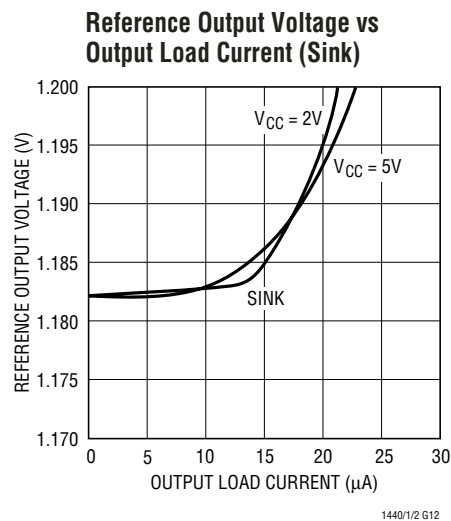
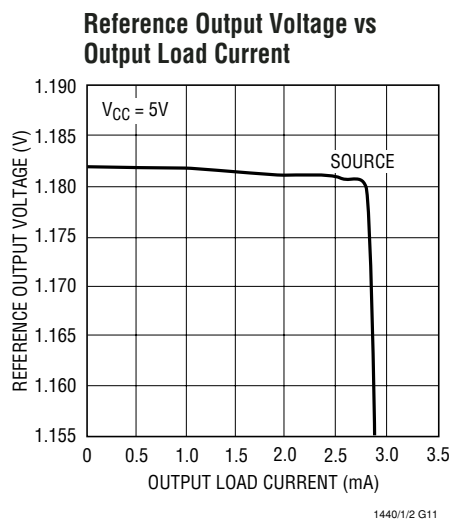
LTC1440/LTC1442
Hysteresis Control

1440/1/2 G09

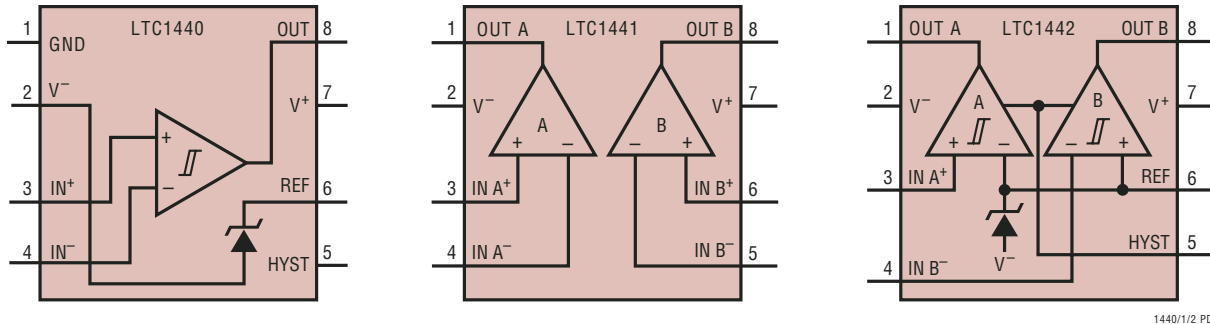
LTC1440
Supply Current vs Temperature

1440/1/2 G10

TYPICAL PERFORMANCE CHARACTERISTICS



PIN FUNCTIONS



1440/1/2 PD

LTC1440

GND (Pin 1): Ground. Connect to V^- for single supply operation.

V^- (Pin 2): Negative Supply. Connect to ground for single supply operation. Potential should be more negative than GND.

IN^+ (Pin 3): Noninverting Comparator Input. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

IN^- (Pin 4): Inverting Comparator Input. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

HYST (Pin 5): Hysteresis Input. Connect to REF if not used. Input voltage range is from V_{REF} to $V_{REF} - 50mV$.

REF (Pin 6): Reference Output. 1.182V with respect to V^- . Can source up to 200 μ A and sink 15 μ A at 25°C. Drive 0.01 μ F bypass capacitor without oscillation.

V^+ (Pin 7): Positive Supply. 2V to 11V.

OUT (Pin 8): Comparator CMOS Output. Swings from GND to V^+ . Output can source up to 40mA and sink 5mA.

LTC1441

OUT A (Pin 1): Comparator A CMOS Output. Swings from V^- to V^+ . Output can source up to 40mA and sink 5mA.

V^- (Pin 2): Negative Supply.

$IN A^+$ (Pin 3): Noninverting Input of Comparator A. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

$IN A^-$ (Pin 4): Inverting Input of Comparator A. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

$IN B^-$ (Pin 5): Inverting Input of Comparator B. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

$IN B^+$ (Pin 6): Noninverting Input of Comparator B. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

V^+ (Pin 7): Positive Supply. 2V to 11V.

OUT B (Pin 8): Comparator B CMOS Output. Swings from V^- to V^+ . Output can source up to 40mA and sink 5mA.

LTC1442

OUT A (Pin 1): Comparator A CMOS Output. Swings from V^- to V^+ . Output can source up to 40mA and sink 5mA.

V^- (Pin 2): Negative Supply.

$IN A^+$ (Pin 3): Noninverting Input of Comparator A. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

$IN B^-$ (Pin 4): Inverting Input of Comparator B. Input common mode range from V^- to $V^+ - 1.3V$. Input current typically 10pA at 25°C.

HYST (Pin 5): Hysteresis Input. Connect to REF if not used. Input voltage range is from V_{REF} to $V_{REF} - 50mV$.

REF (Pin 6): Reference Output. 1.182V with respect to V^- . Can source up to 200 μ A and sink 15 μ A at 25°C. Drive 0.01 μ F bypass capacitor without oscillation.

V^+ (Pin 7): Positive Supply. 2V to 11V.

OUT B (Pin 8): Comparator B CMOS Output. Swings from V^- to V^+ . Output can source up to 40mA and sink 5mA.

APPLICATIONS INFORMATION

LTC1440/LTC1441/LTC1442 are a family of micropower comparators with built-in 1.182V reference. Features include programmable hysteresis (LTC1440/LTC1442), wide supply voltage range (2V to 11V) and the ability of the reference to drive up to a 0.01 μ F capacitor without oscillation. The comparators' CMOS outputs can source up to 40mA and the supply current glitches, that normally occur when switching logic states, have been eliminated.

Power Supplies

The comparator family operates from a single 2V to 11V supply. The LTC1440 includes a separate ground for the comparator output stage, allowing a split supply ranging from ± 1 V to ± 5.5 V. Connecting V^- to GND on the LTC1440 will allow single supply operation. If the comparator output is required to source more than 1mA, or the supply source impedance is high, V^+ should be bypassed with a 0.1 μ F capacitor.

Comparator Inputs

The comparator inputs can swing from the negative supply V^- to within 1.3V max of the positive supply V^+ . The inputs can be forced 300mV below V^- or above V^+ without damage and the typical input leakage current is only ± 10 pA.

Comparator Outputs

The LTC1440 comparator output swings between GND and V^+ to assure TTL compatibility with a split supply. The LTC1441 and LTC1442 outputs swing between V^- and V^+ . The outputs are capable of sourcing up to 40mA and sinking up to 5mA while still maintaining microampere quiescent currents. The output stage does not generate crowbar switching currents during transitions which helps minimize parasitic feedback through the supply pins.

Voltage Reference

The internal bandgap reference has a voltage of 1.182V referenced to V^- . The reference accuracy is 1.5% from -40°C to 85°C . It can source up to 200 μ A and sink up to 20 μ A with a 5V supply. The reference can drive a bypass

capacitor of up to 0.01 μ F without oscillation and by inserting a series resistor, capacitance values up to 100 μ F can be used (Figure 1).

Figure 2 shows the resistor value required for different capacitor values to achieve critical damping. Bypassing the reference can help prevent false tripping of the comparators by preventing glitches on V^+ or reference load transients from disturbing the reference output voltage.

Figure 3 shows the bypassed reference output with a square wave applied to the V^+ pin. Resistors R2 and R3 set 10mV of hysteresis voltage band while R1 damps the reference response. Note that the comparator output doesn't trip.

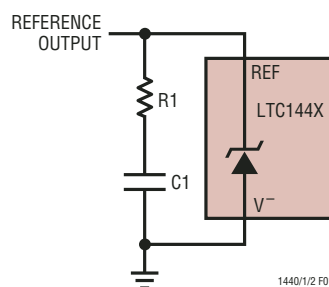


Figure 1. Damping the Reference Output

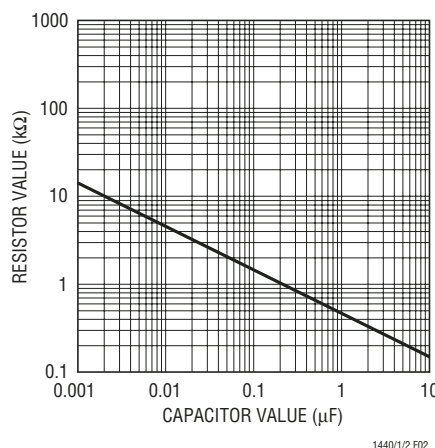


Figure 2. Damping Resistance vs Bypass Capacitor Value

APPLICATIONS INFORMATION

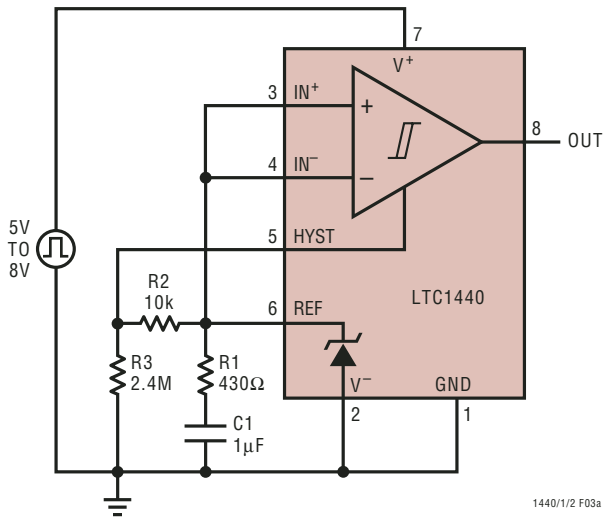


Figure 3a. Reference Transient Response Test Circuit

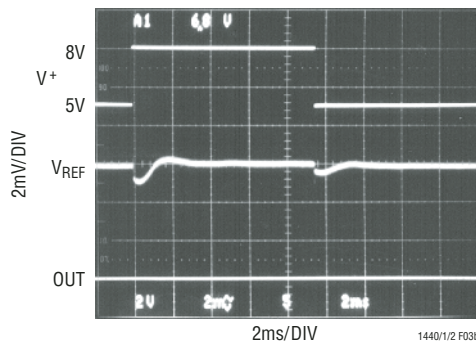


Figure 3b. Reference and Comparator Output Transient Response

Hysteresis

Hysteresis can be added to the LTC1440 by connecting a resistor (R1) between the REF and HYST pins and a second resistor (R2) from HYST to V⁻ (Figure 4).

The difference between the upper and lower threshold voltages, or hysteresis voltage band (V_{HB}), is equal to twice the voltage difference between the REF and HYST pins.

When more hysteresis is added, the upper threshold increases the same amount as the low threshold decreases. The maximum voltage allowed between REF and HYST pins is 50mV, producing a maximum hysteresis voltage band of 100mV. The hysteresis band could vary by

up to 15%. If hysteresis is not wanted, the HYST pin should be shorted to REF. Acceptable values for I_{REF} range from 0.1μA to 5μA. If 2.4M is chosen for R2, then the value of R1 is equal to the value of V_{HB}.

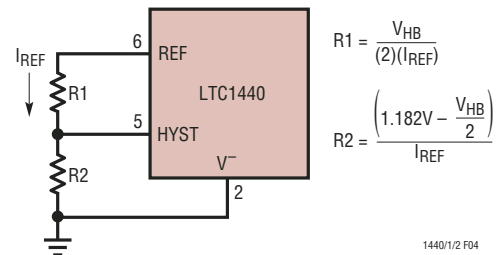


Figure 4. Programmable Hysteresis

Level Detector

The LTC1440 is ideal for use as a micropower level detector as shown in Figure 5. R1 and R2 form a voltage divider from V_{IN} to the noninverting comparator input. R3 and R4 set the hysteresis voltage, and R5 and C1 bypass the reference output. The following design procedure can be used to select the component values:

1. Choose the V_{IN} voltage trip level, in this example 4.65V.

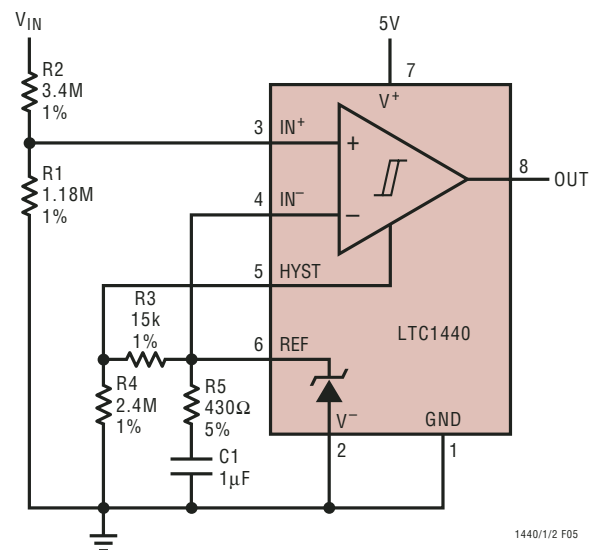


Figure 5. Glitch-Free Level Detector with Hysteresis

APPLICATIONS INFORMATION

2. Calculate the required resistive divider ratio.

$$\text{Ratio} = V_{\text{REF}}/V_{\text{IN}}$$

$$\text{Ratio} = 1.182\text{V}/4.65\text{V} = 0.254$$

3. Choose the required hysteresis voltage band at the input V_{HBIN} , in this example 60mV. Calculate the hysteresis voltage band referred to the comparator input V_{HB} .

$$V_{\text{HB}} = (V_{\text{HBIN}})(\text{Ratio})$$

$$V_{\text{HB}} = (60\text{mV})(0.254)$$

$$V_{\text{HB}} = 15.24\text{mV}$$

4. Choose the values for R3 and R4 to set the hysteresis.

$$R4 = 2.4\text{M}$$

$$R3(\text{k}\Omega) = V_{\text{HB}} = 15\text{k}$$

5. Choose the values for R1 and R2 to set the trip point.

$$R1 = \frac{V_{\text{REF}}}{I_{\text{BIAS}}} = \frac{1.182\text{V}}{1\mu\text{A}} = 1.18\text{M}$$

$$R2 = R1 \left[\frac{V_{\text{IN}}}{V_{\text{REF}} + \frac{V_{\text{HB}}}{2}} - 1 \right]$$

$$R2 = 1.18\text{M} \left[\frac{4.65\text{V}}{1.182\text{V} + \frac{15\text{mV}}{2}} - 1 \right]$$

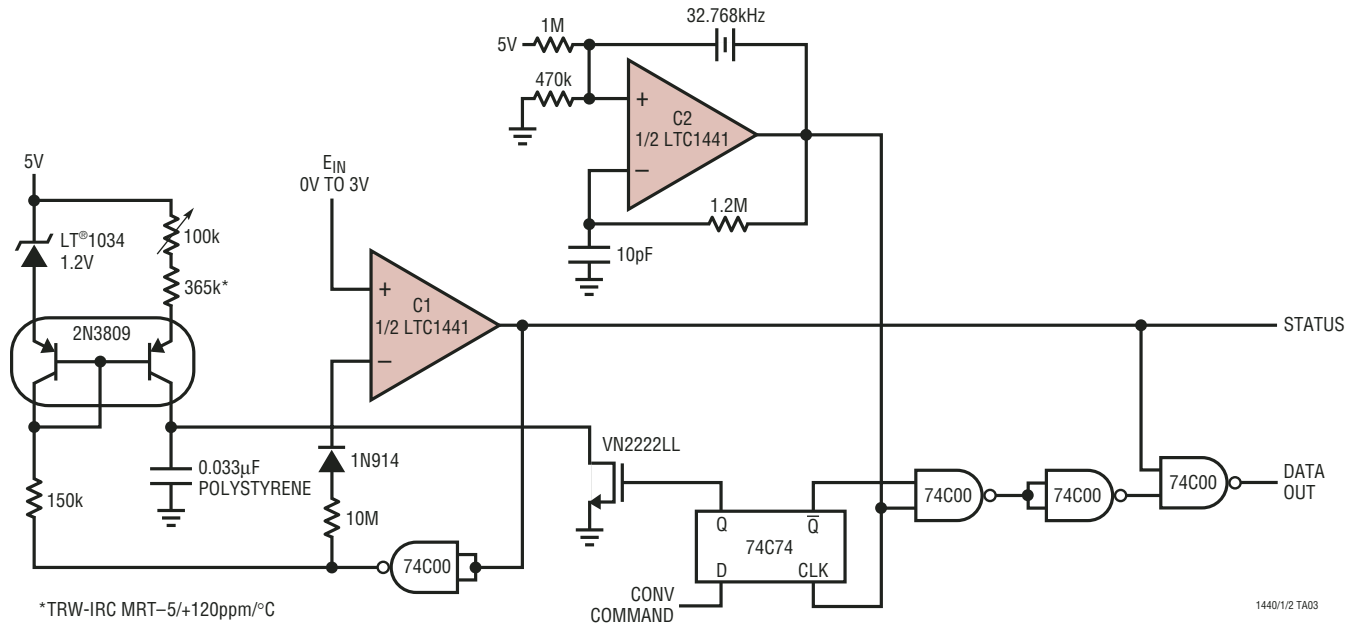
$$R2 = 3.40\text{M}$$

Low Voltage Operation

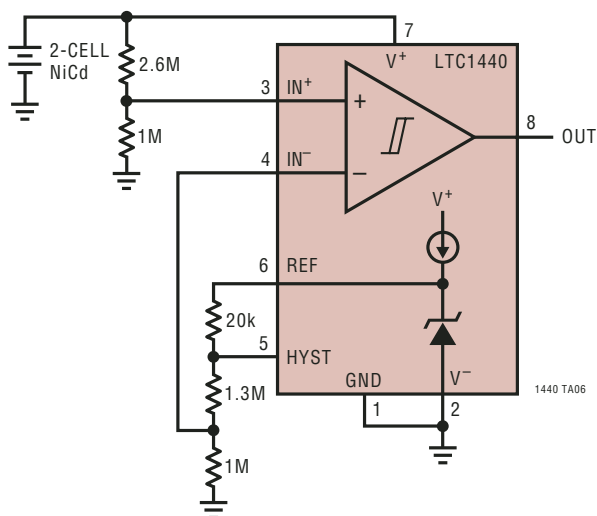
The guaranteed minimum operating voltage is 2V or ($\pm 1\text{V}$). As the total supply voltage is reduced below 2V, the performance degrades and the supply current falls. Both the reference and the comparator are still functioning with supply voltage down to 1.5V. The reference will have slightly worse temperature coefficient. While the comparator would have less output drive and the propagation delay increases. The useful input voltage range extends from the negative supply to a little under 1V below the positive supply, which is slightly closer to the positive rail than the device operating from higher supply voltages.

TYPICAL APPLICATIONS

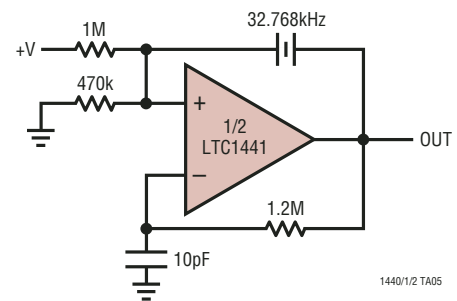
10-Bit 30 μ A A/D Converter



Low Battery Detect Operates Down to 1.5V



32.768kHz “Watch Crystal” Oscillator



3.5 ± 0.05

2.15 ± 0.05

1.65 ± 0.05
(2 SIDES)

0.675 ± 0.05

0.25 ± 0.05

0.50
BSC

2.38 ± 0.05
(2 SIDES)

PACKAGE OUTLINE

1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE MO-229 VARIATION OF (WEED-1)
2. DRAWING NOT TO SCALE
3. ALL DIMENSIONS ARE IN MILLIMETERS
4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
5. EXPOSED PAD SHALL BE SOLDER PLATED
6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON TOP AND BOTTOM OF PACKAGE

Figure 1: Dimensions of the test board. The diagram shows a rectangular board with a central shaded area. Dimensions include: $R = 0.115$ TYP (radius of the central area), 5 (width of the central area), 8 (width of the side areas), 0.38 ± 0.10 (thickness of the board), 0.25 ± 0.05 (width of the side areas), 1 (width of the central area), 1 (width of the side areas), 0.50 BSC (width of the central area), 2.38 ± 0.10 (total width of the board), and (VIB) OFN 1203 (text on the right side).

Figure 1 is a dimensional drawing of a four-hole plate. The drawing shows a rectangular plate with four holes. The dimensions are as follows:

- Overall width: 0.889 ± 0.127 ($0.035 \pm .005$)
- Distance between the centers of the two outer holes: 5.23 (0.206) MIN
- Distance between the centers of the two inner holes: $3.20 - 3.45$ ($0.126 - .136$)
- Distance from the center of the left hole to the left edge: 0.42 ± 0.038 ($0.0165 \pm .0015$) TYP
- Distance from the center of the right hole to the right edge: 0.65 (0.0256) BSC

DETAIL "A"

0.254
(.010)

GAUGE PLANE

0° - 6° TYP

0.53 ± 0.152
(.021 ± .006)

1. DIMENSIONS IN MILLIMETER/(INCH) (0.256)
BSC

2. DRAWING NOT TO SCALE

3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE

4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006") PER SIDE

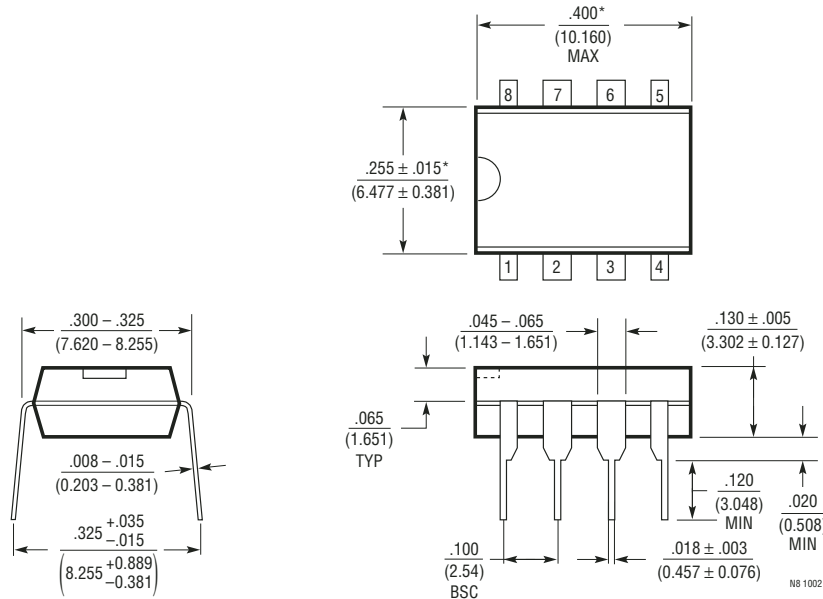
5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX

The drawing shows the mechanical specifications for the MS8 package. The top view is a square package with pins 1-4 on the bottom and 5-8 on the top. Dimensions include a total width of 3.00 ± 0.102 (.118 ± .004) (NOTE 3), a pin pitch of 0.52 (.0205) REF, a pin width of 0.127 ± 0.076 (.005 ± .003), a pin-to-pin distance of 0.65 (.0256) BSC, and a pin-to-body distance of 0.22 - 0.38 (.009 - .015) TYP. The side view shows a maximum height of 1.10 (.043) MAX and a reference height of 0.86 (.034) REF. The package is seated on a SEATING PLANE.

MSOP (MS8) 0204

PACKAGE DESCRIPTION

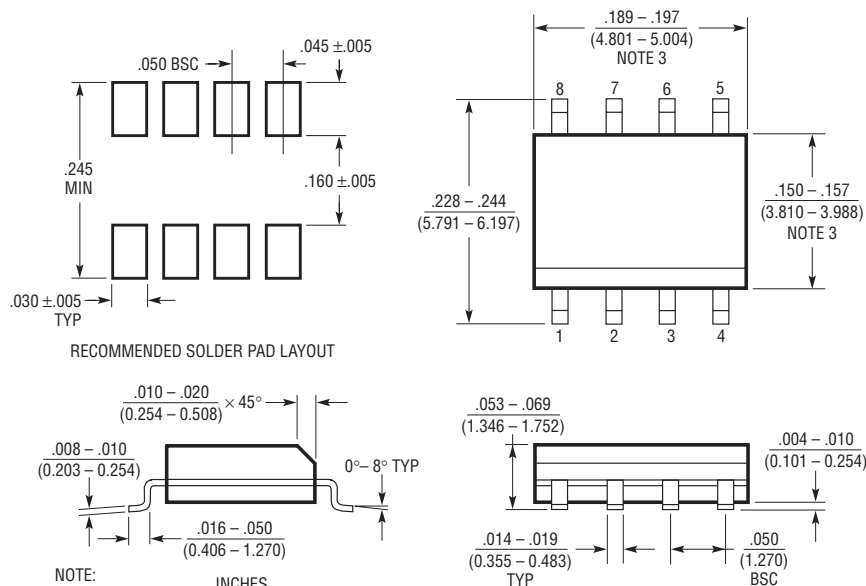
N8 Package 8-Lead PDIP (Narrow 0.300) (LTC DWG # 05-08-1510)



NOTE:

1. DIMENSIONS ARE $\frac{\text{INCHES}}{\text{MILLIMETERS}}$ *THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)

S8 Package 8-Lead Plastic Small Outline (Narrow 0.150) (LTC DWG # 05-08-1610)



NOTE:

1. DIMENSIONS IN $\frac{\text{INCHES}}{\text{MILLIMETERS}}$

2. DRAWING NOT TO SCALE

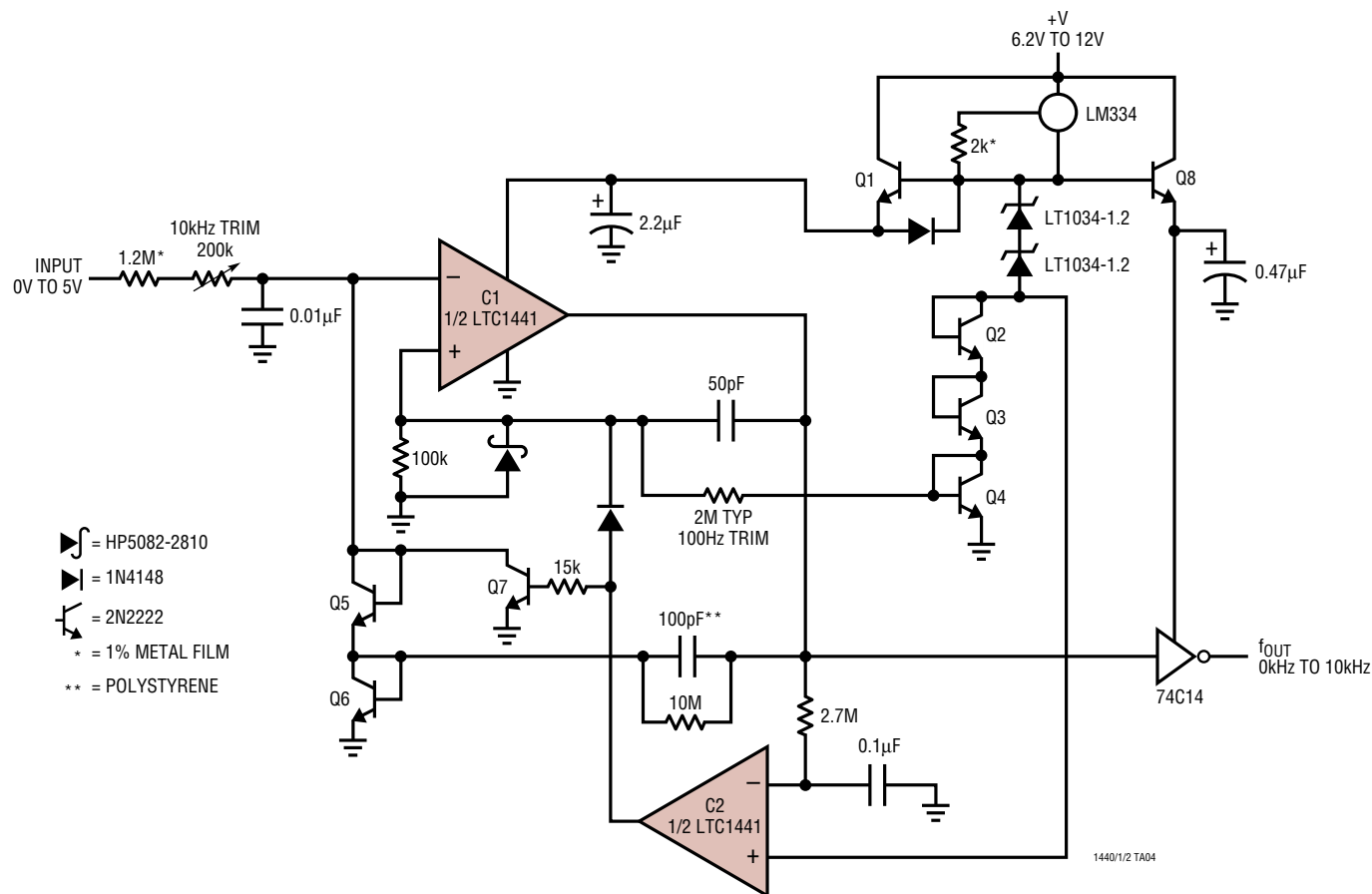
3. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .006" (0.15mm)

S08 0303

144012fc

TYPICAL APPLICATION

10kHz V/F Converter



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LTC1443	1.182V Reference with Micropower Quad Comparators	1% Accuracy, 8.5μA Maximum Current, Ref Output Drives 0.01μF
LTC1444/LTC1445	1.2V Reference with Quad Comparator with Adjustable Hysteresis	1% Accuracy, 8.5μA Maximum Current, Ref Output Drives 0.01μF
LTC1540	1.182V Reference with Nanopower Comparator with Adjustable Hysteresis	DFN Package 0.3μA Quiescent Current (Typical), Reference Drives 0.01μF
LTC1541	1.2V Reference with Micropower Amplifier and Comparator	DFN Package 1.25% Accuracy, Rail-to-Rail Out, Low Offset Amplifier
LTC1842/LTC1843	1.82V Reference with Dual Comparators with Adjustable Hysteresis	1% Accuracy, Open-Drain Out, Reference Drives 0.01μF
LTC1998	1.2 Reference with Comparator with Adjustable Thresholds	Li-Ion Low Battery Monitor, SOT23, 1% Accuracy
LT6700-1 LT6700-2/LT6700-3	0.4 Reference with Low Voltage Dual Comparators	SOT23, 1.4V to 18.5V Supply Range, ±2% Over Temperature