

# LMH0001 SMPTE 259M / 344M Serial Digital Cable Driver

Check for Samples: LMH0001

#### **FEATURES**

- SMPTE 259M and SMPTE 344M Compliant
- Data Rates to 540 Mbps
- Supports DVB-ASI at 270 Mbps
- Differential Input
- 75Ω Differential Output
- Adjustable Output Amplitude
- Single 3.3V Supply Operation
- Industrial Temperature Range: -40°C to +85°C
- 125mW Typical Power Consumption
- 16-pin WQFN Package
- Footprint Compatible with the LMH0002SQ and the GS9078A.

### **APPLICATIONS**

- SMPTE 259M and SMPTE 344M Serial Digital Interfaces
- DVB-ASI Applications
- Sonet/SDH and ATM Interfaces
- Digital Routers and Switches
- Distribution Amplifiers
- Buffer Applications
- Set Top Boxes
- Security Cameras

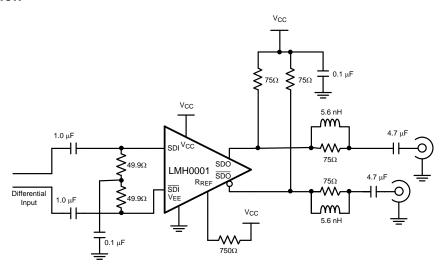
## **Typical Application**

#### DESCRIPTION

The LMH0001 SMPTE 259M / 344M Serial Digital Cable Driver is designed for use in SMPTE 259M / 344M serial digital video applications. The LMH0001 drives  $75\Omega$  transmission lines (Belden 8281, Belden 1694A or equivalent) at data rates up to 540 Mbps.

The output voltage swing of the LMH0001 is adjustable via a single external resistor.

The LMH0001 is powered from a single 3.3V supply. Power consumption is typically 125mW. The LMH0001 is available in a 16-pin WQFN package.



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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings (1)

Absolute maximum ratings	
Supply Voltage:	-0.5V to 3.6V
Input Voltage (all inputs)	-0.3V to V <sub>CC</sub> +0.3V
Output Current	28mA
Storage Temperature Range	−65°C to +150°C
Junction Temperature	+150°C
Lead Temperature (Soldering 4 Sec)	+260°C
Package Thermal Resistance $\theta_{JA}$ 16-pin WQFN $\theta_{JC}$ 16-pin WQFN	+78.9°C/W +42.7°C/W
ESD Rating (HBM)	5kV
ESD Rating (MM)	250V

Absolute Maximum Ratings are those parameter values beyond which the life and operation of the device cannot be ensured. The stating herein of these maximums shall not be construed to imply that the device can or should be operated at or beyond these values. The table of Electrical Characteristics specifies acceptable device operating conditions.

## **Recommended Operating Conditions**

Supply Voltage (V <sub>CC</sub> – V <sub>EE</sub> ):	3.3V ±5%
Operating Free Air Temperature (T <sub>A</sub> )	-40°C to +85°C

#### **DC Electrical Characteristics**

Over Supply Voltage and Operating Temperature ranges, unless otherwise specified (1)(2).

	Parameter	Test Conditions	Reference	Min	Тур	Max	Unit
V <sub>CMIN</sub>	Input Common Mode Voltage		SDI, <del>SDI</del>	1.6 + V <sub>SDI</sub> /2		V <sub>CC</sub> – V <sub>SDI</sub> /2	V
$V_{SDI}$	Input Voltage Swing	Differential		100		2000	$mV_{P-P}$
V <sub>CMOUT</sub>	Output Common Mode Voltage		SDO, SDO		V <sub>CC</sub> – V <sub>SDO</sub>		V
V <sub>SDO</sub>	Output Voltage Swing	Single-ended, $75\Omega$ load, $R_{REF} = 750\Omega$ 1%		750	800	850	$mV_{P-P}$
		Single-ended, $75\Omega$ load, $R_{REF} = 590\Omega$ 1%		900	1000	1100	$mV_{P-P}$
Icc	Supply Current				<sup>(3)</sup> 38	43	mA

Current flow into device pins is defined as positive. Current flow out of device pins is defined as negative. All voltages are stated referenced to  $V_{EE}$  = 0 Volts. Typical values are stated for  $V_{CC}$  = +3.3V and  $T_A$  = +25°C.

Maximum  $I_{CC}$  is measured at  $V_{CC}$  = +3.465V and  $T_A$  = +70°C.



#### **AC Electrical Characteristics**

Over Supply Voltage and Operating Temperature ranges, unless otherwise specified (1).

	Parameter	Test Conditions	Reference	Min	Тур	Max	Unit
DR <sub>SDI</sub>	Input Data Rate		<sup>(2)</sup> SDI, SDI			540	Mbps
t <sub>jit</sub>	Additive Jitter	270 Mbps	SDO, SDO		18		ps <sub>P-P</sub>
t <sub>r</sub> ,t <sub>f</sub>	Output Rise Time, Fall Time	20% - 80%		400	560	800	ps
	Mismatch in Rise/Fall Time	(2)				30	ps
	Duty Cycle Distortion	(2)				100	ps
t <sub>OS</sub>	Output Overshoot	(2)				8	%
RL <sub>SDO</sub>	Output Return Loss			<sup>(3)</sup> 15	20		dB

- Typical values are stated for  $V_{CC}$  = +3.3V and  $T_A$  = +25°C.
- Specification is ensured by characterization.

  Output return loss is dependent on board design. The LMH0001 meets this specification on the SD001SQ evaluation board from 5MHz to 1.5GHz.

## **CONNECTION DIAGRAM**

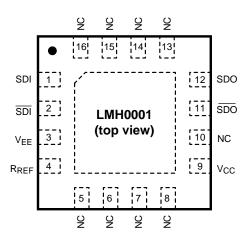


Figure 1. 16-Pin WQFN Package See Package Number RUM0016A

#### **PIN DESCRIPTIONS**

SOIC Pin No.	WQFN Pin No.	Name	Description
1	1	SDI	Serial data true input.
2	2	SDI	Serial data complement input.
3	3	V <sub>EE</sub>	Negative power supply (ground).
4	4	R <sub>REF</sub>	Output driver level control. Connect a resistor to V <sub>CC</sub> to set output voltage swing.
5	9	V <sub>CC</sub>	Positive power supply (+3.3V).
7	11	SDO	Serial data complement output.
8	12	SDO	Serial data true output.
_	5, 6, 7, 8, 10, 13, 14, 15, 16	NC	No connect.
_	DAP	V <sub>EE</sub>	Connect exposed DAP to negative power supply (ground).



#### **DEVICE OPERATION**

#### INPUT INTERFACING

The LMH0001 accepts either differential or single-ended input. The inputs are self-biased, allowing for simple AC or DC coupling. DC-coupled inputs must be kept within the specified common-mode range. SDI and  $\overline{\text{SDI}}$  are self-biased at approximately 2.1V with  $V_{\text{CC}} = 3.3V$ . Figure 2 shows the differential input stage for SDI and  $\overline{\text{SDI}}$ .

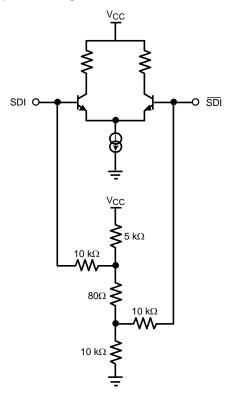


Figure 2. Differential Input Stage for SDI and SDI.

#### **OUTPUT INTERFACING**

The LMH0001 uses current mode outputs. Single-ended output levels are 800 mV<sub>P-P</sub> into 75 $\Omega$  AC-coupled coaxial cable (with R<sub>REF</sub> = 750 $\Omega$ ). Output level is controlled by the value of the R<sub>REF</sub> resistor connected between the R<sub>REF</sub> pin and V<sub>CC</sub>.

The  $R_{REF}$  resistor should be placed as close as possible to the  $R_{REF}$  pin. In addition, the copper in the plane layers below the  $R_{REF}$  network should be removed to minimize parasitic capacitance.



## **REVISION HISTORY**

Cł	hanges from Revision B (April 2013) to Revision C	Page
•	Changed layout of National Data Sheet to TI format	4



## PACKAGE OPTION ADDENDUM

6-Feb-2020

#### **PACKAGING INFORMATION**

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LMH0001SQ/NOPB	ACTIVE	WQFN	RUM	16	1000	Green (RoHS & no Sb/Br)	SN	Level-1-260C-UNLIM	-40 to 85	L001	Samples
LMH0001SQE/NOPB	ACTIVE	WQFN	RUM	16	250	Green (RoHS & no Sb/Br)	SN	Level-1-260C-UNLIM	-40 to 85	L001	Samples

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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## PACKAGE MATERIALS INFORMATION

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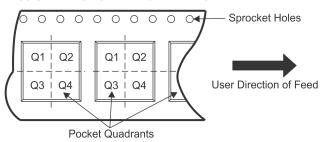
## TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

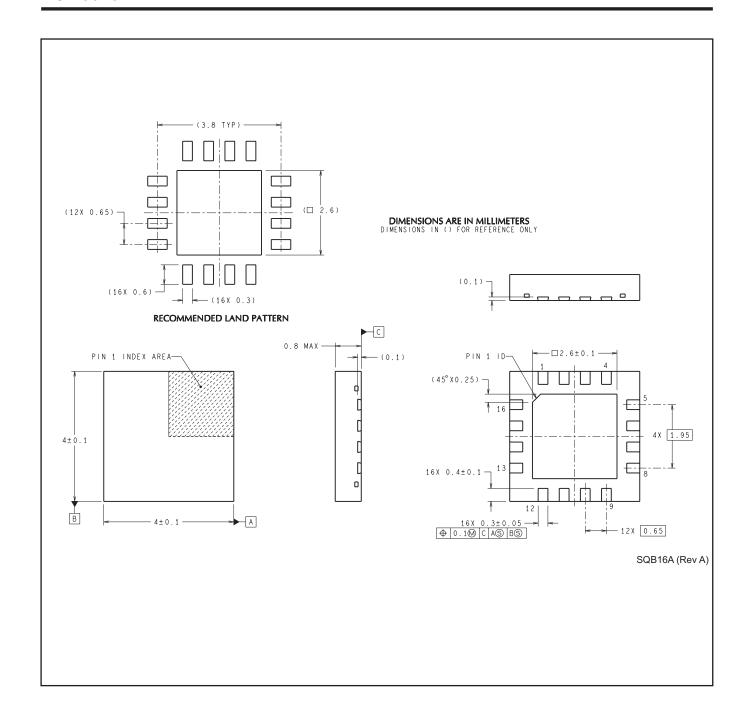
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LMH0001SQ/NOPB	WQFN	RUM	16	1000	178.0	12.4	4.3	4.3	1.3	8.0	12.0	Q1
LMH0001SQE/NOPB	WQFN	RUM	16	250	178.0	12.4	4.3	4.3	1.3	8.0	12.0	Q1

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#### \*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LMH0001SQ/NOPB	WQFN	RUM	16	1000	210.0	185.0	35.0
LMH0001SQE/NOPB	WQFN	RUM	16	250	210.0	185.0	35.0



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