# 1.5A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

## Features and Benefits

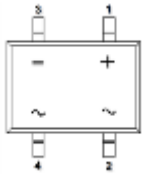
- Glass Passivated Die Construction
- Low Forward Voltage Drop, High Current Capability
- Surge Overload Rating to 50A Peak
- Designed for Surface Mount Applications
- UL Listed Under Recognized Component Index, File Number E94661
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

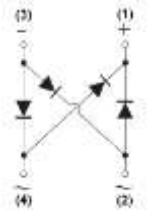
- Case: DF-S
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Tin. Solder Plated Leads, Solderable per MIL-STD-202, Method 208
- Polarity: As Marked on Case
- Marking: Type Number
- Weight: 0.38 grams (Approximate)



Top View



Pin Diagram



Internal Schematic

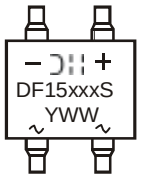
## Ordering Information (Note 4)

| Device | Packaging | Shipping |
|---|---|---|
| DF15xxxS-T | DF-S | 1500/Tape & Reel |
| DF15xxxS | DF-S | 50/Tube |

Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

.

## Marking Information




= Manufacturers' Code Marking
DF15xxxS = Product Type Marking Code
ex: DF1510S
YWW = Date Code Marking
Y = Last Digit of Year (ex: 6 for 2016)
WW = Week Code (01 to 53)


© Diodes Incorporated

## Maximum Ratings and Electrical Characteristics (@$T_A$ = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

| Characteristic | Symbol | DF 15005S | DF 1501S | DF 1502S | DF 1504S | DF 1506S | DF 1508S | DF 1510S | Unit |
|---|---|---|---|---|---|---|---|---|---|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage | $V_{RRM}$<br>$V_{RWM}$<br>$V_R$ | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| RMS Reverse Voltage | $V_{R(RMS)}$ | 35 | 70 | 140 | 280 | 420 | 580 | 700 | V |
| Average Forward Rectified Current @ $T_A$ = +40°C | $I_O$ | 1.5 | | | | | | | A |
| Non-Repetitive Peak Forward Surge Current, 8.3 ms<br>Single Half Sine-Wave Superimposed on Rated Load | $I_{FSM}$ | 50 | | | | | | | A |
| Forward Voltage (Per Element) @ $I_F$ = 1.5A | $V_{FM}$ | 1.1 | | | | | | | V |
| Peak Reverse Current at Rated @ $T_A$ = +25°C<br>DC Blocking Voltage (Per Element) @ $T_A$ = +125°C | $I_{RM}$ | 10<br>500 | | | | | | | µA |
| $I^2t$ Rating for Fusing (t<8.3ms) | $I^2t$ | 10.4 | | | | | | | $A^2s$ |
| Typical Total Capacitance per Element (Note 5) | $C_T$ | 25 | | | | | | | pF |
| Typical Thermal Resistance, Junction to Ambient (Note 6) | $R_{\theta JA}$ | 40 | | | | | | | °C/W |
| Operating and Storage Temperature Range | $T_J$, $T_{STG}$ | -65 to +150 | | | | | | | °C |

5. Measured at 1.0 MHz and applied reverse voltage of 4.0V DC.
6. Thermal resistance, junction to ambient, measured on PC board with 5.0mm$^2$ (0.03mm thick) land areas.

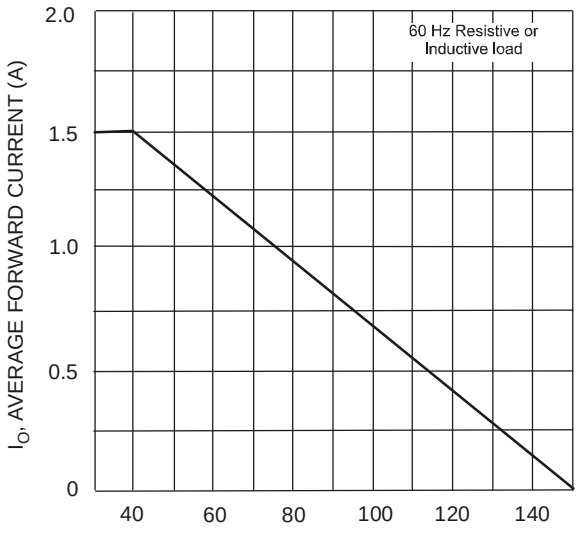


Fig. 1 Output Current Derating Curve

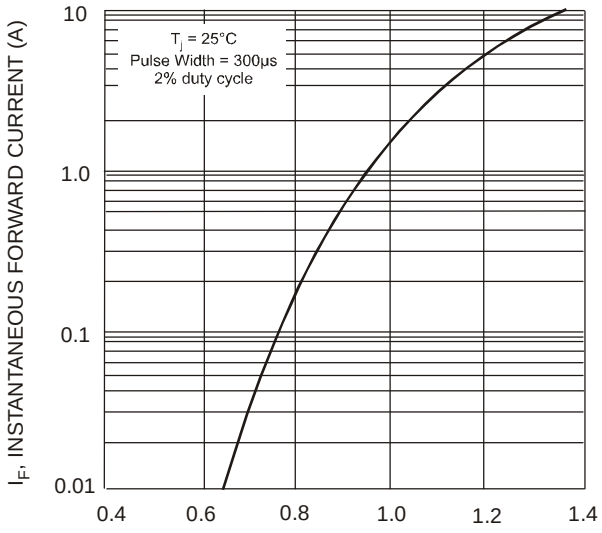


Fig. 2 Typical Forward Characteristics (per element)


© Diodes Incorporated

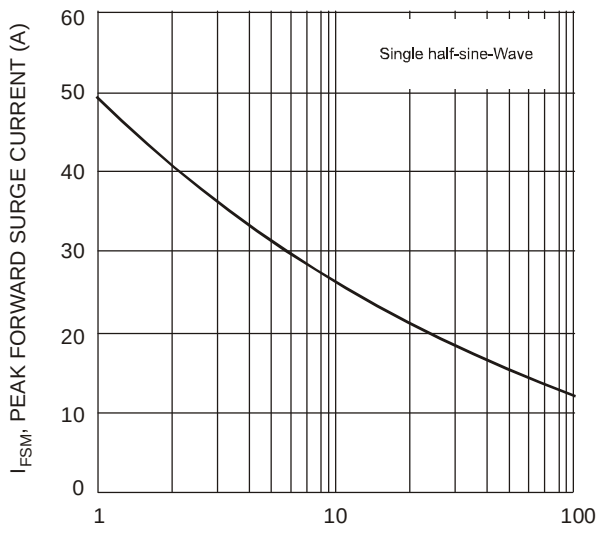



Fig. 3 Max Non-Repetitive Peak Forward Surge Current

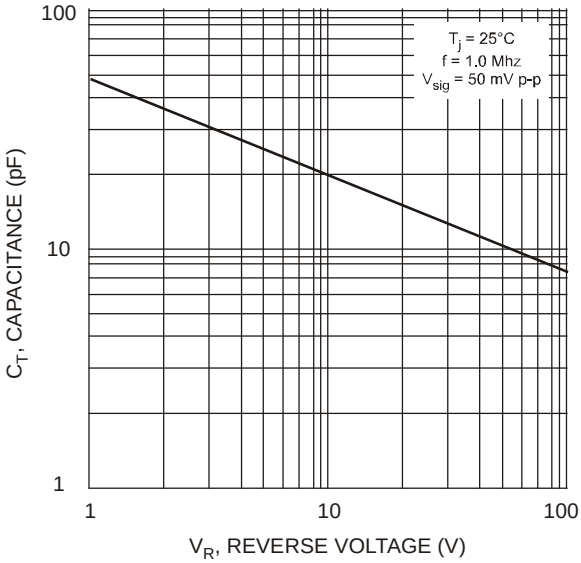



Fig. 4 Typical Total Capacitance (per element)

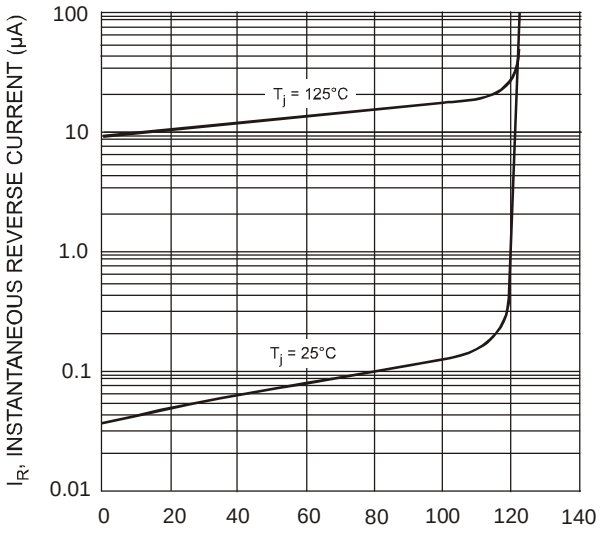


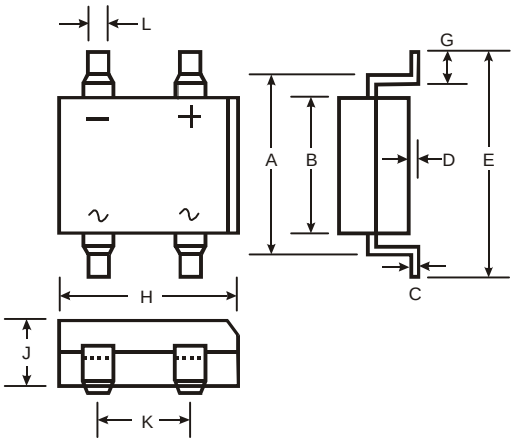

Fig. 5 Typical Reverse Characteristics (per element)


© Diodes Incorporated

## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

**DF-S**

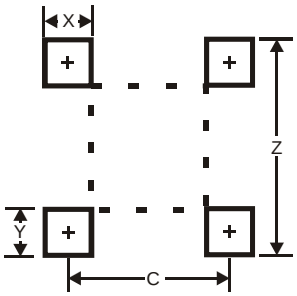



| DF-S | | |
|---|---|---|
| **Dim** | **Min** | **Max** |
| **A** | 7.40 | 7.90 |
| **B** | 6.20 | 6.50 |
| **C** | 0.22 | 0.30 |
| **D** | 0.076 | 0.33 |
| **E** | — | 10.40 |
| **G** | 1.02 | 1.53 |
| **H** | 8.13 | 8.51 |
| **J** | 2.40 | 2.60 |
| **K** | 5.00 | 5.20 |
| **L** | 1.00 | 1.20 |
| **All Dimensions in mm** | | |

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

**DF-S**




| Dimensions | Value (in mm) |
|---|---|
| **Z** | 10.26 |
| **X** | 1.2 |
| **Y** | 1.52 |
| **C** | 5.2 |


© Diodes Incorporated

**IMPORTANT NOTICE**

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

**LIFE SUPPORT**

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2016, Diodes Incorporated

**www.diodes.com**