

Pb Free Plating Product

DF005S thru DF10S



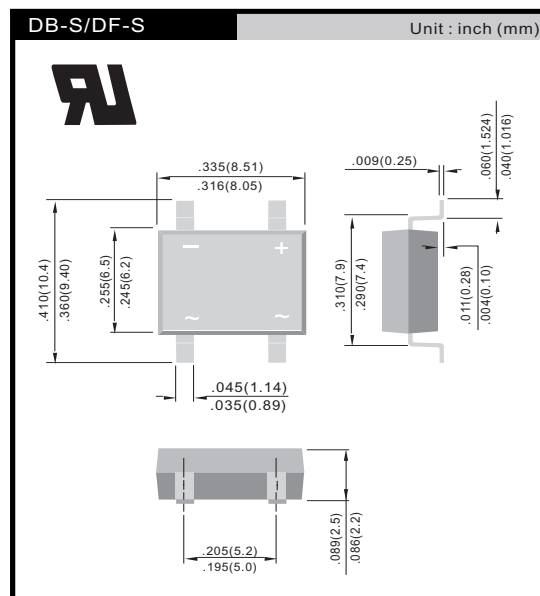
1.0 Ampere Surface Mount Glass Passivated Bridge Rectifier

Features

- Glass passivated chip junction
- Low forward voltage drop
- High surge overload rating of 50 A peak
- Ideal for printed circuit board

Mechanical Data

- Case: Molded plastic, DB-S/DF-S
- Epoxy: UL 94V-0 rate flame retardant
- Terminals: Leads solderable per MIL-STD-202, method 208 guaranteed
- Mounting position: Any

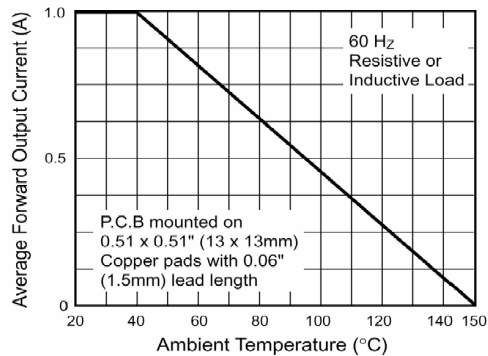
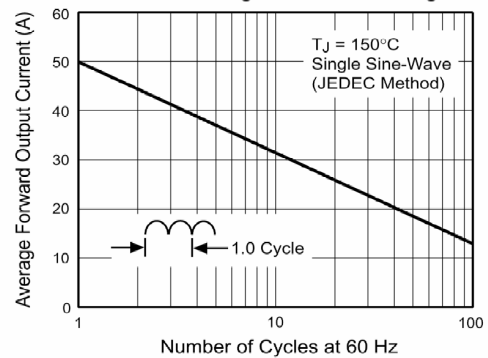
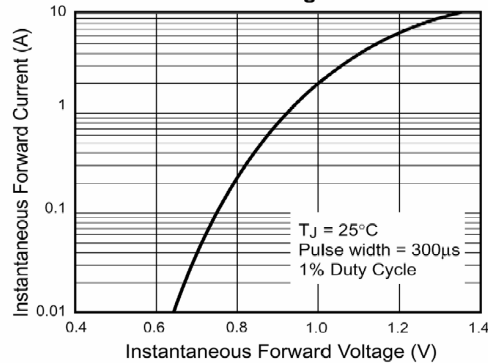
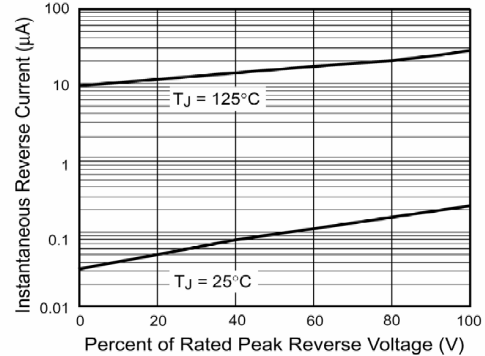
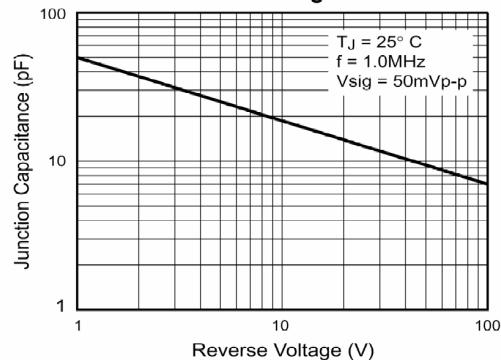
**Absolute Maximum Ratings and Characteristics**

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter		Symbols	DB101S DF005S	DB102S DF01S	DB103S DF02S	DB104S DF04S	DB105S DF06S	DB106S DF08S	DB107S DF10S	Unit
Maximum Recurrent Peak Reverse Voltage		V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage		V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage		V _{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current at T _A = 40 °C		I _(AV)	1							A
Peak Forward Surge Current 8.3 ms Single Half-sine-wave Superimposed on Rated Load (JEDEC Method)		I _{FSM}	50							A
Maximum Forward Voltage at 1 A		V _F	1.1							V
Maximum Reverse Current at Rated DC Blocking Voltage	at T _A = 25 °C	I _R	5							μA
	at T _A = 125 °C		500							
Typical Junction Capacitance ¹⁾		C _J	25							pF
Typical Thermal Resistance ²⁾		R _{θJA}	40							°C/W
Typical Thermal Resistance ²⁾		R _{θJL}	15							°C/W
Operating and Storage Temperature Range		T _J , T _S	-55 to +150							°C

¹⁾ Measured at 1 MHz and applied reverse voltage of 4 V

²⁾ Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B with 0.5 X 0.5" (13 X 13 mm) copper pads.

Fig. 1 - Derating Curve Output Rectified Current

Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Leg

Fig. 3 - Typical Forward Characteristics Per Leg

Fig. 4 - Typical Reverse Leakage Characteristics Per Leg

Fig. 5 - Typical Junction Capacitance Per Leg

Fig. 6 - Typical Transient Thermal Impedance
