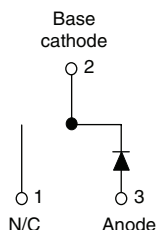


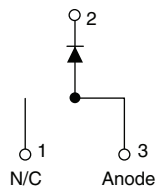
Ultrafast Rectifier, 15 A FRED Pt™

MURB1520PbF



D²PAK

MURB1520-1PbF



TO-262

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for AEC Q101 level



RoHS*
COMPLIANT

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

PRODUCT SUMMARY

t_{rr}	35 ns
$I_{F(AV)}$	15 A
V_R	200 V

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	V_{RRM}		200	V
Average rectified forward current	$I_{F(AV)}$	Total device, rated V_R , $T_C = 150\text{ °C}$	15	A
Non-repetitive peak surge current	I_{FSM}		200	
Peak repetitive forward current	I_{FM}	Rated V_R , square wave, 20 kHz, $T_C = 150\text{ °C}$	30	
Operating junction and storage temperatures	T_J, T_{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100\text{ }\mu\text{A}$	200	-	-	V
Forward voltage	V_F	$I_F = 15\text{ A}$	-	-	1.05	
		$I_F = 15\text{ A}, T_J = 150\text{ °C}$	-	-	0.85	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	-	10	μA
		$T_J = 150\text{ °C}, V_R = V_R$ rated	-	-	500	
Junction capacitance	C_T	$V_R = 200\text{ V}$	-	55	-	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t _{rr}	I _F = 1.0 A, dI _F /dt = 50 A/μs, V _R = 30 V		-	-	35	ns
		T _J = 25 °C	I _F = 15 A dI _F /dt = 200 A/μs V _R = 160 V	-	22	-	
		T _J = 125 °C		-	39	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	1.6	-	A
		T _J = 125 °C		-	4.1	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	19	-	nC
		T _J = 125 °C		-	90	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		- 65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to case	R_{thJC}		-	-	1.5	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction to ambient	R_{thJA}		-	-	50	
Thermal resistance, case to heatsink	R_{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D ² PAK	MURB1520			
		Case style TO-262	MURB1520-1			



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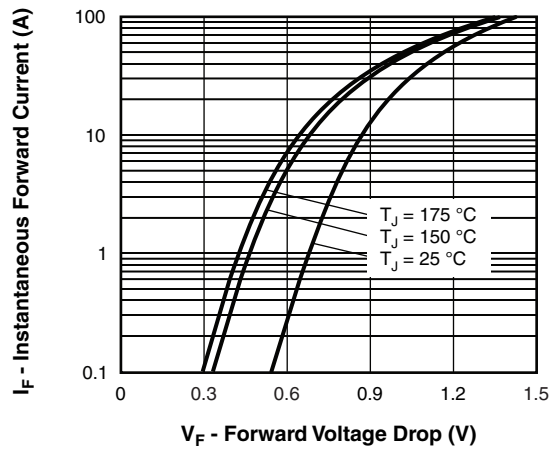


Fig. 1 - Typical Forward Voltage Drop Characteristics

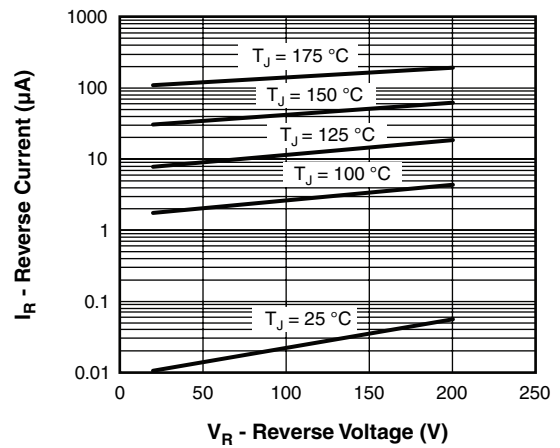


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

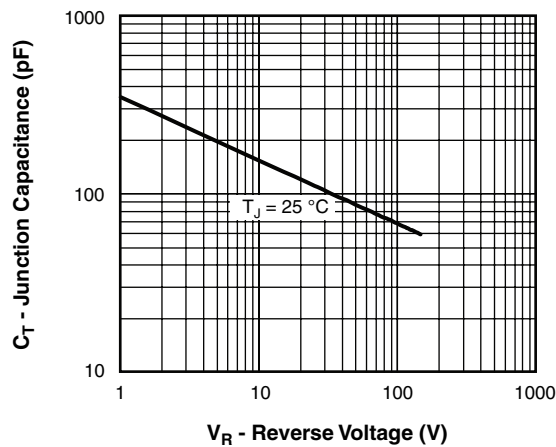


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

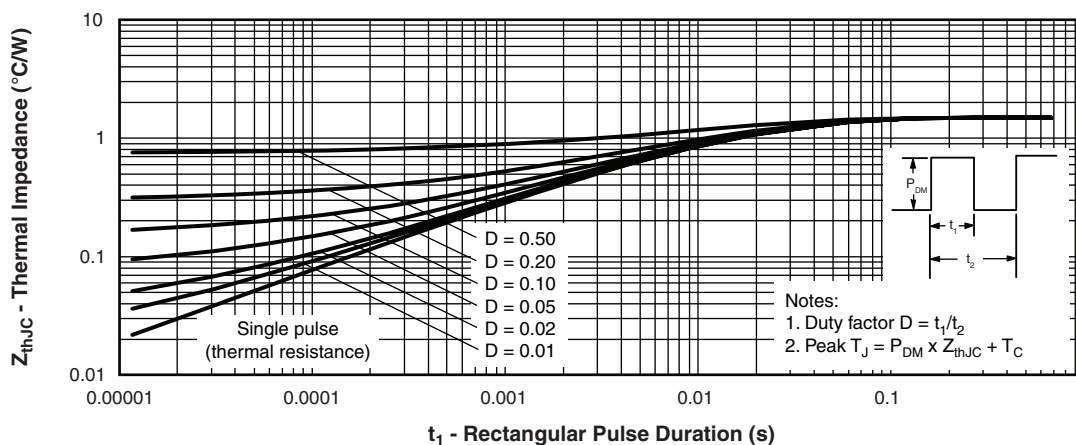


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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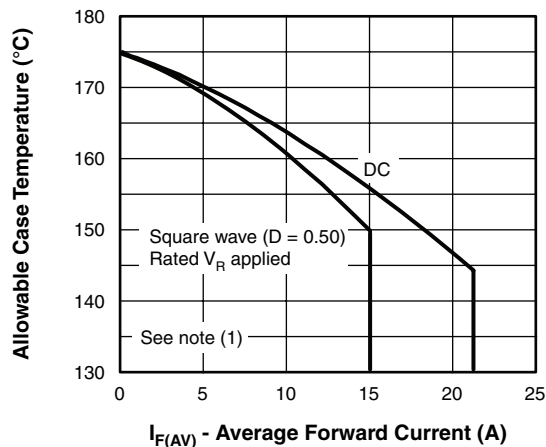


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

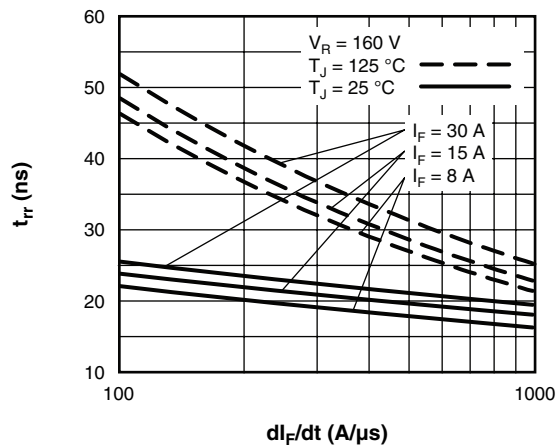


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

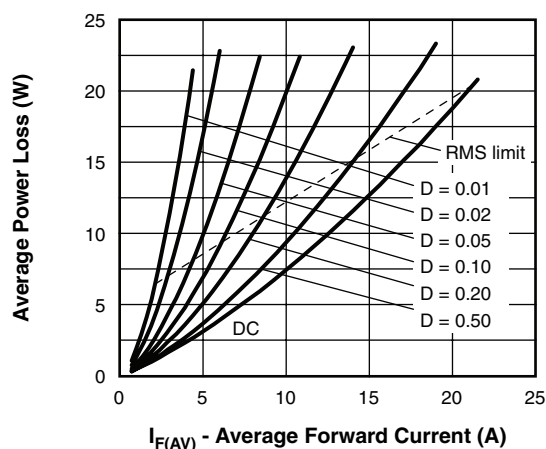


Fig. 6 - Forward Power Loss Characteristics

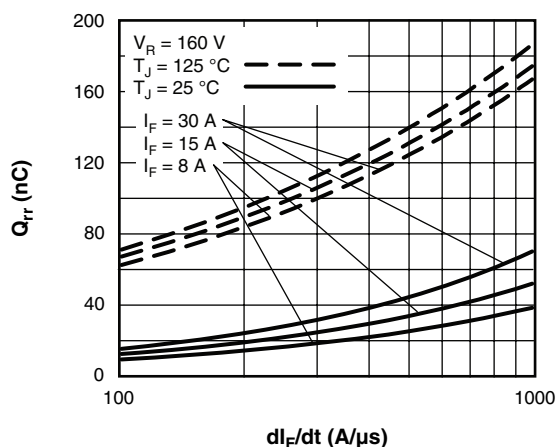


Fig. 8 - Typical Stored Charge vs. dI_F/dt

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R

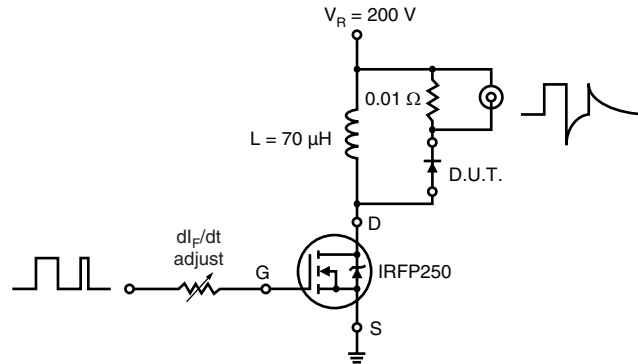


Fig. 9 - Reverse Recovery Parameter Test Circuit

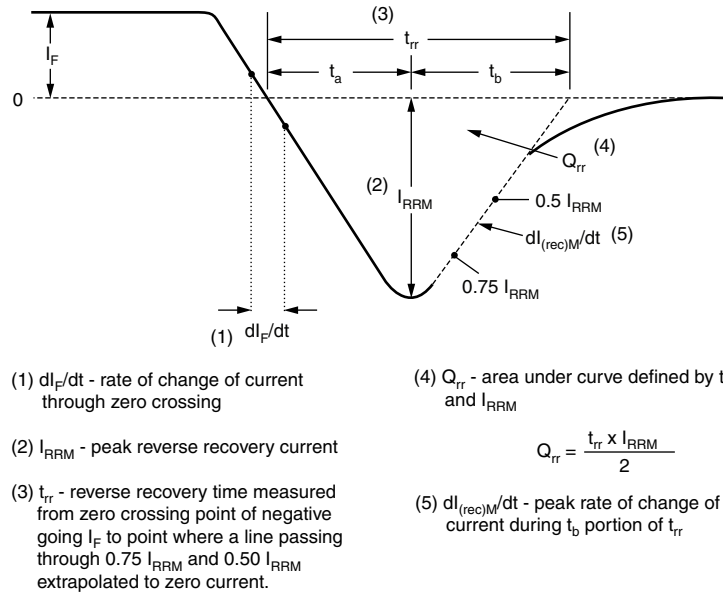


Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	MUR	B	15	20	-1	TRL	PbF
	①	②	③	④	⑤	⑥	⑦
①	-	Ultrafast MUR series					
②	-	B = D ² PAK/TO-262					
③	-	Current rating (15 = 15 A)					
④	-	Voltage rating (20 = 200 V)					
⑤	-	<ul style="list-style-type: none">• None = D²PAK• -1 = TO-262					
⑥	-	<ul style="list-style-type: none">• None = Tube (50 pieces)• TRL = Tape and reel (left oriented, for D²PAK package)• TRR = Tape and reel (right oriented, for D²PAK package)					
⑦	-	<ul style="list-style-type: none">• None = Standard production• PbF = Lead (Pb)-free					

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95014
Part marking information	http://www.vishay.com/doc?95008
Packaging information	http://www.vishay.com/doc?95032
SPICE model	http://www.vishay.com/doc?95271



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