

Pb Free Plating Product

HER2001CTD thru HER2008CTD



20.0 Ampere Heatsink Dual Doubler Polarity High Efficiency Rectifiers

Features

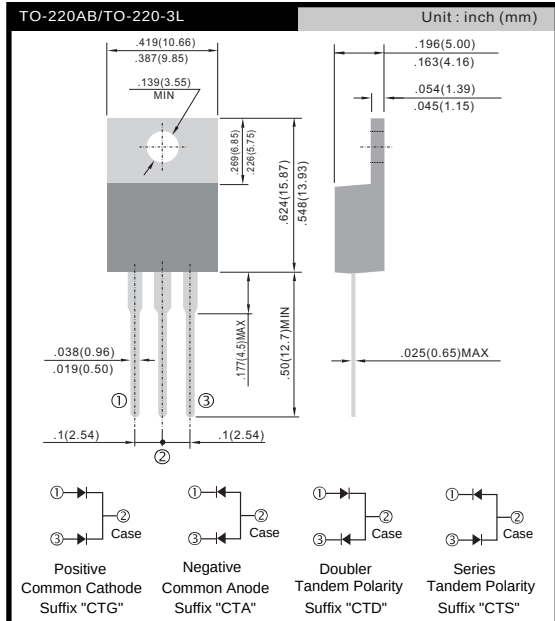
- ★ Fast switching for high efficiency
- ★ Low forward voltage drop
- ★ High current capability
- ★ Low reverse leakage current
- ★ High surge current capability

Application

- ★ Automotive Inverters and Solar Inverters
- ★ Plating Power Supply, SMPS and UPS
- ★ Car Audio Amplifiers and Sound Device Systems

Mechanical Data

- ★ Case: Heatsink TO-220AB open metal package
- ★ Epoxy: UL 94V-0 rate flame retardant
- ★ Terminals: Solderable per MIL-STD-202 method 208
- ★ Polarity: As marked on diode body
- ★ Mounting position: Any
- ★ Weight: 2.2 gram approximately

**MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)**

PARAMETER	SYMBOL	HER 2001CTD	HER 2002CTD	HER 2003CTD	HER 2004CTD	HER 2005CTD	HER 2006CTD	HER 2007CTD	HER 2008CTD	UNIT
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	300	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	210	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	300	400	600	800	1000	V
Maximum average forward rectified current(total device)	I _{F(AV)}	20								A
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	200								A
Maximum instantaneous forward voltage (Note 1) @ 10 A(per leg)	V _F	1.0				1.3	1.7			V
Maximum reverse current @ rated V _R T _J =25°C	I _R	5.0								μA

Note 1: Pulse test with PW=300μs, 1% duty cycle

Note 2: Test conditions: I_F=0.5A, I_R=1.0A, I_{RR}=0.25A

Note 3: Measured at 1 MHz and applied reverse voltage of 4.0V DC.

RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)

FIG. 1 MAXIMUM FORWARD CURRENT DERATING CURVE

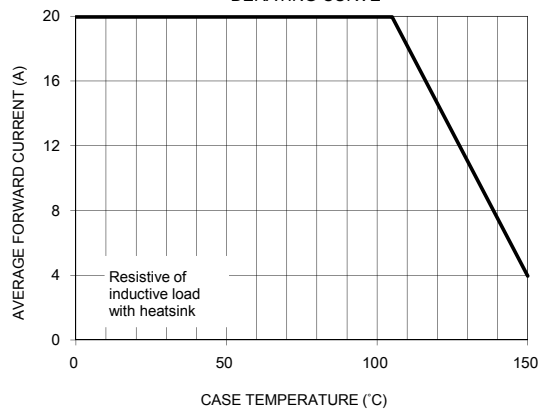


FIG. 2 TYPICAL REVERSE CHARACTERISTICS

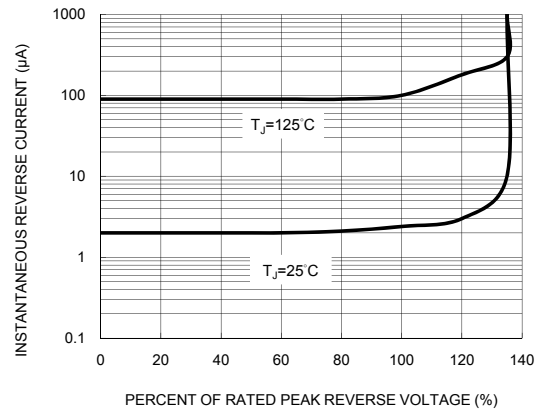


FIG. 3 MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

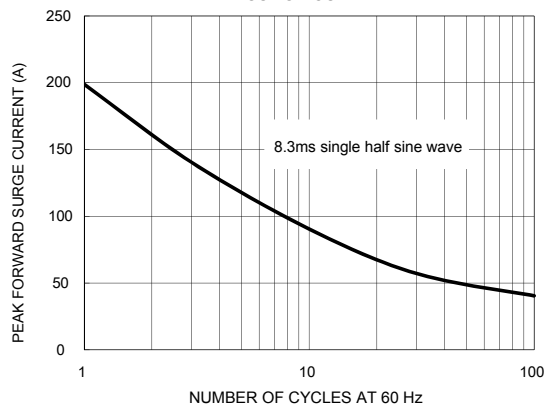


FIG. 4 TYPICAL FORWARD CHARACTERISTICS

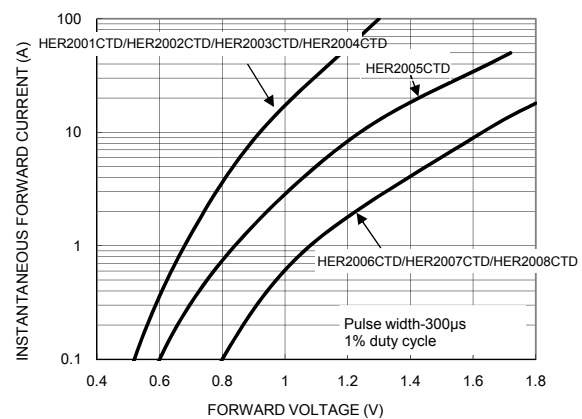


FIG. 5 TYPICAL JUNCTION CAPACITANCE

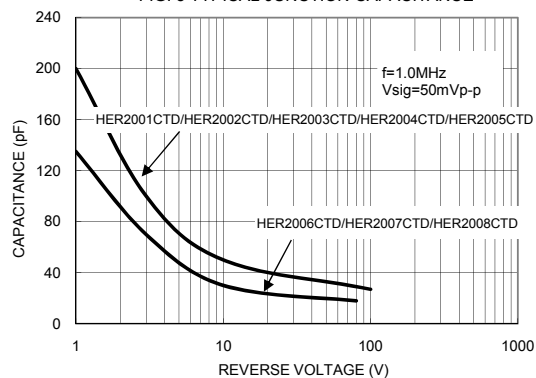


FIG. 6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

