Features

- Superior circuit protection
- Overcurrent and overvoltage protection
- Blocks surges up to rated limits
- High-speed performance
- Small SMT package
- Agency listing:
- RoHS* and AEC-Q101 compliant**

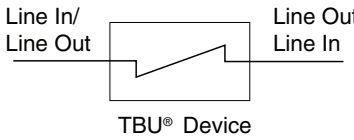
Applications

- Voice / VDSL cards
- Protection modules and dongles
- Process control equipment
- Test and measurement equipment
- General electronics

TBU-CA-Q Series - TBU® High-Speed Protectors

General Information

The TBU-CA-Q Series of Bourns® TBU® products are low capacitance single bidirectional high-speed protection components, constructed using MOSFET semiconductor technology, and designed to protect against faults caused by short circuits, AC power cross, induction and lightning surges.



The TBU® high-speed protector placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics will not be exposed to large voltages or currents during surge events up to the device's specified maximum limits. The TBU® device is provided in a surface mount DFN package and meets industry standard requirements such as RoHS and Pb Free solder reflow profiles.

Agency Listing

Description	
UL	File Number: E315805

Absolute Maximum Ratings (@ T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Part Number	Value	Unit
V_{imp}	Peak impulse voltage withstand with duration less than 10 ms	TBU-CA025-050-WH-Q TBU-CA065-100-WH-Q TBU-CA065-300-WH-Q TBU-CA085-500-WH-Q	250 650 650 850	V
V_{rms}	Continuous A.C. RMS voltage	TBU-CA025-050-WH-Q TBU-CA065-100-WH-Q TBU-CA065-300-WH-Q TBU-CA085-500-WH-Q	100 300 300 425	V
T_{op}	Operating temperature range		-55 to +125	°C
T_{stg}	Storage temperature range		-65 to +150	°C
T_{amax}	Maximum ambient temperature		+125	°C
ESD	HBM ESD protection per IEC 61000-4-2		±2	kV

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WARNING Cancer and Reproductive Harm - www.P65Warnings.ca.gov

*RoHS Directive 2015/863, Mar 31, 2015 and Annex.
**"Q" part number suffix for automotive and other applications requiring appropriate AEC-Q101 compliance.
Specifications are subject to change without notice.
Users should verify actual device performance in their specific applications.

Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

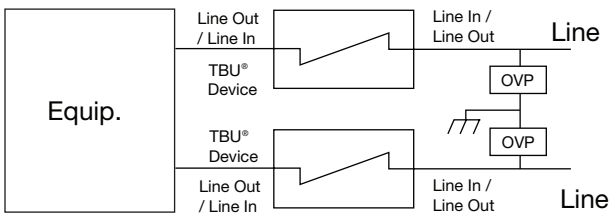
Symbol	Parameter		Part Number	Min.	Typ.	Max.	Unit
$I_{trigger}$	Current required for the device to go from operating state to protected state		TBU-CA025-050-WH-Q TBU-CA065-100-WH-Q TBU-CA065-300-WH-Q TBU-CA085-500-WH-Q	50 100 300 500	75 150 450 750	100 200 600 1000	mA
R_{device}	Series resistance of the TBU device	V_{imp} = 250 V $I_{trigger}$ (min.) = 50 mA V_{imp} = 650 V $I_{trigger}$ (min.) = 100 mA V_{imp} = 650 V $I_{trigger}$ (min.) = 300 mA V_{imp} = 850 V $I_{trigger}$ (min.) = 500 mA	TBU-CA025-050-WH-Q TBU-CA065-100-WH-Q TBU-CA065-300-WH-Q TBU-CA085-500-WH-Q		13.3 11.5 7.6 10.7	15.3 13.2 8.8 12.2	Ω
t_{block}	Time for the device to go from normal operating state to protected state					1	µs
I_Q	Current through the triggered TBU® device with 50 Vdc circuit voltage			0.25	0.50	1.00	mA
V_{reset}	Voltage below which the triggered TBU® device will transition to normal operating state			12	16	20	V
$R_{th(j-l)}$	Junction to ambient - FR4 using JESD51-3 board				129		°C/W
$R_{th(j-l)}$	Junction to ambient - FR4 using JESD51-7 board				40		°C/W

Environmental Characteristics

Parameter	Value
Moisture Sensitivity Level	1
ESD Classification (HBM)	1A

Reference Application

The TBU® devices are general use protectors used in a wide variety of applications. The maximum voltage rating of the TBU® device should never be exceeded. Where necessary, an OVP should be employed to limit the maximum voltage. A cost-effective protection solution combines Bourns® TBU® protection devices with a pair of Bourns® MOVs. For bandwidth sensitive applications, a Bourns® GDT may be substituted for the MOV.



Basic TBU Operation

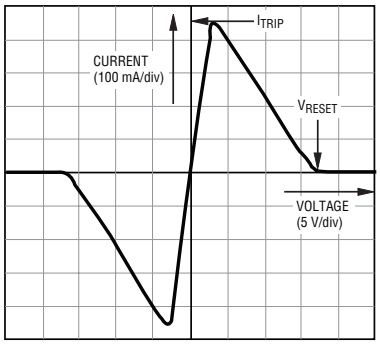
The TBU® device, constructed using MOSFET semiconductor technology, placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics are not exposed to large voltages or currents during surge events up to the device's specified maximum limits. The TBU® device operates in approximately 1 µs - once line current exceeds the TBU® device's trigger current $I_{trigger}$. When operated, the TBU® device will limit the current to less than the $I_{trigger}$ value within the t_{block} duration. If voltage above V_{reset} is continuously sustained, the TBU® device will subsequently reduce the current to a quiescent current level within a period of time that is dependent upon the applied voltage.

After the surge, the TBU® device resets when the voltage across the TBU® device falls to the V_{reset} level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below V_{reset} (such as unpowered signal lines).

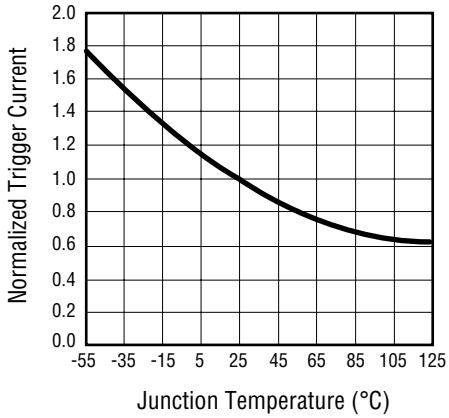
If the line has a normal DC bias above V_{reset}, the voltage across the TBU® device may not fall below V_{reset} after the surge. In such cases, special care needs to be taken to ensure that the TBU® device will reset, with software monitoring as one method used to accomplish this. Bourns application engineers can provide further assistance.

Performance Graphs

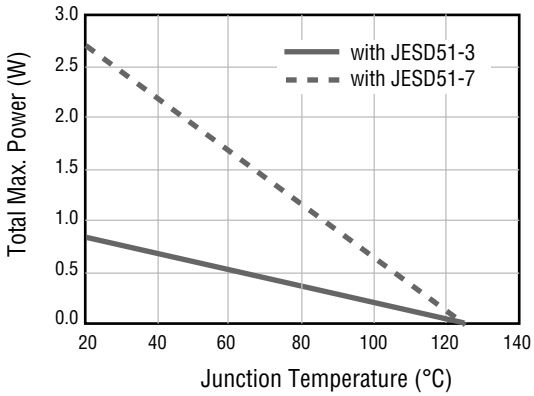
Typical V-I Characteristics



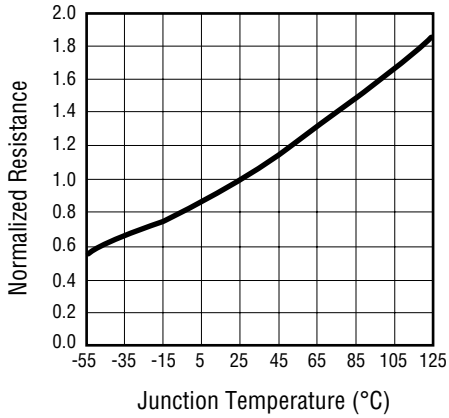
Typical Trigger Current vs. Temperature



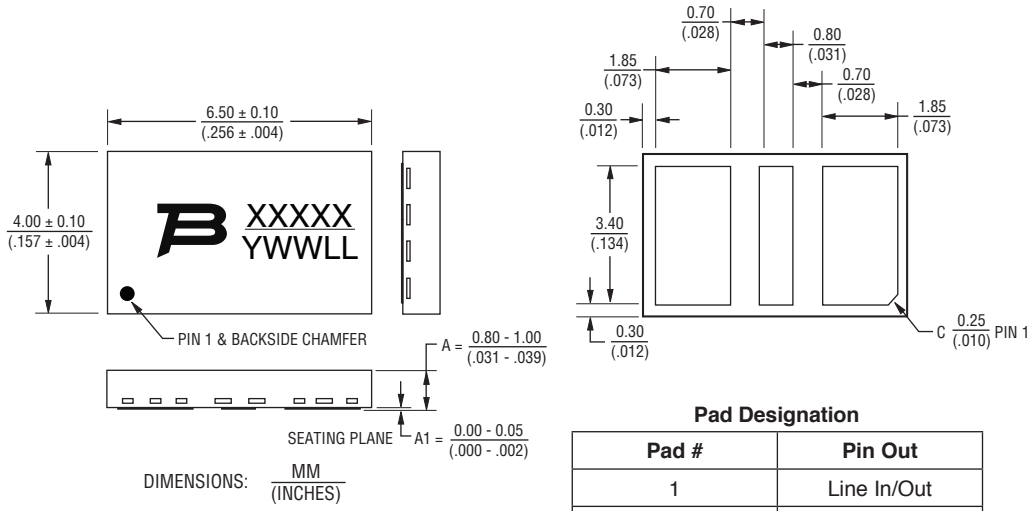
Power Derating Curve



Typical Resistance vs. Temperature



Product Dimensions

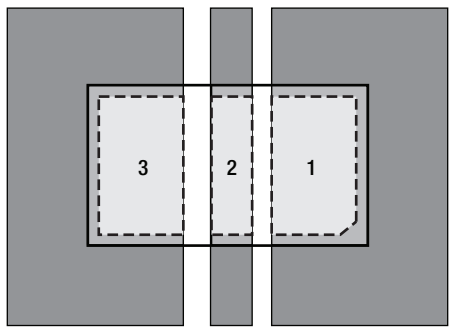


Pad Designation

Pad #	Pin Out
1	Line In/Out
2	NU
3	Line Out/In

Recommended Pad Layout

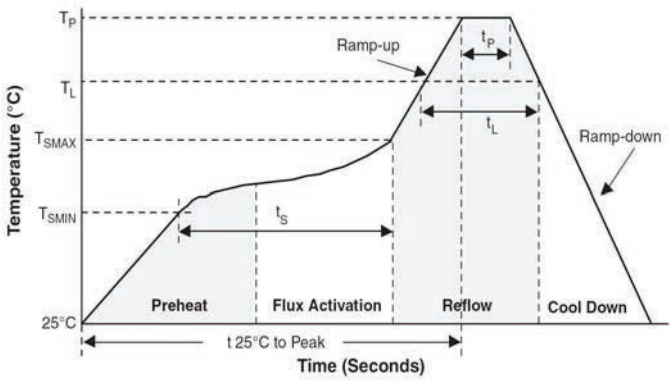
TBU® High-Speed Protectors have a 100 % matte-tin termination finish. For improved thermal dissipation, the recommended layout uses PCB copper areas which extend beyond the exposed solder pad. The exposed solder pads should be defined by a solder mask which matches the pad layout of the TBU® device in size and spacing. For best performance, Bourns recommends that solder pads be the same dimension as the TBU® pads, but if smaller solder pads are used, they should be centered on the TBU® package terminal pads and not be more than 0.10-0.12 mm (0.004-0.005 in.) smaller in overall width or length. Solder pad areas should not be larger than the TBU® pad sizes to ensure adequate clearance is maintained. The recommended stencil thickness is 0.10-0.12 mm (0.004-0.005 in.) with a stencil opening size 0.025 mm (0.0010 in.) less than the solder pad size. Extended copper areas beyond the solder pad significantly improve the junction to ambient thermal resistance, resulting in operation at lower junction temperatures with a corresponding benefit of reliability. All pads should be soldered to the PCB, including pads marked as NC or NU but no electrical connection should be made to these pads. For minimum parasitic capacitance, Bourns recommends that ground or power signals not be routed beneath any pad.



Dark grey areas show added PCB copper area for better thermal resistance.

Reflow Profile

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (Tsmax to Tp)	3 °C/sec. max.
Preheat - Temperature Min. (Tsmin) - Temperature Max. (Tsmax) - Time (tsmin to tsmax)	 150 °C 200 °C 60-180 sec.
Time maintained above: - Temperature (TL) - Time (tL)	 217 °C 60-150 sec.
Peak/Classification Temperature (Tp)	260 °C
Time within 5 °C of Actual Peak Temp. (tp)	20-40 sec.
Ramp-Down Rate	6 °C/sec. max.
Time 25 °C to Peak Temperature	8 min. max.



How to Order

TBU - CA xxx - yyy - WH - Q

TBU® Product

Series
CA = Bi-Series

Impulse Voltage Rating
025 = 250 V
065 = 650 V
085 = 850 V

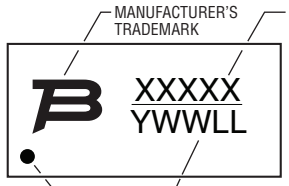
Trigger Current
050 = 50 mA
100 = 100 mA
300 = 300 mA
500 = 500 mA

Hold to Trip Ratio Suffix
W = Hold to Trip Ratio

Package Suffix
H = DFN Package

AEC-Q101 Suffix
Q = AEC-Q101 Compliant

Typical Part Marking



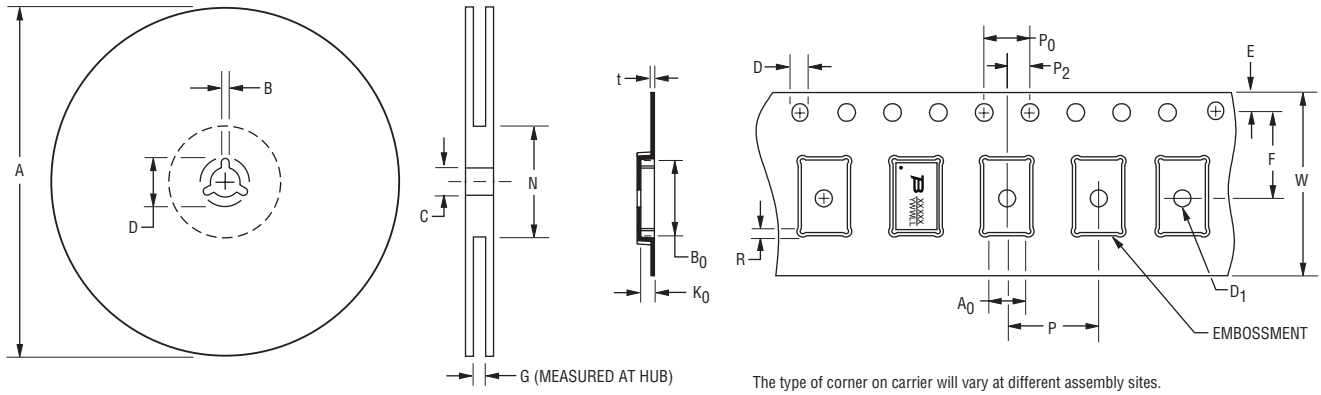
MANUFACTURER'S TRADEMARK

5 DIGIT PRODUCT CODE:
- 1ST ALPHA CHARACTER INDICATES PRODUCT FAMILY:
  A = TBU-CA SERIES
- 2ND & 3RD DIGITS INDICATE IMPULSE VOLTAGE.
- 4TH & 5TH DIGITS INDICATE TRIGGER CURRENT.

UNDERSCORE DENOTES AEC-Q101 COMPLIANCY.

MANUFACTURING DATE CODE:
- 1ST DIGIT INDICATES THE YEAR.
- 2ND & 3RD DIGITS INDICATE THE WEEK NUMBER.
- 4TH & 5TH DIGITS INDICATE LOT CODE.

Packaging Specifications



The type of corner on carrier will vary at different assembly sites.

USER DIRECTION OF FEED

QUANTITY: 3000 PIECES PER REEL

A		B		C		D		G	N
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Ref.	Ref.
326 (12.835)	330 (13.002)	1.5 (.059)	2.5 (.098)	12.8 (.504)	13.5 (.531)	20.2 (.795)	-	16.5 (.650)	102 (4.016)

A_0		B_0		D		D_1		E		F	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	max.
4.3 (.169)	4.5 (.177)	6.7 (.264)	6.9 (.272)	1.5 (.059)	1.6 (.063)	1.5 (.059)	-	1.65 (.065)	1.85 (.073)	7.4 (.291)	7.6 (.299)

K_0		P		P_0		P_2		R		t	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
1.0 (.039)	1.2 (.047)	7.9 (.311)	8.1 (.319)	3.9 (.159)	4.1 (.161)	1.9 (.075)	2.1 (.083)	0 (0)	0.5 (.020)	0.25 (.010)	0.35 (.014)

W	
Min.	Max.
15.7 (.618)	16.3 (.642)

DIMENSIONS: MM (INCHES)

09/20

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