PO3VT3245A

Translator Bus Switch Hot Insertion 8-Bit, 2-Port, Low Voltage

High Bandwidth Potato Chip

FEATURES:

- Patented technology
- High signal -3db passing bandwidth at 1.15GHz
- Near-Zero propagation delay
- VCC = 1.65V to 3.6V
- Ultra-Low Quiescent Power: 0.1μA typical
- Ideally suited for low power applications
- 3.3V to 2.2V level shifting
- 2.5V to 1.6V level shifting
- Industrial operating temperature: -40°C to +85°C
- Available in 20pin TSSOP package

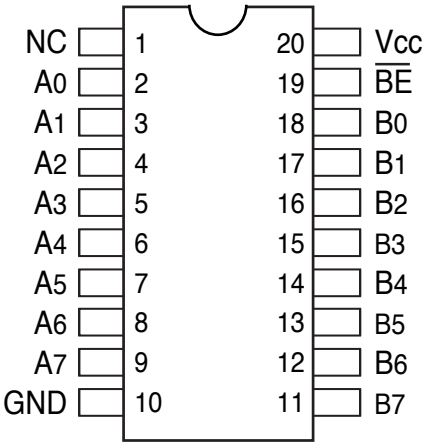
DESCRIPTION:

Potato Semiconductor's PO3VT3245A is designed for world top performance using submicron CMOS technology to achieve GHz high bandwidth.

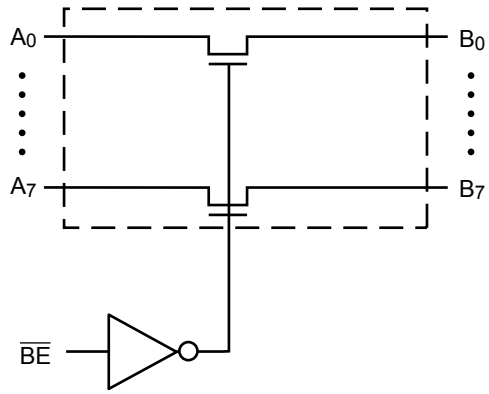
The PO3VT3245A is a Hot Insertion 8-Bit, 2-Port, Signal Voltage Translator Bus Switch. PO3VT3245 can do voltage level shifting from 3.3V to 2.2V or 2.5V to 1.6V.

The switch introduces no additional ground bounce noise or propagation delay.

Pin Configuration



Block Diagram



Pin Description

Pin Name	Description
$\overline{BE}$	Bus Enable Input (Active LOW)
A0–7	Bus A
B0–7	Bus B
GND	Ground
V_{CC}	Power

Truth Table

Function	$\overline{BE}$	A0–7
Disconnect	H	Hi-Z
Connect	L	B0–7

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential	–0.5V to +4.6V
DC Input Voltage	–0.5V to +Vcc
DC Output Current	120mA
Power Dissipation	0.5W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics, 3.3V Supply

(Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 3.3V ±10%)

Parameters	Description	Test Conditions	Min.	Typ.	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level			0.8	
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}			±1	µA
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND			±1	
I_{OZH}	High Impedance Output Current	0 ≤ Y, In ≤ V_{CC}			±1	
R_{ON}	Switch On-Resistance	V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = -8 mA		10	14	Ω
		V_{CC} = 2.3V, V_{IN} = 0.0V, I_{ON} = -8 mA		11	15	
Vp	Pass Voltage	V_{IN} = V_{CC} = 3.3V, I_{OUT} = -5µA	2.1	2.2	2.7	V
		V_{IN} = V_{CC} = 2.5V, I_{OUT} = -5µA		1.6		

Power Supply Characteristics

Symbol	Description	Test Conditions (1)	Min	Typ	Max	Unit
Icc	Quiescent Power Supply Current	Vcc=Max, Vin=Vcc or GND	**-**	**0.1**	**3**	**uA**

Notes:
1. For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at Vcc = 3.3V, 25°C ambient.
3. This parameter is guaranteed but not tested.
4. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
5. VoH = Vcc – 0.6V at rated current

Capacitance (T_A = 25°C f = 1 MHz)

Parameters	Description	Test Conditions	Typ.	Units
C_{IN}	Input Capacitance	V_{IN}=0V	3.0	pF
C_{OFF}	A/B Capacitance, Switch Off	V_{IN}=0V	3.7	pF
C_{ON}	A/B Capacitance, Switch On	V_{IN}=0V	7.4	pF

Dynamic Electrical Characteristics Over the Operating Range

(T_A = -40º to +85º, V_{CC} = 3.3V ± 10%)

Parameter	Description	Test Condition	Typ.	Units
X_{TALK}	Crosstalk	R_L = 50Ω	-60	dB
O_{IRR}	Off-Isolation	R_L = 50Ω	-60	
BW	-3dB Bandwidth	R_L = 50Ω	1.15	GHz

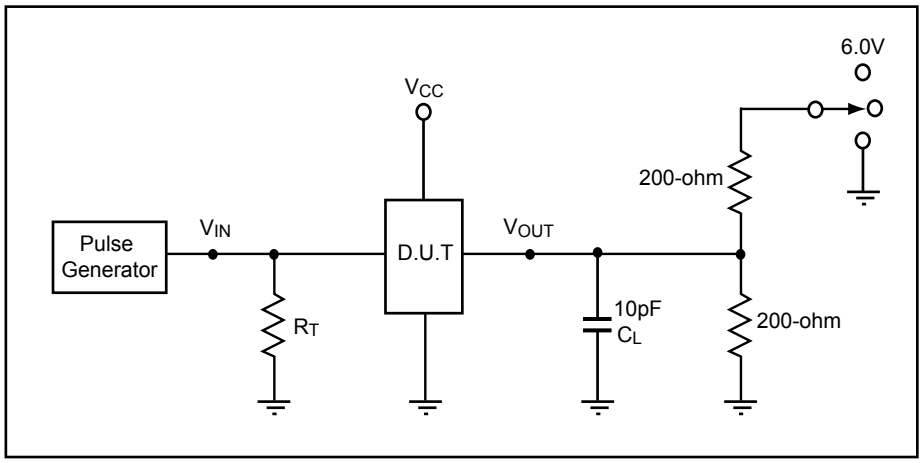
Switching Characteristics over 3.3V Operating Range

Parameters	Description	Conditions	Max.	Units
t_{PLH} t_{PHL}	Propogation Delay	See Test Diagram	0.3	ns
t_{PZH} t_{PZL}	Bus Enable Time	See Test Diagram	2.0	
t_{PHZ} t_{PLZ}	Bus Disable Time		3.0	

Switching Characteristics over 2.5V Operating Range

Parameters	Description	Conditions	Max.	Units
t_{PLH} t_{PHL}	Propogation Delay	See Test Diagram	0.3	ns
t_{PZH} t_{PZL}	Bus Enable Time	See Test Diagram	2.0	
t_{PHZ} t_{PLZ}	Bus Disable Time		3.0	

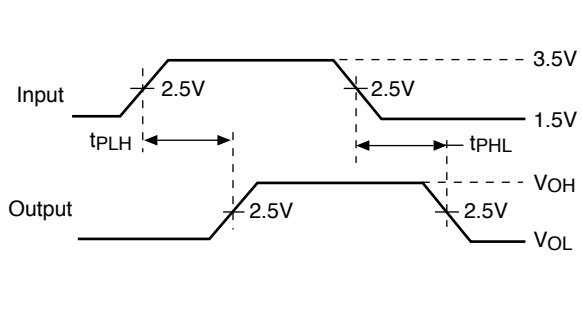
Test Circuit for Electrical Characteristics



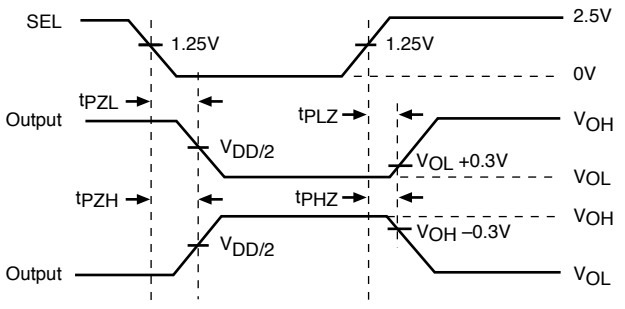
Switch Positions

Test	Switch
t_{PLZ}, t_{PZL}	6.0V
t_{PHZ}, t_{PZH}	GND
Prop Delay	Open

Switching Waveforms

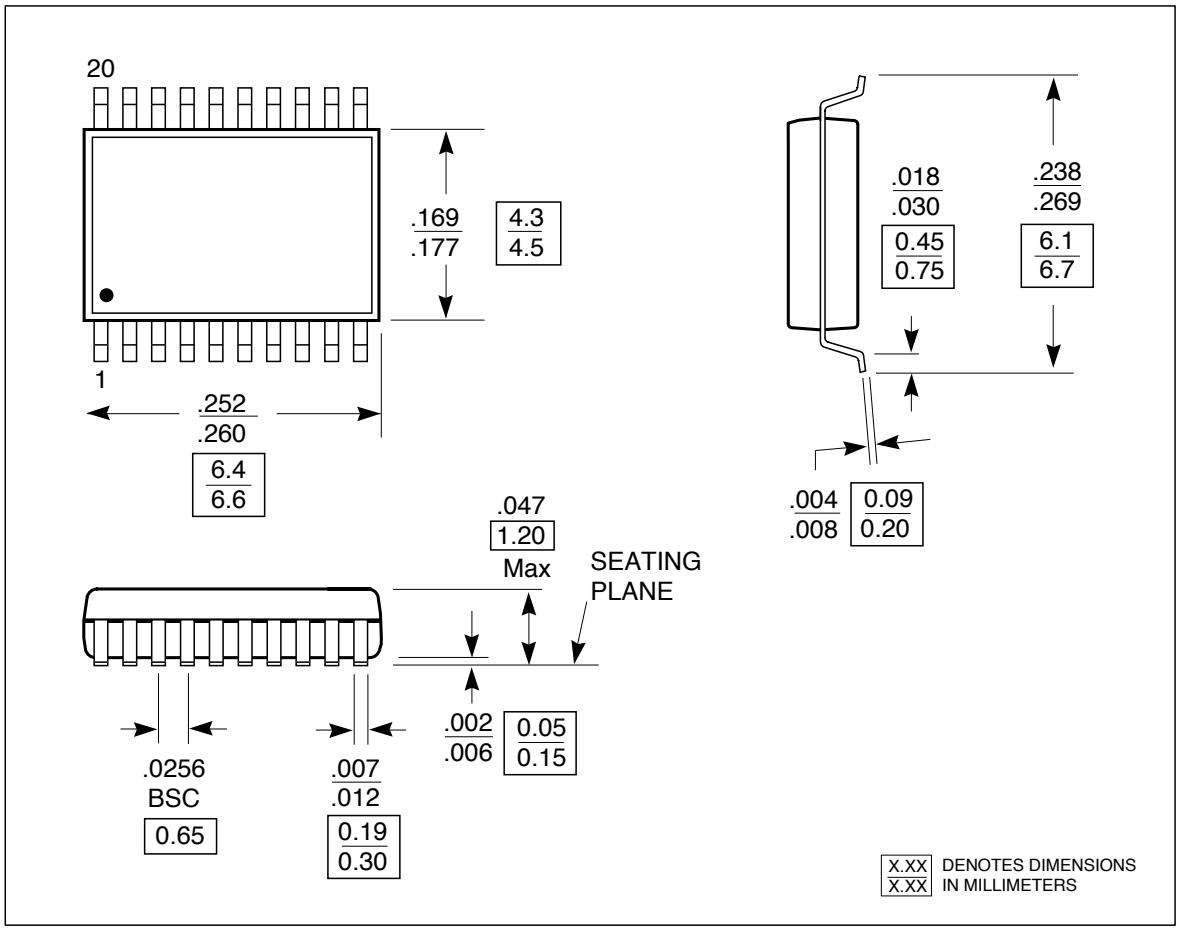


Voltage Waveforms Propagation Delay Times



Voltage Waveforms Enable and Disable Times

Packaging Mechanical Drawing: 20 pin TSSOP



IC Ordering Information

Ordering Code	Package		Top-Marking	T_A
PO3VT3245ATU for Tube	20-pin TSSOP	Pb-free & Green	PO3VT3245AT	-40°C to 85°C
PO3VT3245ATR for Tape & Reel	20-pin TSSOP	Pb-free & Green	PO3VT3245AT	-40°C to 85°C

IC Package Information

PACKAGE CODE	PACKAGE TYPE	TAPE WIDTH (mm)	TAPE PITCH (mm)	PIN 1 LOCATION	TAPE TRAILER LENGTH	QTY PER REEL	TAPE LEADER LENGTH	QTY PER TUBE
T	TSSOP 20	16	8	Top Left Corner	39 (12")	3000	64 (20")	74