

# APPROVAL SHEET

TTL02 · TTL04 · TTL06 · TTL08

±1%, ±0.5%

# **High Power Thin Film Current Sensor**

(RoHS Compliant > Halogen Free)

Size:0201 · 0402 · 0603 · 0805







#### **FEATURES**

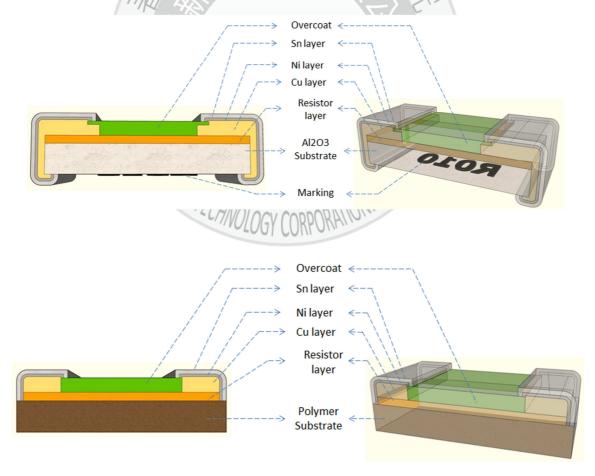
- 1. Extra high power rating and low TCR.
- 2. Extra low resistance and high precision.
- 3. High component and equipment reliability
- 4. Low resistances applied to current sensing
- 5. RoHS compliant & Halogen Free.
- 6. Suitable for lead free soldering.

#### **APPLICATIONS**

- Current sensor
- Medical equipment
- Measuring instrument
- Communication device
- Power supply
- Computer

#### **DESCRIPTION**

This specification describes TTL series current sensor – Extra high power and low TCR with lead-free terminations made by metal film or metal foil with substrate.



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## **Quick Reference Data**

			Functional code					Resistance	Resistance		
Series	Size			Р	ower(	W)			TCR	Range	Tolerance
		Н	I	J	K	М	Р	Q	( ppm/°C)	$m\Omega$	%
TTL	0201 (0603)	1/8	1/5	1/4	1/3				U: ±350	5~20 mΩ	±1%(F)
TTL	0402 (1005)			1/4	1/3	1/2			Q: ±150	2.5 mΩ	±1%(F)
TTL	0402 (1005)			1/4	1/3	1/2			P: ±100	5~20 mΩ	±1%(F)
TTL	0603 (1608)		1/5	1/4	1/3	1/2			Q: ±150	2 ~4 mΩ	±1%(F)
TTL	0603 (1608)			1/4	1/3	1/2	] 几 /	TE	O : ±75	5 ~9 mΩ	±1%(F)
TTL	0603 (1608)		1×1	1/4	1/3	1/2	又じ		O: ±75	10 ~20 mΩ	±0.5%(D) ±1%(F)
TTL	0805 (2012)		11/4	1/4	1/3	1/2			Q: ±150	2~4 mΩ	±1%(F)
TTL	0805 (2012)	5	3	1/4	1/3	1/2	YSTEM	ALLI	O : ±75	5~9 mΩ	±1%(F)
TTL	0805 (2012)	1	DURICH.	1/4	1/3	1/2			O : ±75	10~20 mΩ	±0.5%(D) ±1%(F)

#### Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by  $RCWV = \sqrt{Rated\ Power \times Resistance\ Value}$

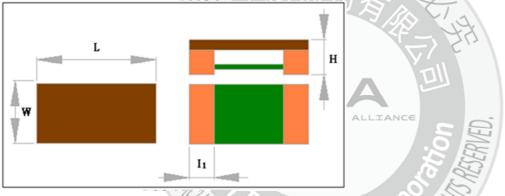


## **DIMENSIONS:(unit:mm)**

### 0201/0402 Series

Туре	Resistance Range(m $\Omega$ )	L(mm)	W(mm)	H(mm)	l1(mm)
TTL02HU TTL02IU TTL02JU TTL02KU	5~20	0.60±0.03	0.31±0.04	Max:0.30	0.14±0.16
TTL04JQ TTL04KQ TTL04MQ	2.5	1.00±0.10	0.55±0.10	Max:0.35	0.25±0.10
TTL04JP TTL04KP TTL04MP	5~20	1.00±0.10	0.55±0.10	Max:0.35	0.25±0.10

## TTL02HU(IU · JU · KU) · TTL04JQ(KQ · MQ) · TTL04JP(KP · MP) Series



## Marking(No mark)

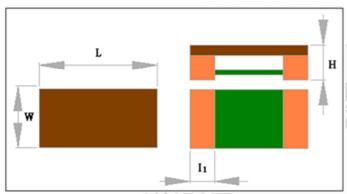


### 0603 series

Туре	Resistance Range(m $\Omega$ )	L(mm)	W(mm)	H(mm)	l1(mm)
TTL06IR	2	1.60±0.25	0.80±0.25	0.40±0.25	0.45±0.20
TTL06JR TTL06KR	2.5~3	1.60±0.25	0.80±0.25	0.40±0.25	0.35±0.20
TTL06MR	4	1.60±0.25	0.80±0.25	0.40±0.25	0.30±0.20
TTL06JO TTL06KO	5~14	1.60±0.20	0.90±0.20	0.65±0.20	0.50±0.20
TTL06MO	15~20	1.60±0.20	0.90±0.20	0.65±0.20	0.40±0.20

PASSIVE SYSTEM ALLIANCE

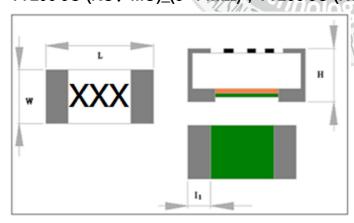
## TTL 06IR(JR、KR、MR)\_( $2\sim4m\Omega$ )



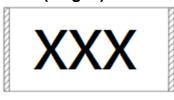
## Marking(No mark)



TTL06 JO (KO、MO)\_(5~14m $\Omega$ ); TTL06 JO (KO、MO)\_(15~20m $\Omega$ )



### Mark (3digits)



 $(010=10 \text{ m}\Omega)$ 

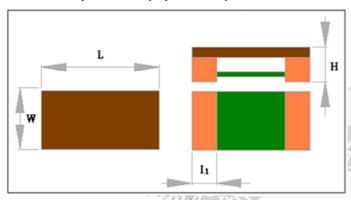
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### 0805 series

Туре	Resistance Range(m $\Omega$ )	L(mm)	W(mm)	H(mm)	l1(mm)
TTL08JQ TTL08KQ TTL08MQ	2~4	2.00±0.25	1.25±0.25	0.30±0.25	0.65±0.25
TTL08JO	5~9	2.03±0.20	1.27±0.20	0.65±0.20	0.55±0.20
TTL08KO TTL08MO	10~20	2.03±0.20	1.27±0.20	0.65±0.20	0.50±0.20

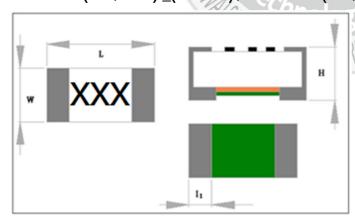
## TTL08 JQ(KQ, MQ)\_( $2\sim4$ m $\Omega$ )



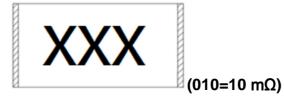
## Marking(No mark)



## TTL08 JO(KO、MO) $\_(5\sim9m\Omega)$ 、TTL08 JO(KO、MO) $\_(10\sim20m\Omega)$



## Mark (3digits)



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## **FUNCTIONAL DESCRIPTION**

#### **Product characterization**

Power rating is based on continuous full load operation at rated ambient temperature of 70 °C. For resistors operated at ambient temp. in excess of 70 °C, the maximum load shall be derated in accordance with the following curve.

The power that the resistor can dissipate depends on the operating temperature; see Fig.1&Fig2

Temperature range of size 0201: -55℃ to +125℃ (Fi g.1)

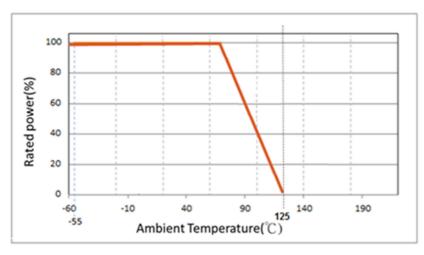


Fig. 1 Maximum dissipation in percentage of rated power.
As a function of the ambient temperature.

Temperature range of size 0402 to 0805: -55℃ to +1 55℃ (Fig.2)

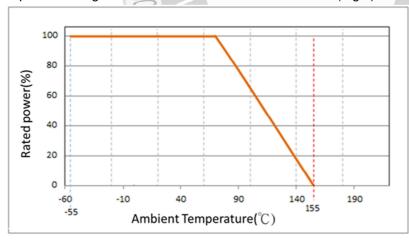


Fig. 2 Maximum dissipation in percentage of rated power. As a function of the ambient temperature.



#### **MOUNTING**

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

#### **SOLDERING CONDITION**

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds within lead-free solder bath. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering profile and condition that provide reliable joints without any damage are given in Fig 3. and Table 1.

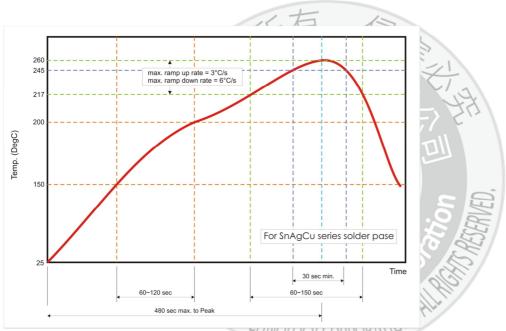


Fig. 3 Infrared soldering profile for Chip Resistors

Table 1. Infrared soldering condition for Chip Resistors

Temperature Condition	Exposure Time
Average ramp-up rate (217°C to 260°C)	Less than 3°C/second
Between 150 and 200°C	Between 60-120 seconds
> 217°C	Between 60-150 seconds
Peak Temperature	260°C +0/-5°C
Time within 245°C	Min. 30 seconds
Ramp-down rate (Peak to 217°C)	Less than 6°C/second
Time from 25°C to Peak	No greater than 480 seconds

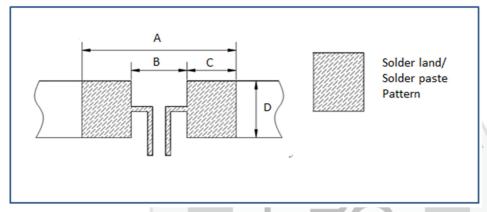
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## **CATALOGUE NUMBERS**

TTL	06	Р	0	XXXX	F	Т	L
Type code	Size code	Power Rating	TCR	Resistance	Tolerance	Packaging code	Termination code
Low	02:0201	M:1/2 W	N:50 ppm/°C	e.q	D: ±0.5%	T:7" Taped	L:Sn Base
Resistance	04:0402	K:1/3 W	<b>O:75</b> ppm/℃	R003=3mΩ	F: ±1%	Reeled	(Lead free)
	06:0603	J: 1/4W	P:100 ppm/℃	R010=10mΩ			
	08:0805	I:1/5W	Q:150 ppm/°C	R2L5=2.5mΩ			
		H:1/8 W	R:200 ppm/°C				
			U:350 ppm/°C				

### **Recommend Solder Pad Dimensions**



## Dimensions of solder Pad : PASSIVE SYSTEM ALLIANCE

Туре	Resistance Range	A(mm)	B(mm)	C(mm)	D(mm)
TTL02	5~20m $\Omega$	0.65	0.25	0.20	0.33
TTL04	2.5~20 m Ω	1.60	0.40	0.6	0.60
TTL06	2~4 m $\Omega$	3.20	0.50	1.35	0.92
TTL06	5~20mΩ	3.20	0.60	1.30	0.92
TTL08	2~4 m $\Omega$	3.60	0.50	1.55	1.44
TTL08	5~20 m $\Omega$	3.60	0.80	1.40	1.44



# **TEST AND REQUIREMENTS(JIS C 5201-1: 1998)**

T	r	r
TEST	PROCEDURE	REQUIREMENT
TEST	PROCEDURE	Resistor
DC resistance IEC 60115-1 / JIS C 5201-1 , Clause 4.5	D: ±0.5%,F: ±1%,	Within the specified tolerance
Temperature Coefficient of Resistance(T.C.R)	Natural resistance change per change in degree centigrade. $\frac{R_2-R_1}{R_1(t_2-t_1)}\!\!\times\!10^6 \qquad \qquad \text{(ppm/°C)}$ $R_1: \text{Resistance at reference temperature}$ $R_2: \text{Resistance at test temperature}$ $t_1: 20\text{C}+5\text{C}-1\text{C}$ $t2: 125\text{C}+5\text{C}-1\text{C}$	Refer to " QUICK REFERENCE DATA "
Short time overload (S.T.O.L) IEC60115-1 4.13	5 times of rated power for 5 seconds at room temperature	No visible damage $\pm (1.0\% + 0.0005\Omega)$
Resistance to soldering heat(R.S.H) MIL-STD-202G- method 210F IEC 60115-1 4.18	Condition B, no pre-heat of samples Lead free solder, 260 ℃, 10 seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol  PASSIVE SYSTEM ALLIANCE	No visible damage ±(0.5%+0.0005□)
Solderability IPC/JEDEC J-STD-002B test B	SMD conditions:  1st step: method B, aging 4 hours at 155 ℃ dry heat  2nd step: leadfree solder bath at 245±3 ℃  Dipping time: 3± 0.5 seconds	good tinning (>95% covered) no visible damage
Thermal Shock MIL-STD-202G- method 107	-55/+125 °C Note: Number of cycles required is 300. Devices mounted Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	±(1.0%+0.0005Ω)
Endurance MIL-STD-202G- method 108 IEC 60115-1 4.25.1	70±2°C, 1000 hours, loaded with RCWV,1.5 hours on and 0.5 hours off	±(2.0%+0.0005Ω)
Bending Strength IEC60115-1 4.33	Device mounted on PCB test board as described, only 1 board bending required Bending for 0201: 3mm 0402 and above: 2mm Holding time: minimum 60 seconds	±(1.0%+0.0005Ω)

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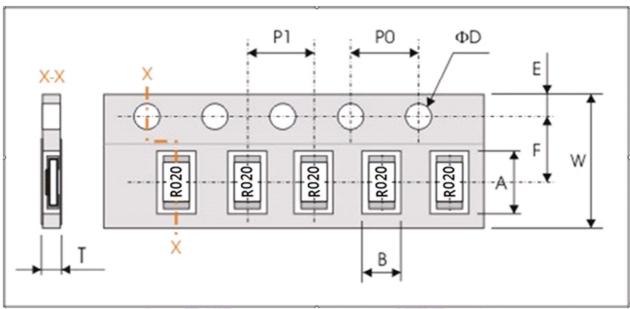
High Temperature Exposure MIL-STD-202G- method 108	1,000 hours at maximum operating temperature depending on specification, unpowered No direct impingement of forced air to the parts Tolerances: 0201: 125±3°C 0402 and above 155±3°C	±(1.0%+0.0005Ω)
Moisture Resistance MIL-STD-202G- method 106	Each temperature / humidity cycle is defined at 8 hours (method 106F), 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered Parts mounted on test-boards, without condensation on parts Measurement at 24± 2 hours after test conclusion	± (0.5%+0.0005 Ω)
Bias Humidity MIL-STD-202 Method 103	1,000 hours at 85°C/85%R.H. 10% of operating power, no condensation on the devices, circulating air.	±(1.0%+0.0005Ω)





## **PACKAGING**

## Paper Tape specifications (unit :mm)

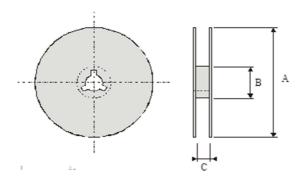


	A. A.				
Series No.	Α	В	w	F	E
TTL02	0.70±0.10	0.40±0.10	8.00±0.30	3.50±0.10	1.75±0.10
TTL04	1.250±0.20	0.75±0.20	8.00±0.30	3.50±0.10	1.75±0.10
TTL06	1.90±0.20	1.10±0.20	8.00±0.30	3.50±0.20	1.75±0.10
TTL08	2.40±0.20	1.65±0.20	8.00±0.30	3.50±0.20	1.75±0.10

Series No.	P1	P0	ΦD	Т
TTL02	2.00±0.10	4.00±0.10	Ф1.50 <sup>+0.1</sup>	Max 0.5
TTL04	2.00±0.10	4.00±0.10	Ф1.50 <sup>+0.1</sup>	Max 0.6
TTL06	4.00±0.10	4.00±0.10	Ф1.50 <sup>+0.1</sup>	Max 1.0
TTL08	4.00±0.10	4.00±0.10	Ф1.50 <sup>+0.1</sup> <sub>-0.0</sub>	Max 1.1



#### **Reel dimensions**



Symbol	А	В	С
	(unit : mm)	(unit : mm)	(unit : mm)
(unit : mm)	Φ178.0±5.0	Φ60.0±2.0	9.0±0.5

#### **Taping quantity**

- Chip resistors 5,000 pcs per reel (TTL08, TTL06)
- Chip resistors 10,000 pcs per reel (TTL04)
- Chip resistors 15,000 pcs per reel (TTL02)

