Leiditech

2R3600TZ-5

Features	Applications
● Extremely small size ● Extremely fast response time	● Splitter
● Excellent SMD handing	● PCI Cards
● Stable performance over life	● Moden
● Very low capacitance	● Line cards
● High insulation resistance	
● Storage and operating temperature -40…+125°C	
● RoHS-compatible	
● UL-identification	

Electrical specifications

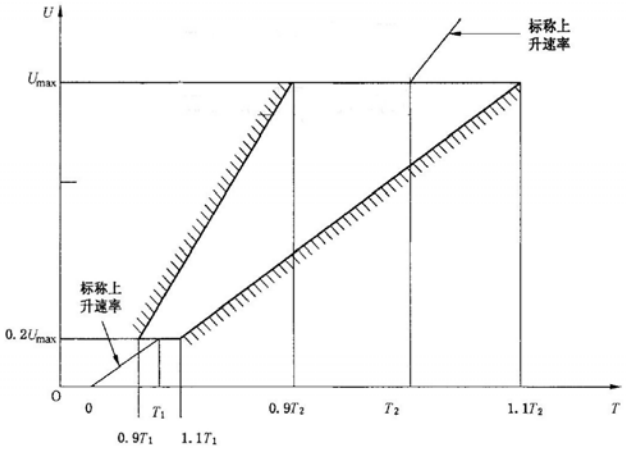
Part Number	DC spark-over Voltage	Max. Impulse Breakdown Voltage	Discharge Current (8/20us)	AC discharge Current	Impulse Life (8/20us)	Minimum Insulation Resistance		Max. Capacitance 1MHz
	100V/S	1KV/us	10 times	50Hz,1S	100A			
	%	V	KA	A	Times	Test Voltage DC(V)	(GΩ)	(Pf)
2R3600TZ-5	±20	4500	3	3	300	100	1	1

Arc voltage at 1A..... 35V

AC Withstanding Voltage ,1S..... 1800V

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DC breakdown voltage



8/20us, Test wave

T1=1.25T=8us±20%

T2=20us±20%

10/700us,Test Wave

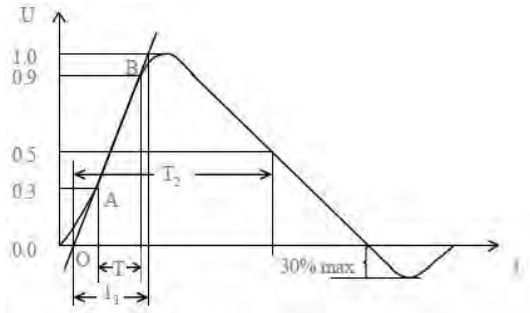
T1=1.67T=10us±20%

T2=700us±20%

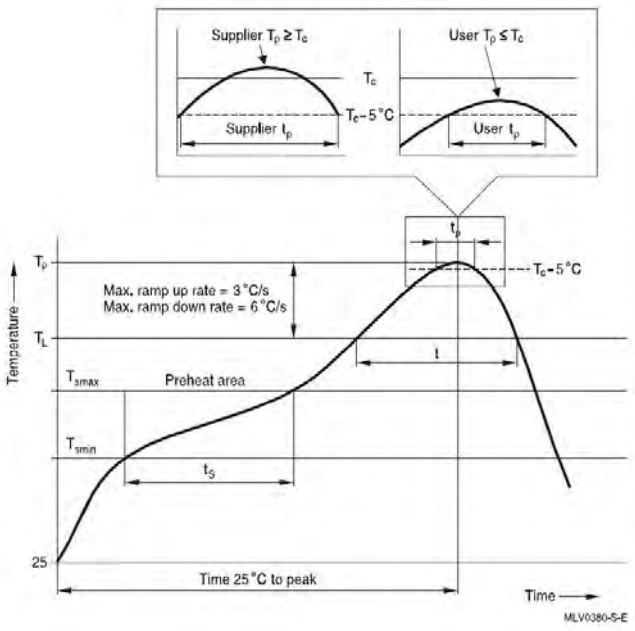
10/1000us,Test Wave

T1=1.67T=10us±20%

T2=1000us±20%



Recommended wave soldering profile

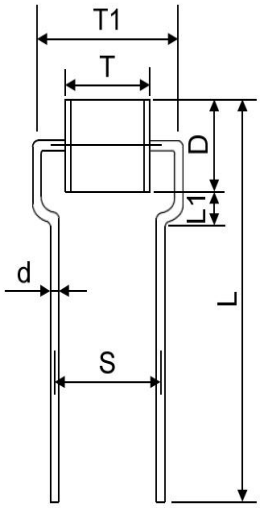


Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	T_{smin} T_{smax} t_{smin} to t_{smax}	100 °C 150 °C 60 ... 120 s	150 °C 200 °C 60 ... 180 s
Average ramp-up rate	T_{smax} to T_p	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	T_L t_L	183 °C 60 ... 150 s	217 °C 60 ... 150 s
Peak package body temperature *, Classification temperature **	T_p, T_C	220 ... 235 °C **	245 ... 260 °C **
Time (t_p) ** within 5 °C of the specified classification temperature (T_C)		20 s ***	30 s ***
Average ramp-down rate	T_p to T_{smax}	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

* = Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.
** = For details please refer to JEDEC J-STD-020D.
*** = Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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1) Sampling size in accordance to AQL(C=0)
2) DC spark-over voltage ±30% after load
3) Tests according to ITU-T Rec. K. 12 and IEC61643-1



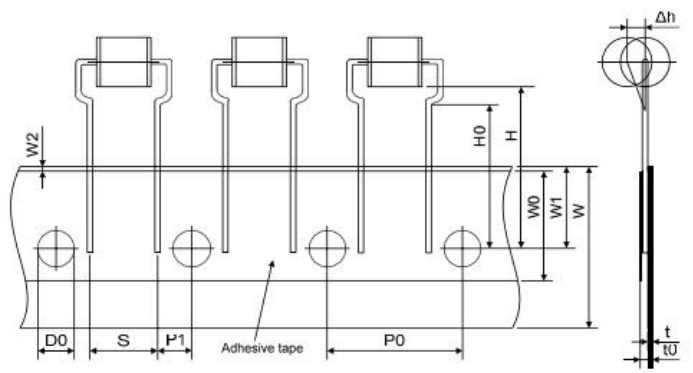
Symbol	Dimension (mm)	
	Spec.	Tolerance
D	5.5	+0.3, -0.5
T	6.0	+0.3, -0.5
T1	10.2	±0.5
S	7.5	±0.5
d	0.6	±0.05
L	25.0	(0, -2)
L1	2.5	±0.3

Wire Tin-plated

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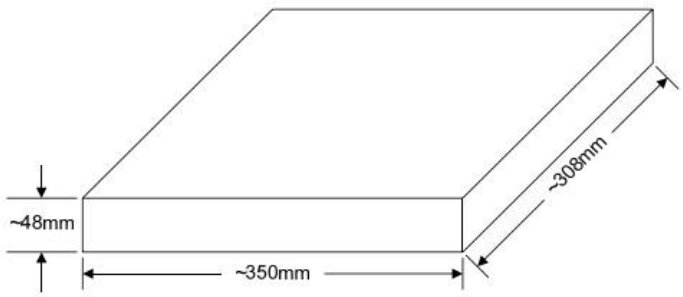
Packaging

Tape



Symbol	Dimension (mm)
H	20(+2, -1)
H0	18.0±0.5
P0	15.0±0.3
P1	3.75
S	7.5±0.5
D0	4.0±0.2
W	18(+1.0, -0.5)
W0	12.7
W1	9.1(+0.75, -0.5)
W2	0~3
t	0.5(+0.05, -0.10)
t0	1.5Max.
Δh	±2Max.

Box (Quantity: 800pcs)



Cautions and warnings

- Surge arresters must not be operated directly in power supply networks
- Surge arresters may become hot in case of longer periods of current stress (danger of burning).
- If the contacts of the surge arrester are defective, current stress can lead to the formation of sparks and loud noises.
- Surge arresters may be used only within their specified values. In case of overload, the head contacts may fail or the component may be destroyed.
- Damaged surge arresters must not be re-used.

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