

12N65

Power MOSFET

12A, 650V N-CHANNEL POWER MOSFET

■ DESCRIPTION

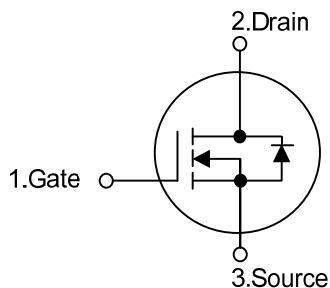
The UTC **12N65** are N-Channel enhancement mode power field effect transistors (MOSFET) which are produced by using UTC's proprietary, planar stripe and DMOS technology.

These devices are suited for high efficiency switch mode power supply. To minimize on-state resistance, provide superior switching performance and withstand high energy pulse in the avalanche and commutation mode, the advanced technology has been especially tailored.

■ FEATURES

- * $R_{DS(ON)} < 0.85\Omega$ @ $V_{GS} = 10$ V
- * Ultra low gate charge (typical 42 nC)
- * Low reverse transfer capacitance (C_{RSS} = typical 25 pF)
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

■ SYMBOL

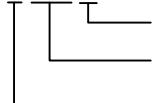


■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
12N65L-TA3-T	12N65G-TA3-T	TO-220	G	D	S	Tube
12N65L-TF1-T	12N65G-TF1-T	TO-220F1	G	D	S	Tube
12N65L-TF2-T	12N65G-TF2-T	TO-220F2	G	D	S	Tube
12N65L-TF3-T	12N65G-TF3-T	TO-220F	G	D	S	Tube
12N65L-T2Q-T	12N65G-T2Q-T	TO-262	G	D	S	Tube
12N65L-T3P-T	12N65G-T3P-T	TO-3P	G	D	S	Tube

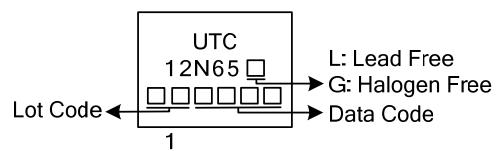
Note: Pin Assignment: G: Gate D: Drain S: Source

12N65L-TA3-T



- (1) T: Tube
- (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2
- TF3: TO-220F, T2Q: TO-262, T3P: TO-3P
- (3) L: Lead Free, G: Halogen Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	± 30	V
Avalanche Current (Note 2)		I_{AR}	12	A
Drain Current	Continuous	I_D	12	A
	Pulsed (Note 2)	I_{DM}	48	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	790	mJ
	Repetitive (Note 2)	E_{AR}	24	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220 / TO-262	P_D	225	W
	TO-220F / TO-220F1		51	W
	TO-220F2		54	W
	TO-3P		260	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Operating Temperature		T_{OPR}	-55 ~ +150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature

3. $L = 10\text{mH}$, $I_{AS} = 12\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 12\text{A}$, $dI/dt \leq 200\text{A/s}$, $V_{DD} \leq BV_{DSS}$ Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT		
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	$^\circ\text{C/W}$		
	TO-220F1/TO-220F2					
	TO-262					
Junction to Case	TO-3P		40	$^\circ\text{C/W}$		
	TO-220 / TO-262	θ_{JC}				
	TO-220F/TO-220F1					
	TO-220F2					
	TO-3P	0.48	$^\circ\text{C/W}$			

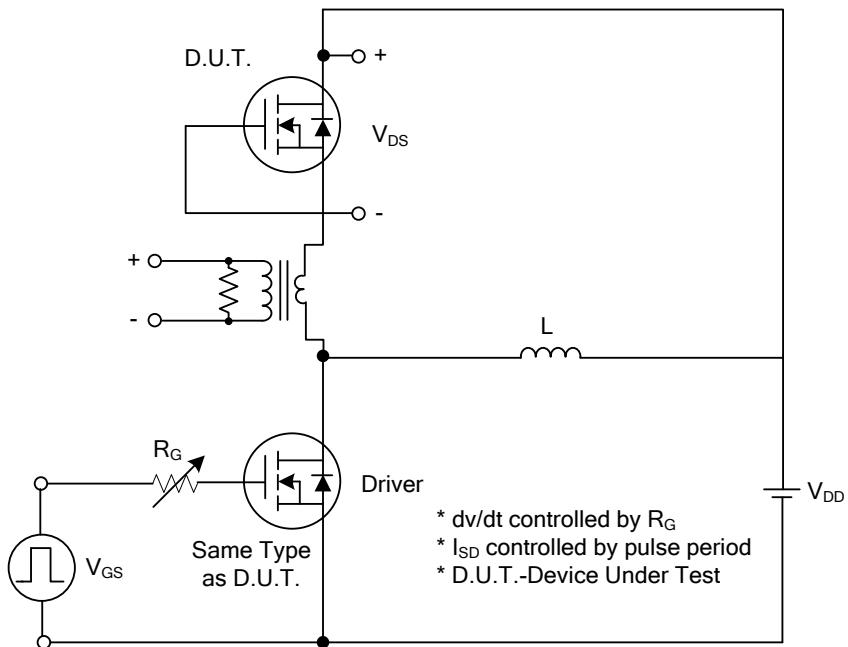
■ ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{D}} = 250 \mu\text{A}$	650			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 650 \text{ V}, V_{\text{GS}} = 0 \text{ V}$		1		μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}} = \pm 30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			± 100	nA
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}} = 250 \mu\text{A}$, Referenced to 25°C	0.7			$\text{V}/^\circ\text{C}$
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250 \mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 6.0 \text{ A}$		0.65	0.85	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$		1480	1900	pF
Output Capacitance	C_{OSS}			200	270	pF
Reverse Transfer Capacitance	C_{RSS}			25	35	pF
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}} = 325 \text{ V}, I_{\text{D}} = 12 \text{ A}, R_{\text{G}} = 25 \Omega$ (Note 1, 2)		30	70	ns
Turn-On Rise Time	t_{R}			115	240	ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			95	200	ns
Turn-Off Fall Time	t_{F}			85	180	ns
Total Gate Charge	Q_{G}	$V_{\text{DS}} = 520 \text{ V}, I_{\text{D}} = 12 \text{ A}, V_{\text{GS}} = 10 \text{ V}$ (Note 1, 2)		42	54	nC
Gate-Source Charge	Q_{GS}			8.6		nC
Gate-Drain Charge	Q_{GD}			21		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{S}} = 12 \text{ A}$			1.4	V
Maximum Continuous Drain-Source Diode Forward Current	I_{S}				12	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				48	A
Reverse Recovery Time	t_{RR}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{S}} = 12 \text{ A}, dI_{\text{F}}/dt = 100 \text{ A}/\mu\text{s}$ (Note 1)		380		ns
Reverse Recovery Charge	Q_{RR}			3.5		μC

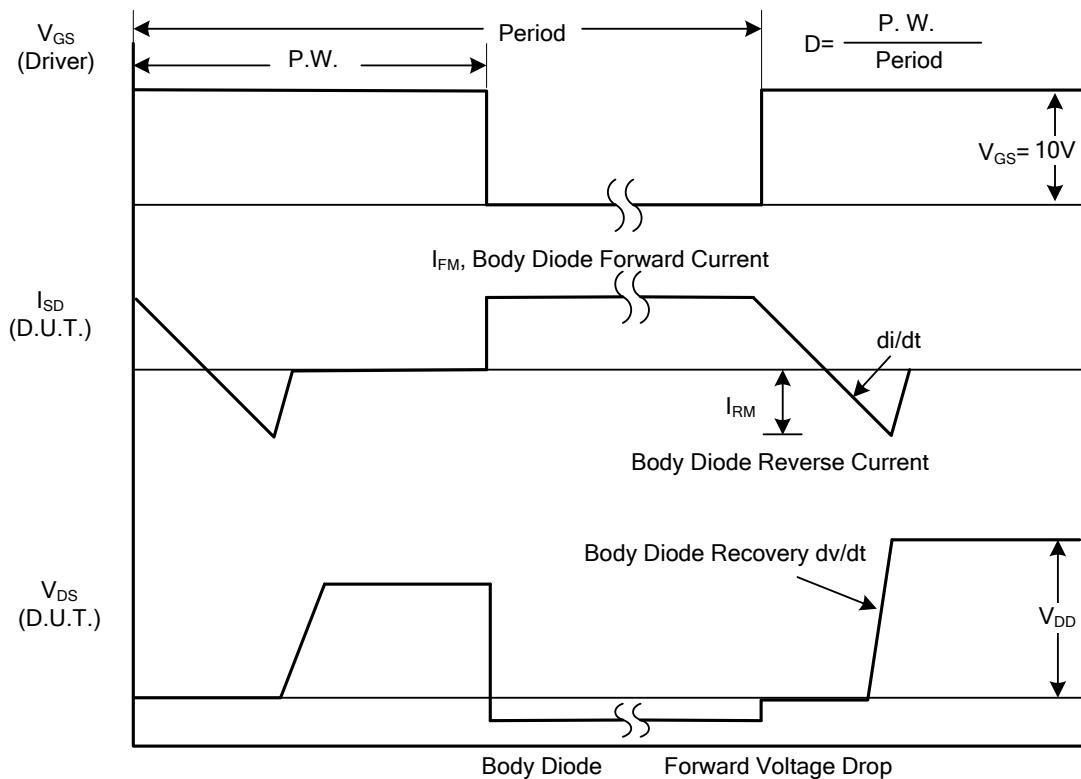
Notes: 1. Pulse Test : Pulse width $\leq 300 \mu\text{s}$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

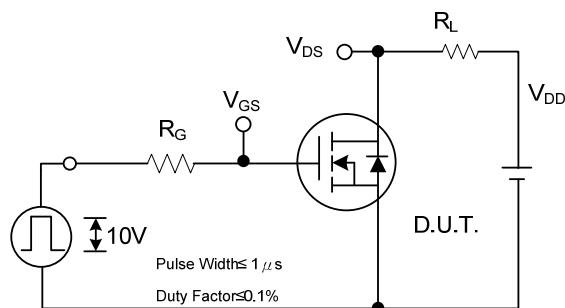


Peak Diode Recovery dv/dt Test Circuit

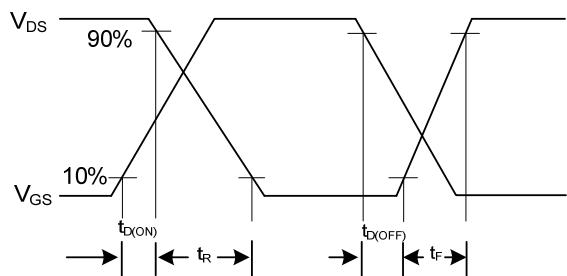


Peak Diode Recovery dv/dt Waveforms

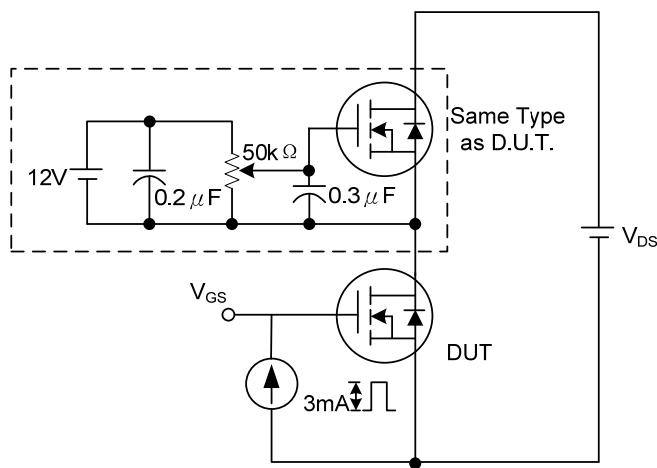
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



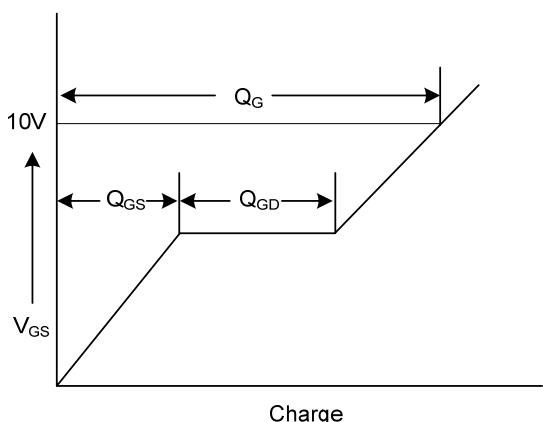
Switching Test Circuit



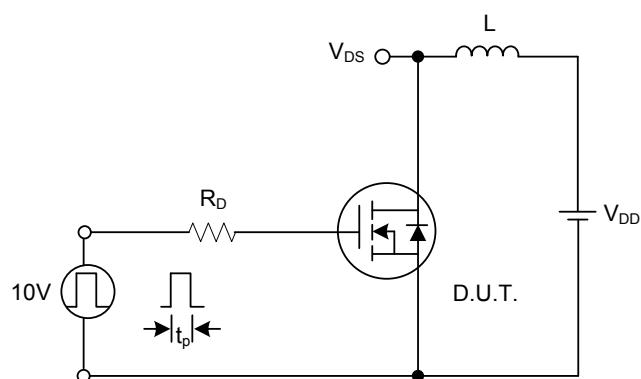
Switching Waveforms



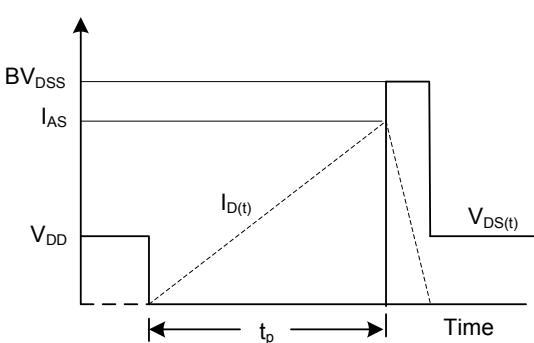
Gate Charge Test Circuit



Gate Charge Waveform

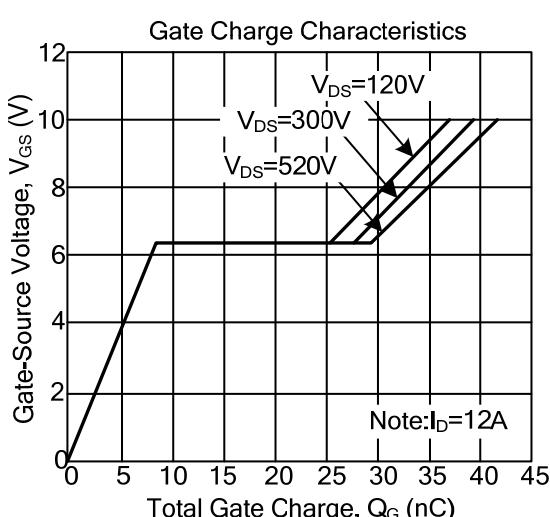
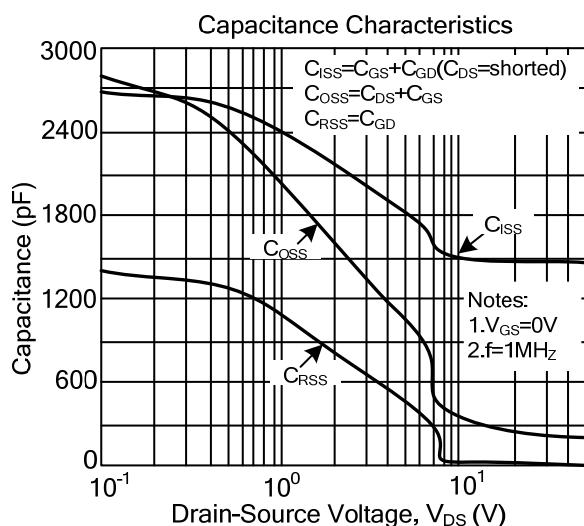
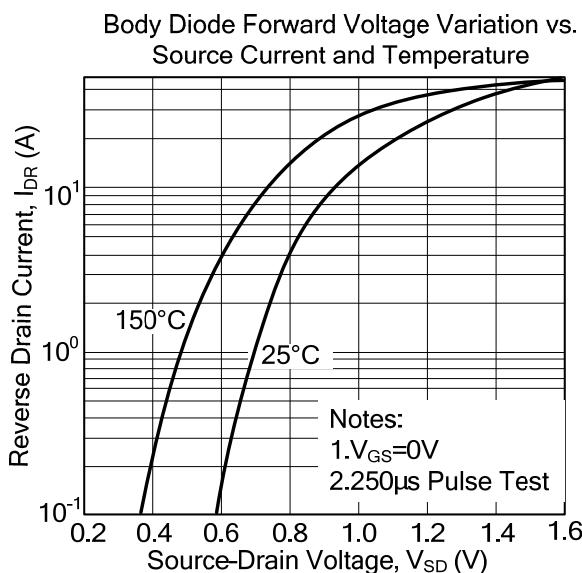
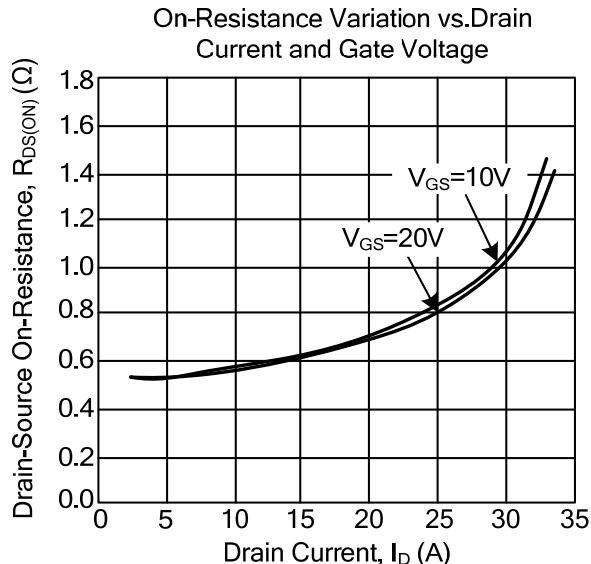
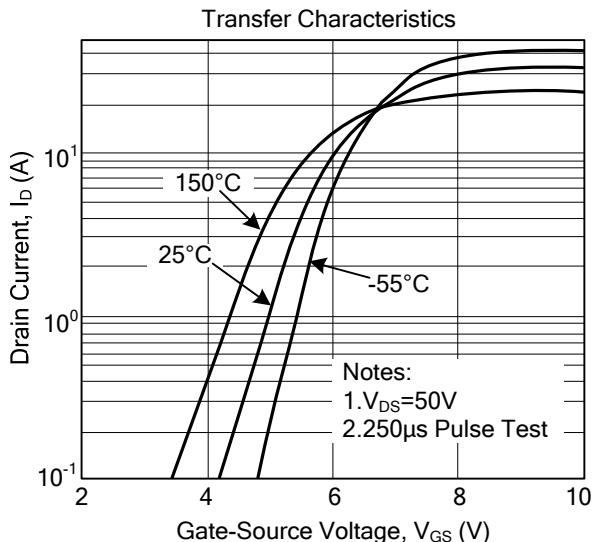
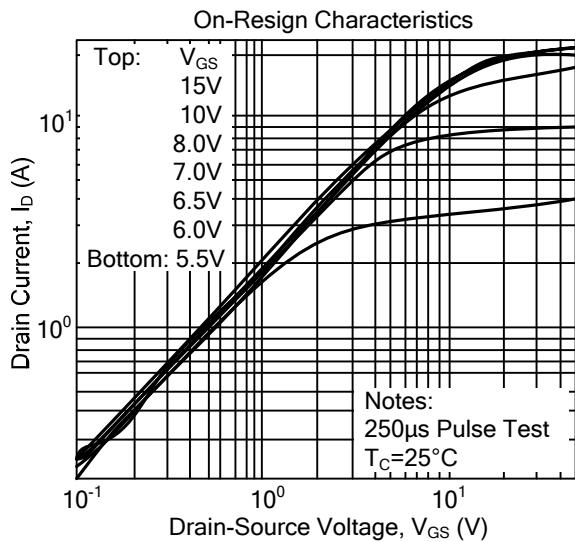


Unclamped Inductive Switching Test Circuit

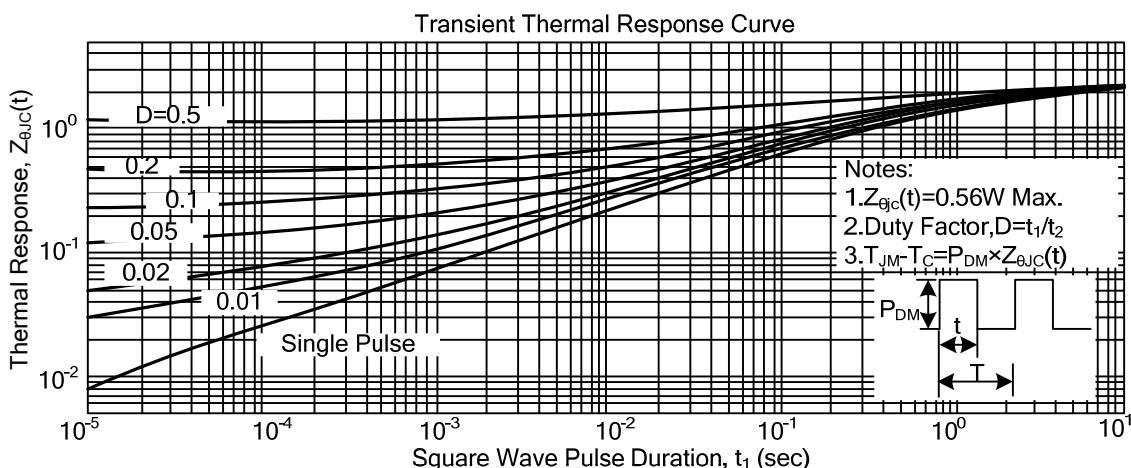
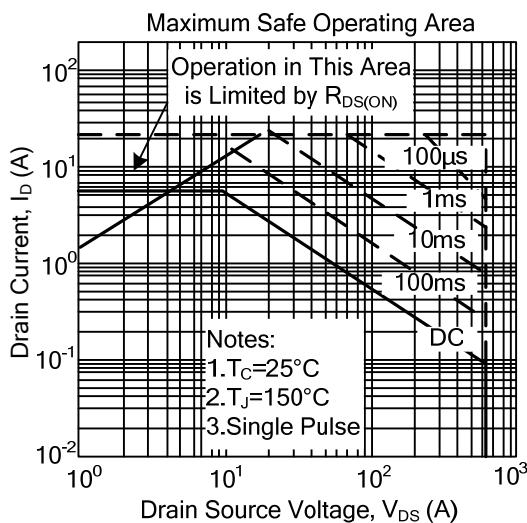


Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice.