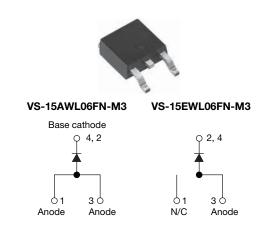


Vishay Semiconductors

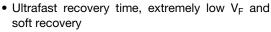
Ultralow V_F Ultrafast Rectifier, 15 A FRED Pt[®]



D-PAK	(TO-252AA)
	(10-202777)

PRODUCT SUMMARY						
D-PAK (TO-252AA)						
15 A						
600 V						
1.05 V						
60 ns						
175 °C						
Single die						

FEATURES

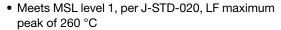






· Low leakage current

• Compliant to RoHS Directive 2002/95/EC



Halogen-free according to IEC 61249-2-21 definition

Ph





FREE

DESCRIPTION/APPLICATIONS

State of the art, ultralow V_F , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

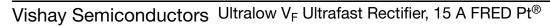
The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Peak repetitive reverse voltage	V_{RRM}		600	V					
Average rectified forward current	I _{F(AV)}	T _C = 148 °C	15						
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	180	Α					
Peak repetitive forward current	I _{FM}	$T_C = 148 ^{\circ}\text{C}, f = 20 \text{kHz}, d = 50 \%$	30						
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C					

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-				
Forward voltage	V _F	I _F = 15 A	-	0.99	1.05	V			
		I _F = 15 A, T _J = 150 °C	-	0.85	0.92				
Deverage leakens as went	I _R	$V_R = V_R$ rated	-	-	10				
Reverse leakage current		T _J = 150 °C, V _R = V _R rated	-	-	120	μA			
Junction capacitance	C _T	V _R = 600 V	-	11	=	pF			
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH			

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		$I_F = 1 A, dI_F/dt = 10$	$00 \text{ A/}\mu\text{s}, \text{ V}_{\text{R}} = 30 \text{ V}$	- 1	60	120			
	+	$I_F = 15 \text{ A}, dI_F/dt = \frac{1}{2}$	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		190	-	ns		
	t _{rr}	T _J = 25 °C		-	220	-	115		
		T _J = 125 °C	I _F = 15 A dI _F /dt = 200 A/µs	-	290	-			
Dools recovery ourrent	I _{RRM}	T _J = 25 °C		-	21	-	Α		
Peak recovery current		T _J = 125 °C	$V_{\rm R} = 390 \text{ V}$	-	25	-	A		
Reverse recovery charge	0	T _J = 25 °C		1	2.6	-	μC		
	Q_{rr}	T _J = 125 °C		-	4	-			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C			
Thermal resistance, junction to case per leg	R _{thJC}		-	1.4	1.8	°C/W			
Thermal resistance, junction to ambient per leg R _{thJA}			-	-	70	C/VV			
Approximate weight				0.3		g			
Approximate weight			0.01			OZ.			
Marking device		Casa style D. BAK (TO 252AA)	15AWL06FN						
IVIAI KII IY GEVICE		Case style D-PAK (TO-252AA)		15EWL06FN					



Ultralow V_F Ultrafast Rectifier, 15 A FRED Pt® Vishay Semiconductors

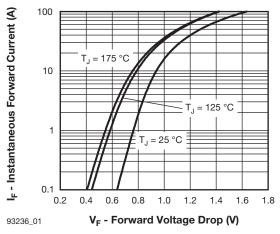


Fig. 1 - Typical Forward Voltage Drop Characteristics

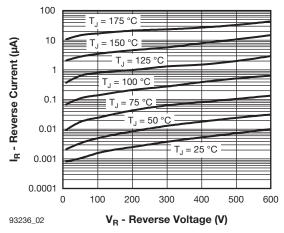


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

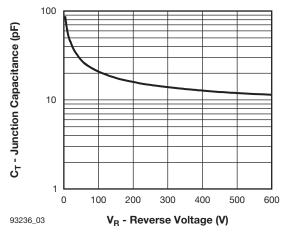


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

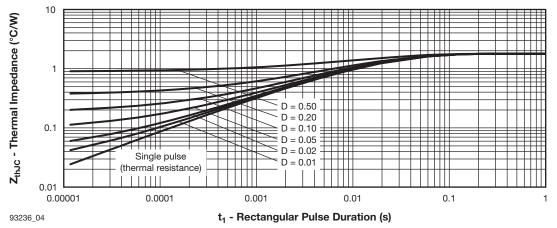


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

Vishay Semiconductors Ultralow V_F Ultrafast Rectifier, 15 A FRED Pt®



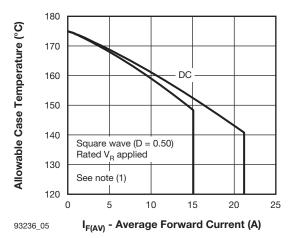


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

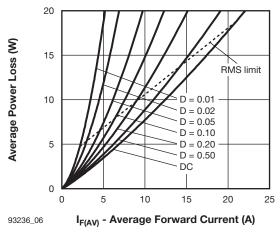


Fig. 6 - Forward Power Loss Characteristics

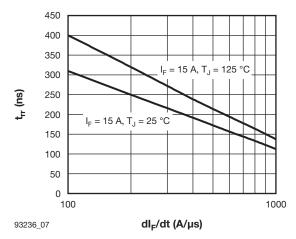


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

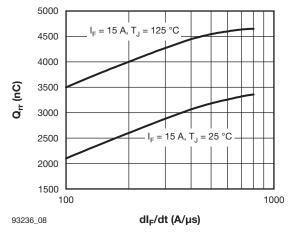


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_{R} (1 - D)$; I_{R} at V_{R1} = Rated V_{R}



Ultralow V_F Ultrafast Rectifier, 15 A FRED Pt® Vishay Semiconductors

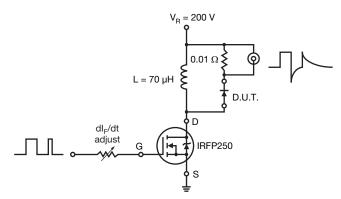
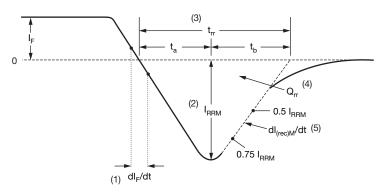


Fig. 9 - Reverse Recovery Parameter Test Circuit

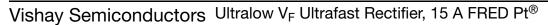


- (1) dl_E/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

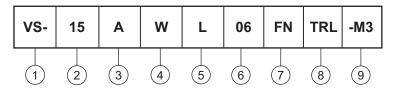
Fig. 10 - Reverse Recovery Waveform and Definitions





ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- Current rating (15 = 15 A)
- Circuit configuration:
 - A = Single diode (2 anodes)
 - E = Single diode
- Package identifier:
 - W = D-PAK
- L = Hyperfast rectifier
- Voltage rating (06 = 600 V)
- FN = TO-252AA
- None = Tube
 - TR = Tape and reel
 - TRL = Tape and reel (left oriented)
 - TRR = Tape and reel (right oriented)
- 9 Environmental digit:
 - -M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-15AWL06FN-M3	75	3000	Antistatic plastic tube					
VS-15EWL06FN-M3	75	3000	Artiistatic plastic tube					
VS-15AWL06FNTR-M3	2000	2000	13" diameter reel					
VS-15EWL06FNTR-M3	2000	2000						
VS-15AWL06FNTRL-M3	2000	3000	13" diameter reel					
VS-15EWL06FNTRL-M3	3000	3000	ro diameter reei					
VS-15AWL06FNTRR-M3	0000	0000	10 diamatan mad					
VS-15EWL06FNTRR-M3	3000	3000	13" diameter reel					

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95176					
Packaging information	www.vishay.com/doc?95033					
SPICE model	www.vishay.com/doc?95372					



Vishay Semiconductors

NOTES

3

2

MAX.

0.410

0.070

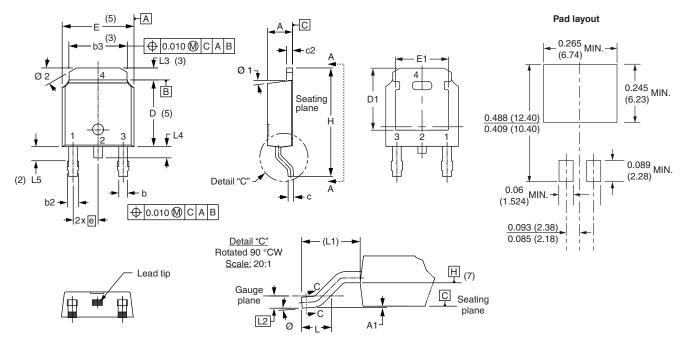
0.050

0.040

0.060

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



Ī	SYMBOL	MILLIMETERS		INCHES		NOTES	CVMDOL	MILLIMETERS		INCHES		
	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	SYMBOL	MIN.	MAX.	MIN.	MAX	
ſ	Α	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC
ſ	A1	-	0.13		0.005			Н	9.40	10.41	0.370	0.41
Ī	b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.07
Ī	b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.
ſ	b3	4.95	5.46	0.195	0.215	3		L2	0.51 BSC		0.020 BSC	
Ī	С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.05
Ī	c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.04
ſ	D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.06
Ī	D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°
ſ	Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°
Ī	E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



Legal Disclaimer Notice

Vishay

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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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