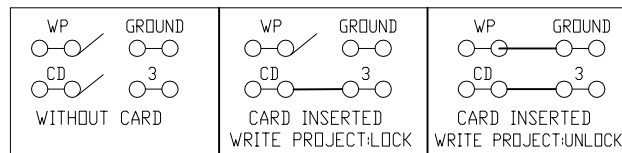


▨ Pad Area  
 ▩ Keep Out Area

PCB LAYOUT TOP VIEW  
 (TOLERANCE:±0.05)

Pin#	Name	Type	Card Detects/ Data Line[Bit#]
1	CD/DAT3	I/O/PP	Command/Response
2	CMD	PP	Supply voltage
3	Vss1	S	Supply voltage ground
4	VDD	S	Supply voltage
5	CLK	I	Clock
6	Vss2	S	Supply voltage ground
7	DAT0	I/O/PP	Date Line [Bit0]
8	DAT1	I/O/PP	Date Line [Bit1]
9	DAT2	I/O/PP	Date Line [Bit2]



**深圳市华宇创精密电子有限公司**

<b>TOLERANCE:</b> X.X ±0.30 X.XX ±0.25 X.XXX ±0.15 X' ±2' X.X' ±0.5'	<b>DRAWN BY :</b> 陈一鸣	<b>DATE :</b> 2014-02-23	<b>PART NAME:</b> SD PUSH 带锁卡
	<b>CHECKED BY:</b> 马跃	<b>DATE :</b> 2014-02-23	<b>PART NO.</b> Hycw33-SD11-288B
<b>UNIT: mm [inch]</b> <b>SCALE:1:1</b> <b>SIZE: A4</b>	<b>APPROVED BY:</b> 邱敏	<b>DATE :</b> 2014-02-23	<b>DRAW NO:</b> Hyc-SIM102010
			<b>SHEET NO.</b> 1 OF 1