3.3V SMD LV-PECL OSCILLATOR
MODEL: VCP3_AXT SERIES

RoHS Compliant

FEATURES
- 3.3V Operation
- LV-PECL
- Enable/Disable
- Tape and Reel (2,000 pcs. STD)

OPTIONS
- -40ºC ~ +85ºC Option ('R' Version)



PRELIMINARY

Quote it!

Learn more about:
Part Marking Identification
Tape and Reel Specification
Internet required

• PART NUMBER SELECTION
Learn More - Internet Required

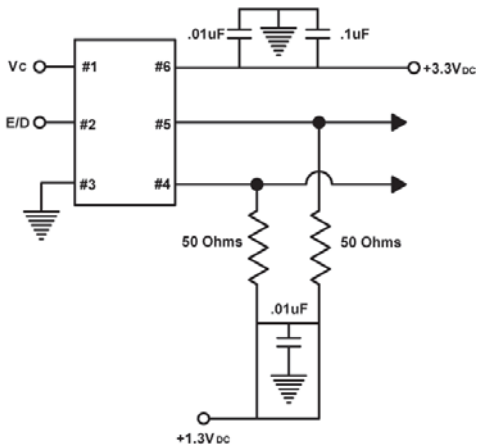
Part Number	Model Number	APR[1]	Operating Temperature	Frequency Range (MHz)
626-Frequency-xxxxx	VCP35AXT	±100PPM	-10 ~ +70 (ºC)	50.000 ~ 600.000
677-Frequency-xxxxx	VCP35AXTR	±100PPM	-40 ~ +85 (ºC)	50.000 ~ 600.000
625-Frequency-xxxxx	VCP32AXT	±125PPM	-10 ~ +70 (ºC)	50.000 ~ 600.000

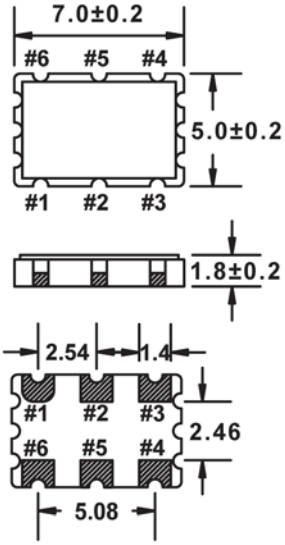
• ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	50.000 ~ 600.000 MHz
Storage Temperature Range (Tstg)	-55ºC ~ +125ºC
Supply Voltage (VDD)	3.3V ± 5%
Input Current (IDD)	120mA
Output Symmetry (50% Vp-p)	40% ~ 60%
Rise Time (20% ~ 80% Vp-p) (TR)	0.55nS
Fall Time (80% ~ 20% Vp-p) (TF)	0.55nS
Output Voltage (VOL) (VOH)	1.7V 2.2V Min
Output Load	See Recommended Circuit
Start-up Time (TS)	10mS
Output Disable Time	100nS
Output Enable Time	10mS
Frequency Linearity	±10%
Modulation Bandwidth	20 kHz Min
Phase Noise (@ 10kHz)	-110dBc/Hz
Phase Jitter (12kHz ~ 20MHz)	5pS RMS

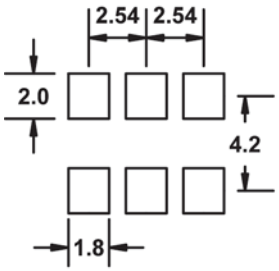
[1] Inclusive of 25ºC tolerance, operating temperature range, input voltage change, load change, aging, shock, vibration and Vc = 1.65V ±1.5V.
[2] An internal pulldown resistor from pin 2 to pin 3 allows active output if pin 2 is left open.
Note: A 0.01µF bypass capacitor should be placed between VDD (Pin 6) and GND (Pin 3) to minimize power supply line noise.
All specifications subject to change without notice. *Rev. 3/28/05*

VCP3_AXT Series Recommended Circuit





Recommended Solder Pad Layout



Pin Connections

#1 Vc	#4 Output 1
#2 E/D or NC	#5 Output 2
#3 GND	#6 VDD

All dimensions are in millimeters.

• ENABLE / DISABLE FUNCTION[2]

(Pin 2)	OUTPUT (Pin 4, Pin 5)
OPEN	ACTIVE
'1' Level VIH ≥ VDD-1.1V	High Z
'0' Level VIL ≤ VDD-1.6V	ACTIVE

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