



## LMT84-Q1 1.5-V, SC70, Analog Temperature Sensors

### 1 Features

- LMT84-Q1 is AEC-Q100 Qualified for Automotive Applications:
  - Device Temperature Grade 0:  $-40^{\circ}\text{C}$  to  $+150^{\circ}\text{C}$
  - Device HBM ESD Classification Level 2
  - Device CDM ESD Classification Level C6
- Very Accurate:  $\pm 0.4^{\circ}\text{C}$  Typical
- Low 1.5-V Operation
- Average Sensor Gain of  $-5.5\text{ mV}/^{\circ}\text{C}$
- Low  $5.4\text{-}\mu\text{A}$  Quiescent Current
- Wide Temperature Range:  $-50^{\circ}\text{C}$  to  $150^{\circ}\text{C}$
- Output is Short-Circuit Protected
- Push-Pull Output With  $\pm 50\text{-}\mu\text{A}$  Drive Capability
- Footprint Compatible With the Industry-Standard LM20/19 and LM35 Temperature Sensors
- Cost-Effective Alternative to Thermistors

### 2 Applications

- Automotive
- Infotainment and Cluster
- Powertrain Systems
- Smoke and Heat Detectors
- Drones
- Appliances

### 3 Description

The LMT84-Q1 is a precision CMOS temperature sensor with  $\pm 0.4^{\circ}\text{C}$  typical accuracy ( $\pm 2.7^{\circ}\text{C}$  maximum) and a linear analog output voltage that is inversely proportional to temperature. The 1.5-V supply voltage operation,  $5.4\text{-}\mu\text{A}$  quiescent current, and  $0.7\text{-ms}$  power-on time enable effective power-cycling architectures to minimize power consumption for battery-powered applications such as drones and sensor nodes. The LMT84-Q1 device is AEC-Q100 Grade 0 qualified and maintains  $\pm 2.7^{\circ}\text{C}$  maximum accuracy over the full operating temperature range without calibration; this makes the LMT84-Q1 suitable for automotive applications such as infotainment, cluster, and powertrain systems. The accuracy over the wide operating range and other features make the LMT84-Q1 an excellent alternative to thermistors.

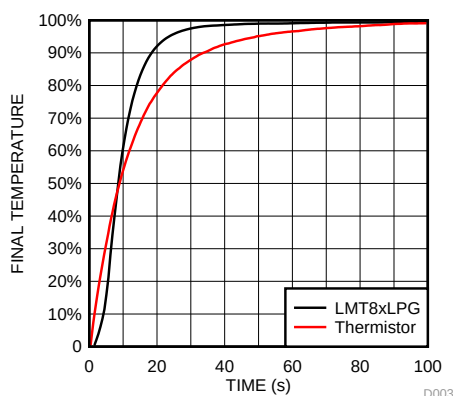
For devices with different average sensor gains and comparable accuracy, refer to [Comparable Alternative Devices](#) for alternative devices in the LMT8x family.

#### Device Information<sup>(1)</sup>

| PART NUMBER | PACKAGE | BODY SIZE (NOM)   |
|-------------|---------|-------------------|
| LMT84-Q1    | SOT (5) | 2.00 mm x 1.25 mm |

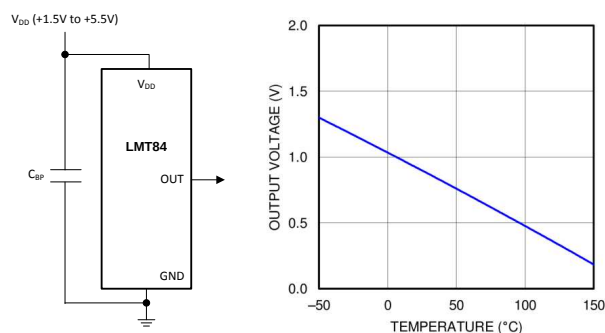
(1) For all available packages, see the orderable addendum addendum at the end of the data sheet.

**Thermal Time Constant**



\* Fast thermal response NTC

**Output Voltage vs Temperature**



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## 4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

| DATE         | REVISION | NOTES                                                                                     |
|--------------|----------|-------------------------------------------------------------------------------------------|
| October 2017 | *        | Initial release. Moved the automotive device from the SNIS167 to a standalone data sheet. |

## 5 Device Comparison Tables

**Table 1. Available Device Packages**

| ORDER NUMBER <sup>(1)</sup> | PACKAGE                              | PIN | BODY SIZE (NOM)   | MOUNTING TYPE                |
|-----------------------------|--------------------------------------|-----|-------------------|------------------------------|
| LMT84DCK                    | SOT (AKA <sup>(2)</sup> : SC70, DCK) | 5   | 2.00 mm × 1.25 mm | Surface Mount                |
| LMT84LP                     | TO-92 (AKA <sup>(2)</sup> : LP)      | 3   | 4.30 mm × 3.50 mm | Through-hole; straight leads |
| LMT84LPG                    | TO-92S (AKA <sup>(2)</sup> : LPG)    | 3   | 4.00 mm × 3.15 mm | Through-hole; straight leads |
| LMT84LPM                    | TO-92 (AKA <sup>(2)</sup> : LPM)     | 3   | 4.30 mm × 3.50 mm | Through-hole; formed leads   |
| LMT84DCK-Q1                 | SOT (AKA <sup>(2)</sup> : SC70, DCK) | 5   | 2.00 mm × 1.25 mm | Surface Mount                |

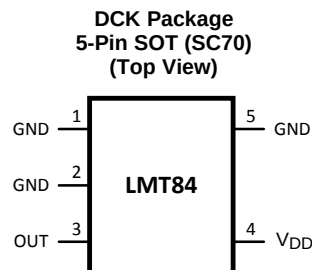
(1) For all available packages and complete order numbers, see the Package Option addendum at the end of the data sheet.

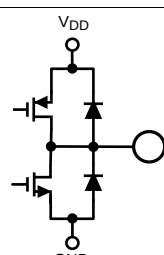
(2) AKA = Also Known As

**Table 2. Comparable Alternative Devices**

| DEVICE NAME              | AVERAGE OUTPUT SENSOR GAIN | POWER SUPPLY RANGE |
|--------------------------|----------------------------|--------------------|
| <a href="#">LMT84-Q1</a> | –5.5 mV/°C                 | 1.5 V to 5.5 V     |
| <a href="#">LMT85-Q1</a> | –8.2 mV/°C                 | 1.8 V to 5.5 V     |
| <a href="#">LMT86-Q1</a> | –10.9 mV/°C                | 2.2 V to 5.5 V     |
| <a href="#">LMT87-Q1</a> | –13.6 mV/°C                | 2.7 V to 5.5 V     |

## 6 Pin Configuration and Functions


**Pin Functions**

| PIN             |                         | TYPE          | DESCRIPTION                                                                         |                                                                 |
|-----------------|-------------------------|---------------|-------------------------------------------------------------------------------------|-----------------------------------------------------------------|
| NAME            | SOT (SC70)              |               | EQUIVALENT CIRCUIT                                                                  | FUNCTION                                                        |
| GND             | 1, 2 <sup>(1)</sup> , 5 | Ground        | N/A                                                                                 | Power Supply Ground                                             |
| OUT             | 3                       | Analog Output |  | Outputs a voltage that is inversely proportional to temperature |
| V <sub>DD</sub> | 4                       | Power         | N/A                                                                                 | Positive Supply Voltage                                         |

(1) Direct connection to the back side of the die

## 7 Specifications

### 7.1 Absolute Maximum Ratings

 See <sup>(1)(2)</sup>

|                                                   | MIN  | MAX                     | UNIT |
|---------------------------------------------------|------|-------------------------|------|
| Supply voltage                                    | −0.3 | 6                       | V    |
| Voltage at output pin                             | −0.3 | (V <sub>DD</sub> + 0.5) | V    |
| Output current                                    | −7   | 7                       | mA   |
| Input current at any pin <sup>(3)</sup>           | −5   | 5                       | mA   |
| Maximum junction temperature (T <sub>JMAX</sub> ) |      | 150                     | °C   |
| Storage temperature T <sub>stg</sub>              | −65  | 150                     | °C   |

- (1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) *Soldering process must comply with Reflow Temperature Profile specifications. Refer to [www.ti.com/packaging](http://www.ti.com/packaging).*
- (3) When the input voltage (V<sub>I</sub>) at any pin exceeds power supplies (V<sub>I</sub> < GND or V<sub>I</sub> > V), the current at that pin should be limited to 5 mA.

### 7.2 ESD Ratings

|                                            | VALUE                                                   | UNIT  |
|--------------------------------------------|---------------------------------------------------------|-------|
| LMT84DCK-Q1 in SC70 package                |                                                         |       |
| V <sub>(ESD)</sub> Electrostatic discharge | Human-body model (HBM), per AEC Q100-002 <sup>(1)</sup> | ±2500 |
|                                            | Charged-device model (CDM), per AEC Q100-011            | ±1000 |

- (1) AEC Q100-002 indicates that HBM stressing shall be in accordance with the ANSI/ESDA/JEDEC JS-001 specification.

### 7.3 Recommended Operating Conditions

|                                   | MIN                                                  | MAX | UNIT |
|-----------------------------------|------------------------------------------------------|-----|------|
| Specified temperature             | T <sub>MIN</sub> ≤ T <sub>A</sub> ≤ T <sub>MAX</sub> |     | °C   |
|                                   | −50 ≤ T <sub>A</sub> ≤ 150                           |     | °C   |
| Supply voltage (V <sub>DD</sub> ) | 1.5                                                  | 5.5 | V    |

### 7.4 Thermal Information<sup>(1)</sup>

| THERMAL METRIC <sup>(2)</sup> |                                                          | LMT84-Q1       | UNIT |
|-------------------------------|----------------------------------------------------------|----------------|------|
|                               |                                                          | DCK (SOT/SC70) |      |
|                               |                                                          | 5 PINS         |      |
| R <sub>θJA</sub>              | Junction-to-ambient thermal resistance <sup>(3)(4)</sup> | 275            | °C/W |
| R <sub>θJC(top)</sub>         | Junction-to-case (top) thermal resistance                | 84             | °C/W |
| R <sub>θJB</sub>              | Junction-to-board thermal resistance                     | 56             | °C/W |
| ψ <sub>JT</sub>               | Junction-to-top characterization parameter               | 1.2            | °C/W |
| ψ <sub>JB</sub>               | Junction-to-board characterization parameter             | 55             | °C/W |

- (1) For information on self-heating and thermal response time, see section [Mounting and Thermal Conductivity](#).
- (2) For more information about traditional and new thermal metrics, see the [IC Package Thermal Metrics](#) application report.
- (3) The junction to ambient thermal resistance (R<sub>θJA</sub>) under natural convection is obtained in a simulation on a JEDEC-standard, High-K board as specified in JESD51-7, in an environment described in JESD51-2. Exposed pad packages assume that thermal vias are included in the PCB, per JESD 51-5.
- (4) Changes in output due to self-heating can be computed by multiplying the internal dissipation by the thermal resistance.

## 7.5 Accuracy Characteristics

These limits do not include DC load regulation. These stated accuracy limits are with reference to the values in [Table 3](#).

| PARAMETER                           | TEST CONDITIONS                                   | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT |
|-------------------------------------|---------------------------------------------------|--------------------|--------------------|--------------------|------|
| Temperature accuracy <sup>(3)</sup> | 70°C to 150°C; V <sub>DD</sub> = 1.5 V to 5.5 V   | -2.7               | ±0.6               | 2.7                | °C   |
|                                     | 0°C to 70°C; V <sub>DD</sub> = 1.5 V to 5.5 V     | -2.7               | ±0.9               | 2.7                | °C   |
|                                     | -50°C to +0°C; V <sub>DD</sub> = 1.6 V to 5.5 V   | -2.7               | ±0.9               | 2.7                | °C   |
|                                     | -50°C to +150°C; V <sub>DD</sub> = 2.3 V to 5.5 V |                    | ±0.4               |                    | °C   |

- (1) Limits are specified to TI's AOQL (Average Outgoing Quality Level).
- (2) Typicals are at T<sub>J</sub> = T<sub>A</sub> = 25°C and represent most likely parametric norm.
- (3) Accuracy is defined as the error between the measured and reference output voltages, tabulated in [Table 3](#) at the specified conditions of supply gain setting, voltage, and temperature (expressed in °C). Accuracy limits include line regulation within the specified conditions. Accuracy limits do not include load regulation; they assume no DC load.

## 7.6 Electrical Characteristics

Unless otherwise noted, these specifications apply for V<sub>DD</sub> = +1.5 V to +5.5 V. minimum and maximum limits apply for T<sub>A</sub> = T<sub>J</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>; typical values apply for T<sub>A</sub> = T<sub>J</sub> = 25°C.

| PARAMETER                              | TEST CONDITIONS                                                                 | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT  |
|----------------------------------------|---------------------------------------------------------------------------------|--------------------|--------------------|--------------------|-------|
| Sensor gain                            |                                                                                 |                    | -5.5               |                    | mV/°C |
| Load regulation <sup>(3)</sup>         | Source ≤ 50 μA, (V <sub>DD</sub> - V <sub>OUT</sub> ) ≥ 200 mV                  | -1                 | -0.22              |                    | mV    |
|                                        | Sink ≤ 50 μA, V <sub>OUT</sub> ≥ 200 mV                                         |                    | 0.26               | 1                  | mV    |
| Line regulation <sup>(4)</sup>         |                                                                                 |                    | 200                |                    | μV/V  |
| I <sub>S</sub> Supply current          | T <sub>A</sub> = 30°C to 150°C, (V <sub>DD</sub> - V <sub>OUT</sub> ) ≥ 100 mV  |                    | 5.4                | 8.1                | μA    |
|                                        | T <sub>A</sub> = -50°C to 150°C, (V <sub>DD</sub> - V <sub>OUT</sub> ) ≥ 100 mV |                    | 5.4                | 9                  | μA    |
| C <sub>L</sub> Output load capacitance |                                                                                 |                    | 1100               |                    | pF    |
| Power-on time <sup>(5)</sup>           | C <sub>L</sub> = 0 pF to 1100 pF                                                |                    | 0.7                | 1.9                | ms    |
| Output drive                           |                                                                                 |                    | ±50                |                    | μA    |

- (1) Limits are specific to TI's AOQL (Average Outgoing Quality Level).
- (2) Typicals are at T<sub>J</sub> = T<sub>A</sub> = 25°C and represent most likely parametric norm.
- (3) Source currents are flowing out of the LMT84-xx. Sink currents are flowing into the LMT84-xx.
- (4) Line regulation (DC) is calculated by subtracting the output voltage at the highest supply voltage from the output voltage at the lowest supply voltage. The typical DC line regulation specification does not include the output voltage shift discussed in [Output Voltage Shift](#).
- (5) Specified by design and characterization.

## 7.7 Typical Characteristics

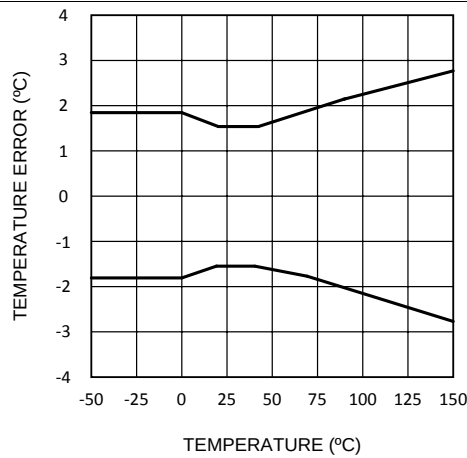


Figure 1. Temperature Error vs Temperature

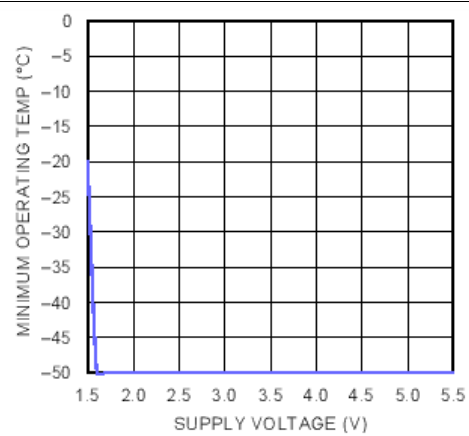


Figure 2. Minimum Operating Temperature vs Supply Voltage

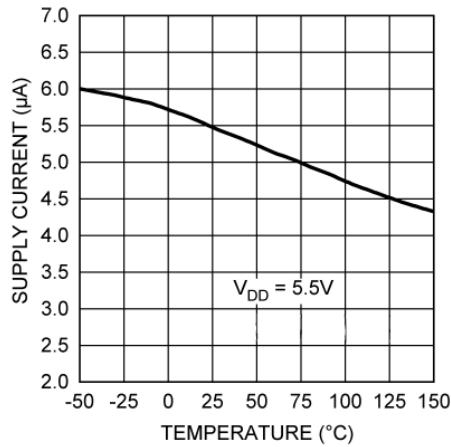


Figure 3. Supply Current vs Temperature

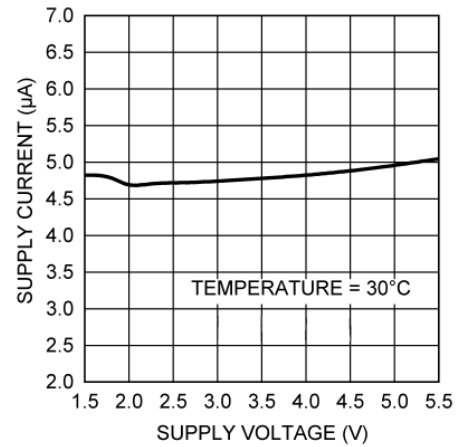


Figure 4. Supply Current vs Supply Voltage

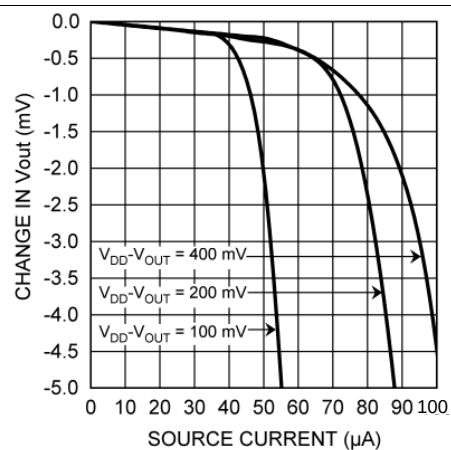


Figure 5. Load Regulation, Sourcing Current

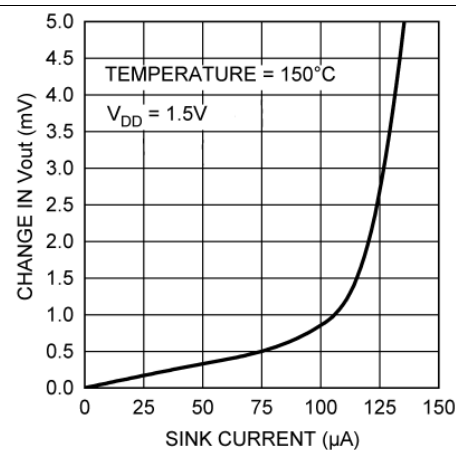
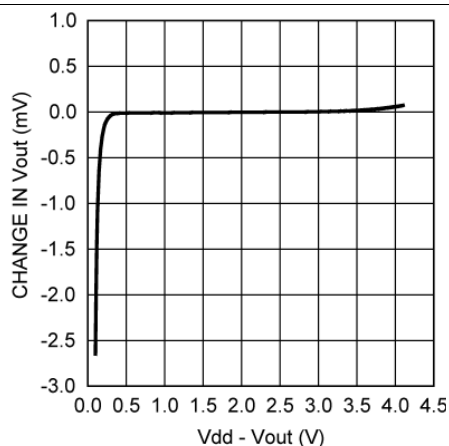
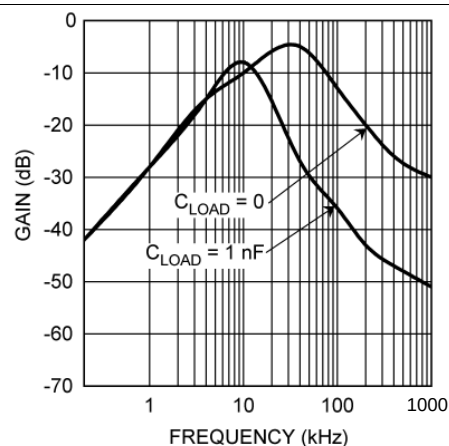


Figure 6. Load Regulation, Sinking Current

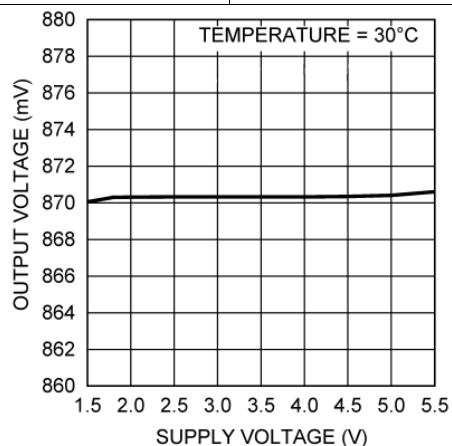
## Typical Characteristics (continued)



**Figure 7. Change in Vout vs Overhead Voltage**



**Figure 8. Supply-Noise Gain vs Frequency**



**Figure 9. Output Voltage vs Supply Voltage**

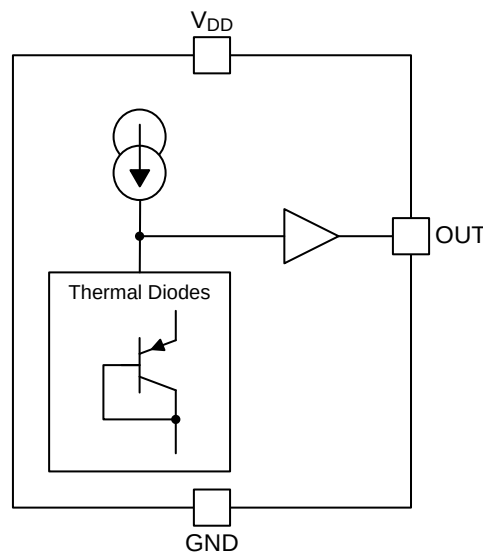
## 8 Detailed Description

### 8.1 Overview

The LMT84-Q1 is an analog output temperature sensor. The temperature-sensing element is comprised of a simple base emitter junction that is forward biased by a current source. The temperature-sensing element is then buffered by an amplifier and provided to the OUT pin. The amplifier has a simple push-pull output stage thus providing a low impedance output source.

### 8.2 Functional Block Diagram

Full-Range Celsius Temperature Sensor (–50°C to +150°C)



### 8.3 Feature Description

#### 8.3.1 LMT84 Transfer Function

The output voltage of the LMT84-Q1, across the complete operating temperature range, is shown in [Table 3](#). This table is the reference from which the LMT84-Q1 accuracy specifications (listed in the [Accuracy Characteristics](#) section) are determined. This table can be used, for example, in a host processor look-up table. A file containing this data is available for download at the [LMT84-Q1](#) product folder under *Tools and Software Models*.

**Table 3. LMT84-Q1 Transfer Table**

| TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) |
|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|
| –50          | 1299                     | –10          | 1088                     | 30           | 871                      | 70           | 647                      | 110          | 419                      |
| –49          | 1294                     | –9           | 1082                     | 31           | 865                      | 71           | 642                      | 111          | 413                      |
| –48          | 1289                     | –8           | 1077                     | 32           | 860                      | 72           | 636                      | 112          | 407                      |
| –47          | 1284                     | –7           | 1072                     | 33           | 854                      | 73           | 630                      | 113          | 401                      |
| –46          | 1278                     | –6           | 1066                     | 34           | 849                      | 74           | 625                      | 114          | 396                      |
| –45          | 1273                     | –5           | 1061                     | 35           | 843                      | 75           | 619                      | 115          | 390                      |
| –44          | 1268                     | –4           | 1055                     | 36           | 838                      | 76           | 613                      | 116          | 384                      |
| –43          | 1263                     | –3           | 1050                     | 37           | 832                      | 77           | 608                      | 117          | 378                      |
| –42          | 1257                     | –2           | 1044                     | 38           | 827                      | 78           | 602                      | 118          | 372                      |
| –41          | 1252                     | –1           | 1039                     | 39           | 821                      | 79           | 596                      | 119          | 367                      |
| –40          | 1247                     | 0            | 1034                     | 40           | 816                      | 80           | 591                      | 120          | 361                      |
| –39          | 1242                     | 1            | 1028                     | 41           | 810                      | 81           | 585                      | 121          | 355                      |



## Feature Description (continued)

**Table 3. LMT84-Q1 Transfer Table (continued)**

| TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) |
|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|
| –38          | 1236                     | 2            | 1023                     | 42           | 804                      | 82           | 579                      | 122          | 349                      |
| –37          | 1231                     | 3            | 1017                     | 43           | 799                      | 83           | 574                      | 123          | 343                      |
| –36          | 1226                     | 4            | 1012                     | 44           | 793                      | 84           | 568                      | 124          | 337                      |
| –35          | 1221                     | 5            | 1007                     | 45           | 788                      | 85           | 562                      | 125          | 332                      |
| –34          | 1215                     | 6            | 1001                     | 46           | 782                      | 86           | 557                      | 126          | 326                      |
| –33          | 1210                     | 7            | 996                      | 47           | 777                      | 87           | 551                      | 127          | 320                      |
| –32          | 1205                     | 8            | 990                      | 48           | 771                      | 88           | 545                      | 128          | 314                      |
| –31          | 1200                     | 9            | 985                      | 49           | 766                      | 89           | 539                      | 129          | 308                      |
| –30          | 1194                     | 10           | 980                      | 50           | 760                      | 90           | 534                      | 130          | 302                      |
| –29          | 1189                     | 11           | 974                      | 51           | 754                      | 91           | 528                      | 131          | 296                      |
| –28          | 1184                     | 12           | 969                      | 52           | 749                      | 92           | 522                      | 132          | 291                      |
| –27          | 1178                     | 13           | 963                      | 53           | 743                      | 93           | 517                      | 133          | 285                      |
| –26          | 1173                     | 14           | 958                      | 54           | 738                      | 94           | 511                      | 134          | 279                      |
| –25          | 1168                     | 15           | 952                      | 55           | 732                      | 95           | 505                      | 135          | 273                      |
| –24          | 1162                     | 16           | 947                      | 56           | 726                      | 96           | 499                      | 136          | 267                      |
| –23          | 1157                     | 17           | 941                      | 57           | 721                      | 97           | 494                      | 137          | 261                      |
| –22          | 1152                     | 18           | 936                      | 58           | 715                      | 98           | 488                      | 138          | 255                      |
| –21          | 1146                     | 19           | 931                      | 59           | 710                      | 99           | 482                      | 139          | 249                      |
| –20          | 1141                     | 20           | 925                      | 60           | 704                      | 100          | 476                      | 140          | 243                      |
| –19          | 1136                     | 21           | 920                      | 61           | 698                      | 101          | 471                      | 141          | 237                      |
| –18          | 1130                     | 22           | 914                      | 62           | 693                      | 102          | 465                      | 142          | 231                      |
| –17          | 1125                     | 23           | 909                      | 63           | 687                      | 103          | 459                      | 143          | 225                      |
| –16          | 1120                     | 24           | 903                      | 64           | 681                      | 104          | 453                      | 144          | 219                      |
| –15          | 1114                     | 25           | 898                      | 65           | 676                      | 105          | 448                      | 145          | 213                      |
| –14          | 1109                     | 26           | 892                      | 66           | 670                      | 106          | 442                      | 146          | 207                      |
| –13          | 1104                     | 27           | 887                      | 67           | 664                      | 107          | 436                      | 147          | 201                      |
| –12          | 1098                     | 28           | 882                      | 68           | 659                      | 108          | 430                      | 148          | 195                      |
| –11          | 1093                     | 29           | 876                      | 69           | 653                      | 109          | 425                      | 149          | 189                      |
|              |                          |              |                          |              |                          |              |                          | 150          | 183                      |

Although the LMT84-Q1 is very linear, the response does have a slight umbrella parabolic shape. This shape is very accurately reflected in [Table 3](#). The transfer table can be calculated by using the parabolic equation ([Equation 1](#)).

$$V_{\text{TEMP}} (\text{mV}) = 870.6 \text{ mV} - \left[ 5.506 \frac{\text{mV}}{^{\circ}\text{C}} (T - 30^{\circ}\text{C}) \right] - \left[ 0.00176 \frac{\text{mV}}{^{\circ}\text{C}^2} (T - 30^{\circ}\text{C})^2 \right] \quad (1)$$

The parabolic equation is an approximation of the transfer table and the accuracy of the equation degrades slightly at the temperature range extremes. [Equation 1](#) can be solved for T, resulting in:

$$T = \frac{5.506 - \sqrt{(-5.506)^2 + 4 \times 0.00176 \times (870.6 - V_{\text{TEMP}} (\text{mV}))}}{2 \times (-0.00176)} + 30 \quad (2)$$

For an even less accurate linear approximation, a line can easily be calculated over the desired temperature range from the table using the two-point equation ([Equation 3](#)):

$$V - V_1 = \left( \frac{V_2 - V_1}{T_2 - T_1} \right) \times (T - T_1)$$

where

- V is in mV,
- T is in °C,
- T<sub>1</sub> and V<sub>1</sub> are the coordinates of the lowest temperature,

- and  $T_2$  and  $V_2$  are the coordinates of the highest temperature. (3)

For example, if the user wanted to resolve this equation, over a temperature range of 20°C to 50°C, they would proceed as follows:

$$V - 925 \text{ mV} = \left( \frac{760 \text{ mV} - 925 \text{ mV}}{50^\circ\text{C} - 20^\circ\text{C}} \right) \times (T - 20^\circ\text{C}) \quad (4)$$

$$V - 925 \text{ mV} = (-5.50 \text{ mV} / ^\circ\text{C}) \times (T - 20^\circ\text{C}) \quad (5)$$

$$V = (-5.50 \text{ mV} / ^\circ\text{C}) \times T + 1035 \text{ mV} \quad (6)$$

Using this method of linear approximation, the transfer function can be approximated for one or more temperature ranges of interest.

## 8.4 Device Functional Modes

### 8.4.1 Mounting and Thermal Conductivity

The LMT84-Q1 can be applied easily in the same way as other integrated-circuit temperature sensors. It can be glued or cemented to a surface.

To ensure good thermal conductivity, the backside of the LMT84 die is directly attached to the GND pin. The temperatures of the lands and traces to the other leads of the LMT84-Q1 will also affect the temperature reading.

Alternatively, the LMT84-Q1 can be mounted inside a sealed-end metal tube, and can then be dipped into a bath or screwed into a threaded hole in a tank. As with any IC, the LMT84 and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. If moisture creates a short circuit from the output to ground or  $V_{DD}$ , the output from the LMT84-Q1 will not be correct. Printed-circuit coatings are often used to ensure that moisture cannot corrode the leads or circuit traces.

The thermal resistance junction to ambient ( $R_{\theta JA}$  or  $\theta_{JA}$ ) is the parameter used to calculate the rise of a device junction temperature due to its power dissipation. Use [Equation 7](#) to calculate the rise in the LMT84-Q1 die temperature:

$$T_J = T_A + \theta_{JA} [(V_{DD} I_S) + (V_{DD} - V_O) I_L]$$

where

- $T_A$  is the ambient temperature,
- $I_S$  is the supply current,
- $I_L$  is the load current on the output,
- and  $V_O$  is the output voltage. (7)

For example, in an application where  $T_A = 30^\circ\text{C}$ ,  $V_{DD} = 5 \text{ V}$ ,  $I_S = 5.4 \mu\text{A}$ ,  $V_{OUT} = 871 \text{ mV}$ , and  $I_L = 2 \mu\text{A}$ , the junction temperature would be  $30.015^\circ\text{C}$ , showing a self-heating error of only  $0.015^\circ\text{C}$ . Because the junction temperature of the LMT84 device is the actual temperature being measured, take care to minimize the load current that the LMT84 is required to drive. [Thermal Information<sup>\(1\)</sup>](#) shows the thermal resistance of the LMT84-Q1.

### 8.4.2 Output Noise Considerations

A push-pull output gives the LMT84-Q1 the ability to sink and source significant current. This is beneficial when, for example, driving dynamic loads like an input stage on an analog-to-digital converter (ADC). In these applications the source current is required to quickly charge the input capacitor of the ADC. The LMT84 is ideal for this and other applications which require strong source or sink current.

The LMT84-Q1 supply-noise gain (the ratio of the AC signal on  $V_{OUT}$  to the AC signal on  $V_{DD}$ ) was measured during bench tests. The typical attenuation is shown in [Figure 8](#) found in the [Typical Characteristics](#) section. A load capacitor on the output can help to filter noise.

For operation in very noisy environments, some bypass capacitance should be present on the supply within approximately 5 centimeters of the LMT84-Q1.

(1) For information on self-heating and thermal response time, see section [Mounting and Thermal Conductivity](#).

## Device Functional Modes (continued)

### 8.4.3 Capacitive Loads

The LMT84-Q1 handles capacitive loading well. In an extremely noisy environment, or when driving a switched sampling input on an ADC, it may be necessary to add some filtering to minimize noise coupling. Without any precautions, the LMT84-Q1 can drive a capacitive load less than or equal to 1100 pF as shown in Figure 10. For capacitive loads greater than 1100 pF, a series resistor may be required on the output, as shown in Figure 11.

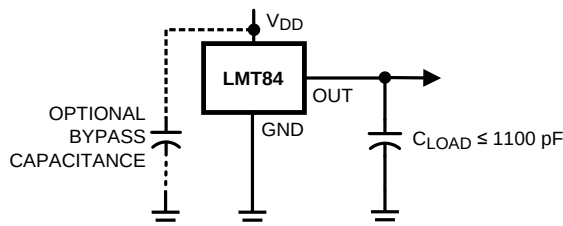


Figure 10. LMT84-Q1 No Decoupling Required for Capacitive Loads Less Than 1100 pF

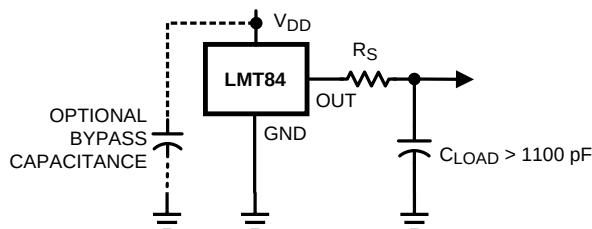


Figure 11. LMT84-Q1 With Series Resistor for Capacitive Loading Greater Than 1100 pF

Table 4. Recommended Series Resistor Values

| $C_{LOAD}$       | MINIMUM $R_S$  |
|------------------|----------------|
| 1.1 nF to 99 nF  | 3 k $\Omega$   |
| 100 nF to 999 nF | 1.5 k $\Omega$ |
| 1 $\mu$ F        | 800 $\Omega$   |

### 8.4.4 Output Voltage Shift

The LMT84-Q1 is very linear over temperature and supply voltage range. Due to the intrinsic behavior of an NMOS or PMOS rail-to-rail buffer, a slight shift in the output can occur when the supply voltage is ramped over the operating range of the device. The location of the shift is determined by the relative levels of  $V_{DD}$  and  $V_{OUT}$ . The shift typically occurs when  $V_{DD} - V_{OUT} = 1$  V.

This slight shift (a few millivolts) takes place over a wide change (approximately 200 mV) in  $V_{DD}$  or  $V_{OUT}$ . Because the shift takes place over a wide temperature change of 5°C to 20°C,  $V_{OUT}$  is always monotonic. The accuracy specifications in the [Accuracy Characteristics](#) table already include this possible shift.

## 9 Application and Implementation

### NOTE

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

### 9.1 Applications Information

The LMT84-Q1 features make it suitable for many general temperature-sensing applications. It can operate down to 1.5-V supply with 5.4- $\mu$ A power consumption, making it ideal for battery-powered devices.

### 9.2 Typical Applications

#### 9.2.1 Connection to an ADC

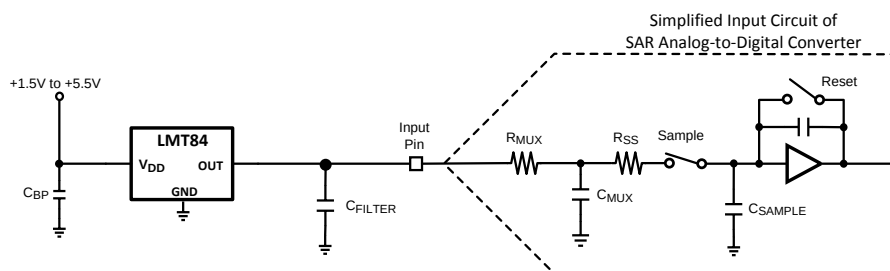


Figure 12. Suggested Connection to a Sampling Analog-to-Digital Converter Input Stage

##### 9.2.1.1 Design Requirements

Most CMOS ADCs found in microcontrollers and ASICs have a sampled data comparator input structure. When the ADC charges the sampling cap, it requires instantaneous charge from the output of the analog source such as the LMT84-Q1 temperature sensor and many op amps. This requirement is easily accommodated by the addition of a capacitor ( $C_{FILTER}$ ).

##### 9.2.1.2 Detailed Design Procedure

The size of  $C_{FILTER}$  depends on the size of the sampling capacitor and the sampling frequency. Because not all ADCs have identical input stages, the charge requirements will vary. This general ADC application is shown as an example only.

##### 9.2.1.3 Application Curve

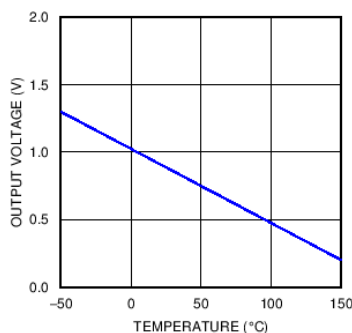


Figure 13. Analog Output Transfer Function

## Typical Applications (continued)

### 9.2.2 Conserving Power Dissipation With Shutdown

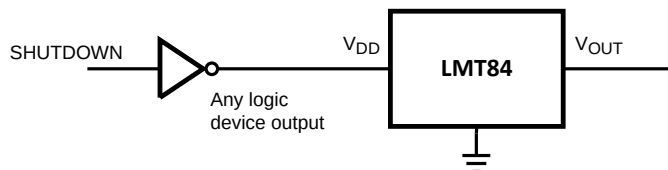


Figure 14. Simple Shutdown Connection of the LMT84-Q1

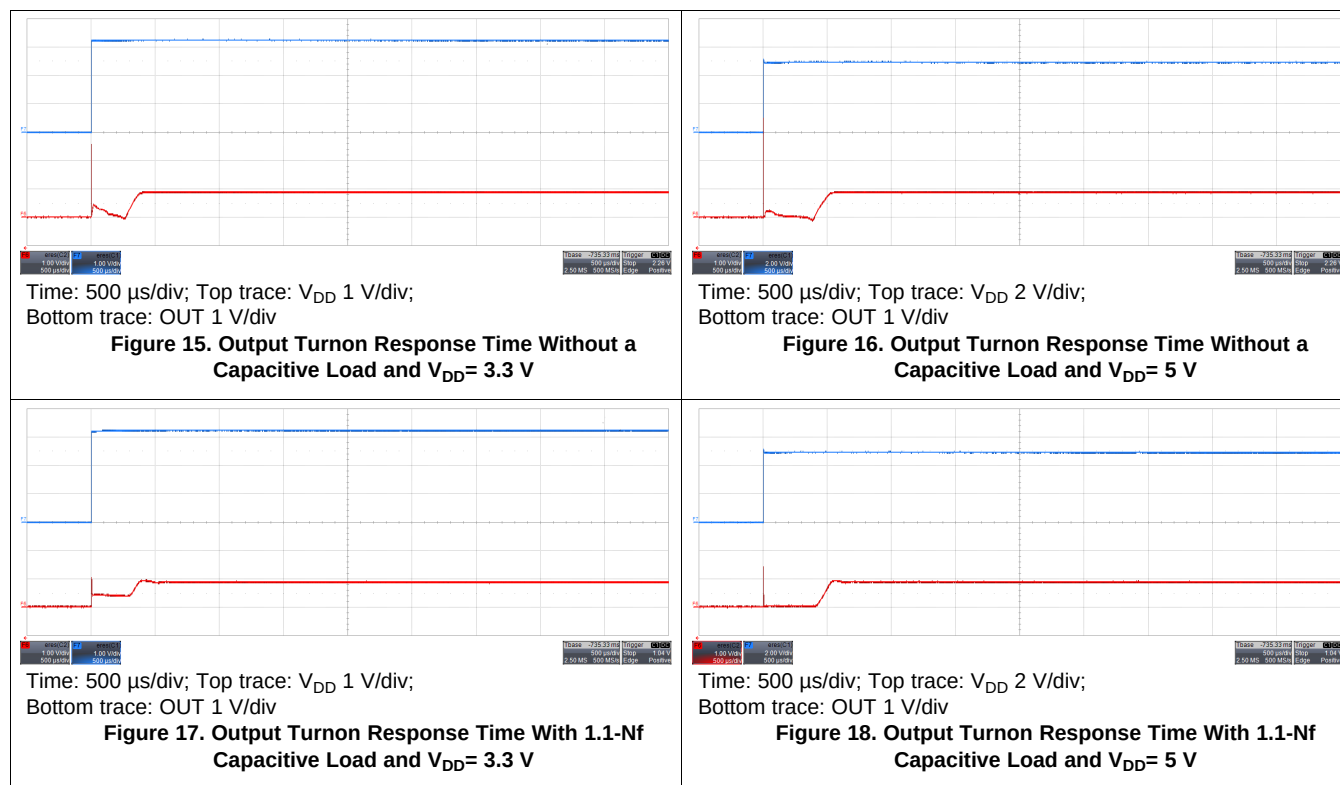
#### 9.2.2.1 Design Requirements

Because the power consumption of the LMT84-Q1 is less than 9  $\mu\text{A}$ , it can simply be powered directly from any logic gate output and therefore not require a specific shutdown pin. The device can even be powered directly from a microcontroller GPIO. In this way, it can easily be turned off for cases such as battery-powered systems where power savings are critical.

#### 9.2.2.2 Detailed Design Procedure

Simply connect the  $V_{DD}$  pin of the LMT84-Q1 directly to the logic shutdown signal from a microcontroller.

#### 9.2.2.3 Application Curves



## 10 Power Supply Recommendations

The low supply current and supply range (1.5 V to 5.5 V) of the LMT84-Q1 allow the device to easily be powered from many sources. Power supply bypassing is optional and is mainly dependent on the noise on the power supply used. In noisy systems, it may be necessary to add bypass capacitors to lower the noise that is coupled to the output of the LMT84-Q1.

## 11 Layout

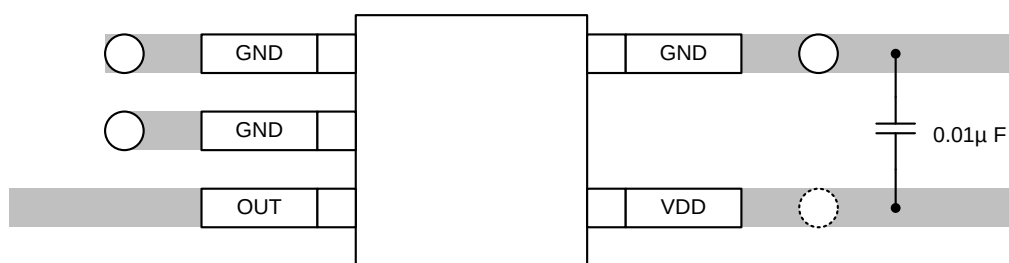
### 11.1 Layout Guidelines

The LMT84-Q1 is extremely simple to layout. If a power-supply bypass capacitor is used, it should be connected as shown in the [Layout Examples](#).

### 11.2 Layout Examples

 VIA to ground plane

 VIA to power plane



**Figure 19. SC70 Package Recommended Layout**

## 12 Device and Documentation Support

### 12.1 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on *Alert me* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

### 12.2 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

**TI E2E™ Online Community** *TI's Engineer-to-Engineer (E2E) Community*. Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

**Design Support** *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

### 12.3 Trademarks

E2E is a trademark of Texas Instruments.  
All other trademarks are the property of their respective owners.

### 12.4 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### 12.5 Glossary

[SLYZ022](#) — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

## 13 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

## PACKAGING INFORMATION

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|------------------|----------------------|--------------|-------------------------|-------------------------|
| LMT84QDCKRQ1     | ACTIVE        | SC70         | DCK                | 5    | 3000           | Green (RoHS<br>& no Sb/Br) | CU SN            | Level-1-260C-UNLIM   | -50 to 150   | BOA                     | <a href="#">Samples</a> |
| LMT84QDCKTQ1     | ACTIVE        | SC70         | DCK                | 5    | 250            | Green (RoHS<br>& no Sb/Br) | CU SN            | Level-1-260C-UNLIM   | -50 to 150   | BOA                     | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

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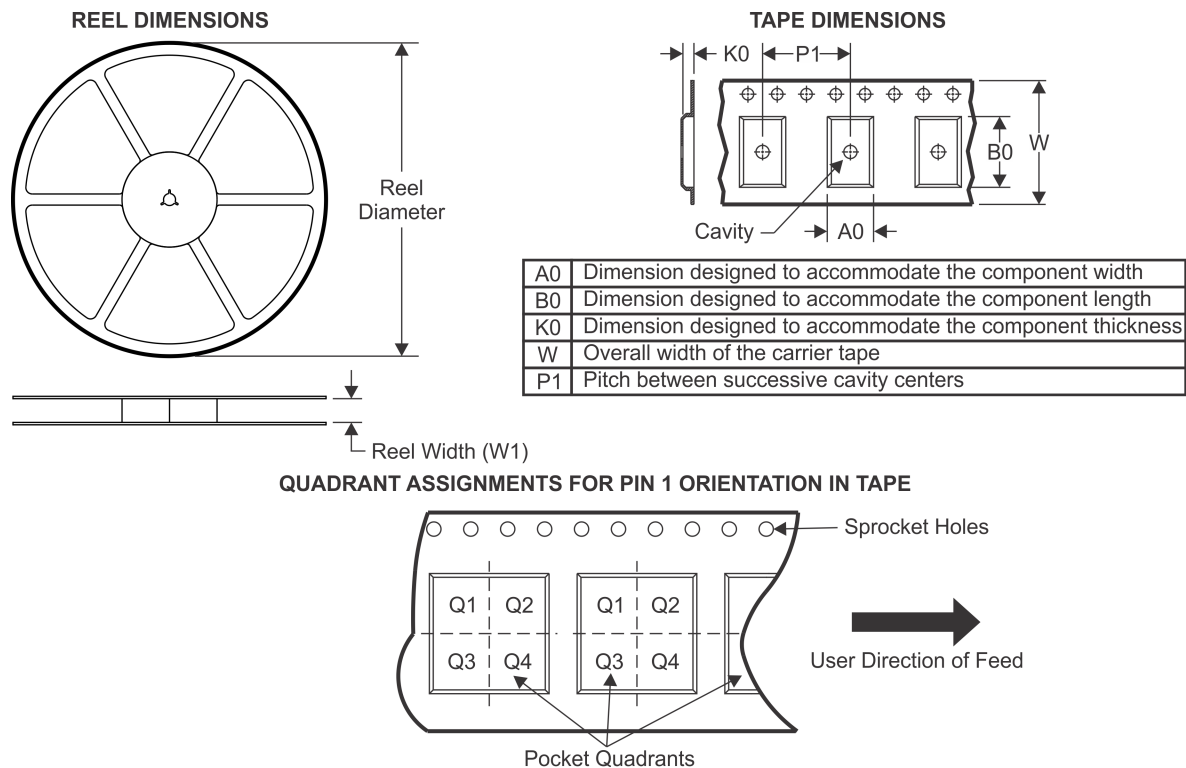


**OTHER QUALIFIED VERSIONS OF LMT84-Q1 :**

- Catalog: [LMT84](#)

NOTE: Qualified Version Definitions:

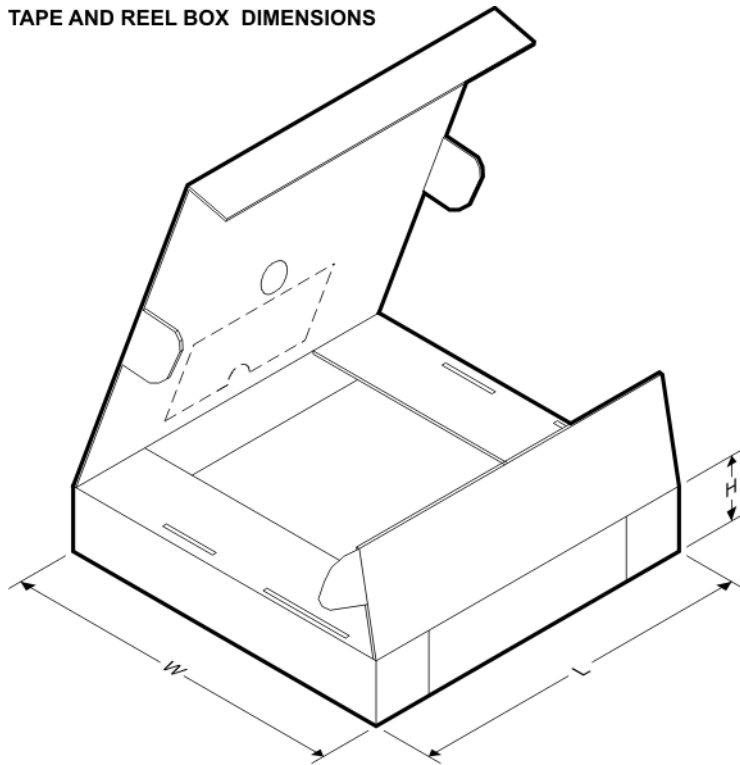
- Catalog - TI's standard catalog product

**TAPE AND REEL INFORMATION**


\*All dimensions are nominal

| Device       | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|--------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| LMT84QDCKRQ1 | SC70         | DCK             | 5    | 3000 | 178.0              | 8.4                | 2.25    | 2.45    | 1.2     | 4.0     | 8.0    | Q3            |
| LMT84QDCKTQ1 | SC70         | DCK             | 5    | 250  | 178.0              | 8.4                | 2.25    | 2.45    | 1.2     | 4.0     | 8.0    | Q3            |

## TAPE AND REEL BOX DIMENSIONS

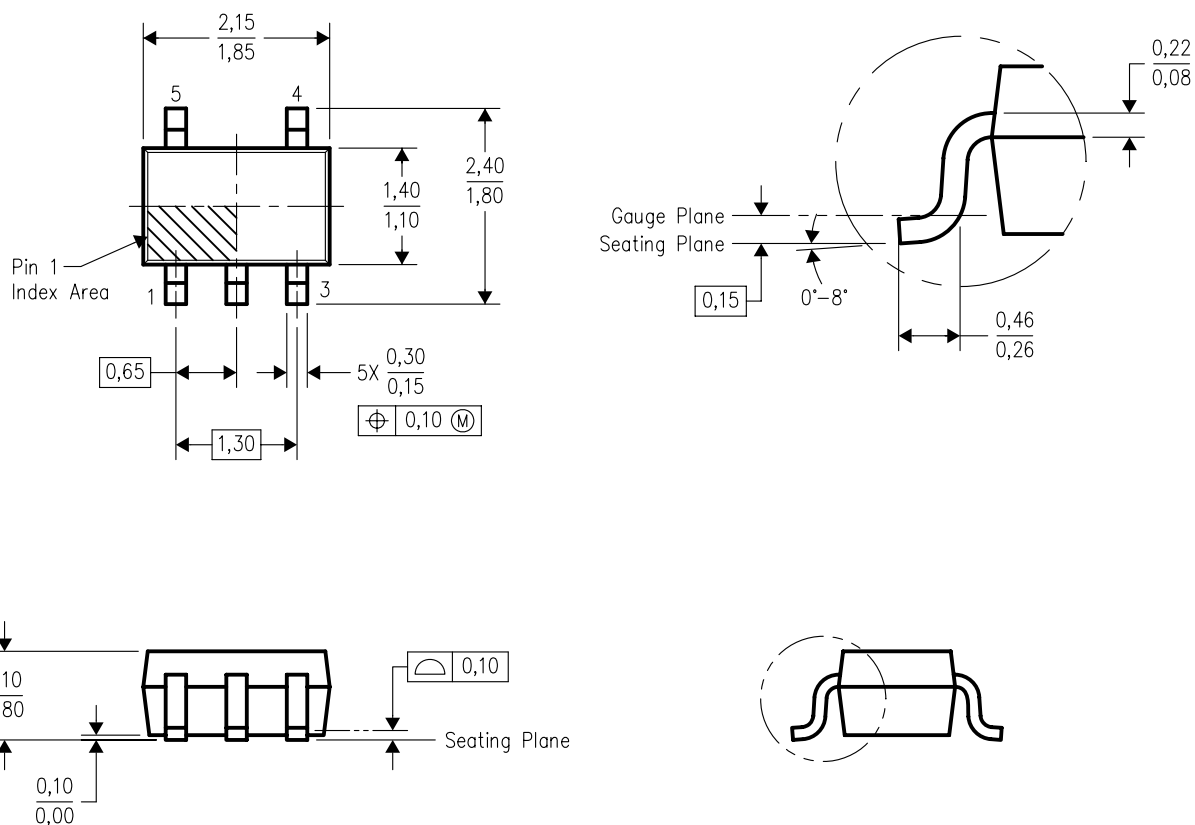


\*All dimensions are nominal

| Device       | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|--------------|--------------|-----------------|------|------|-------------|------------|-------------|
| LMT84QDCKRQ1 | SC70         | DCK             | 5    | 3000 | 210.0       | 185.0      | 35.0        |
| LMT84QDCKTQ1 | SC70         | DCK             | 5    | 250  | 210.0       | 185.0      | 35.0        |

## DCK (R-PDSO-G5)

## PLASTIC SMALL-OUTLINE PACKAGE

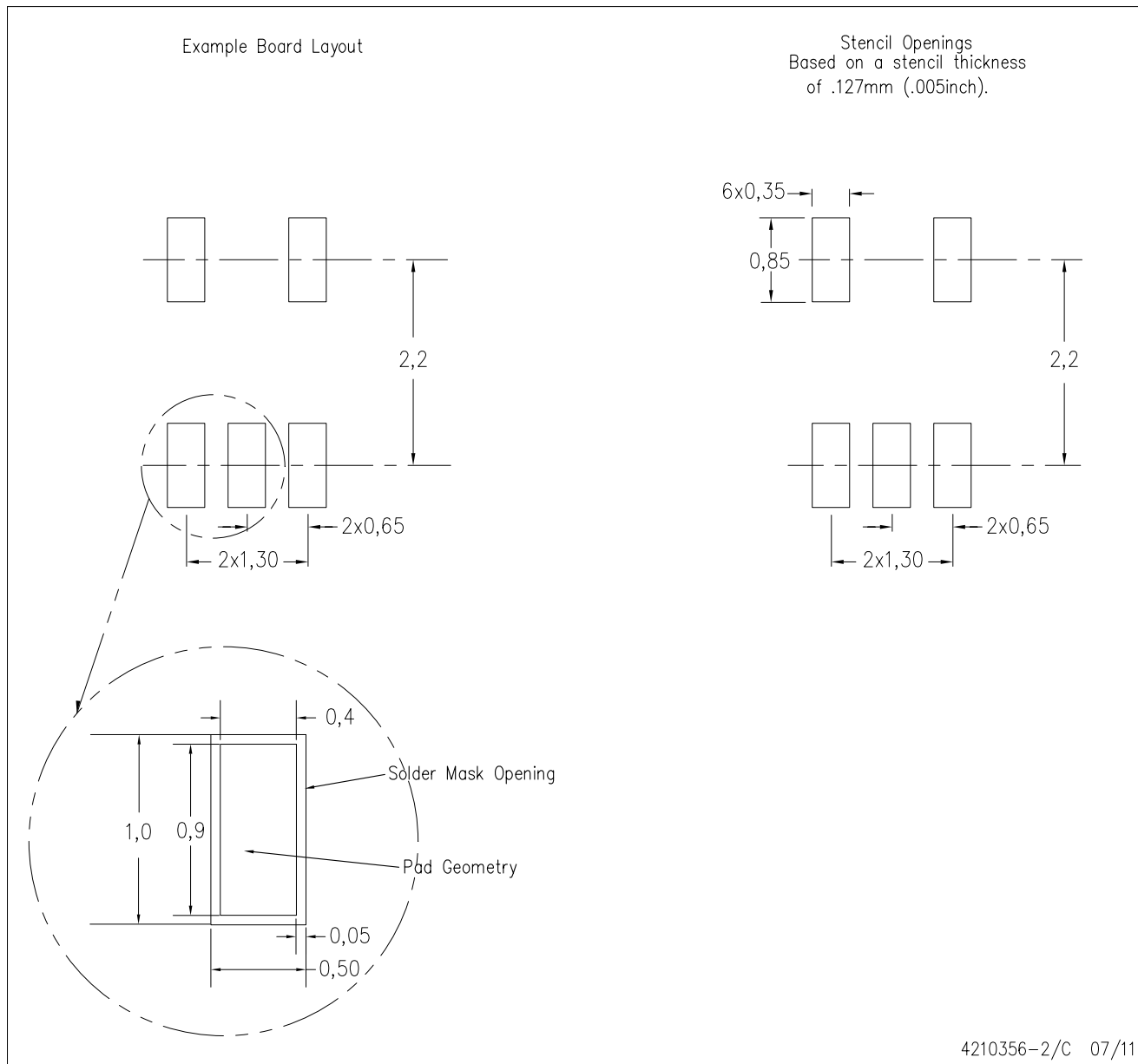


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- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
  - Falls within JEDEC MO-203 variation AA.

DCK (R-PDSO-G5)

PLASTIC SMALL OUTLINE



- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
  - Publication IPC-7351 is recommended for alternate designs.
  - Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.

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