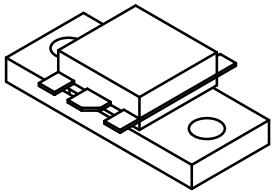
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by MRF6414PHT/D

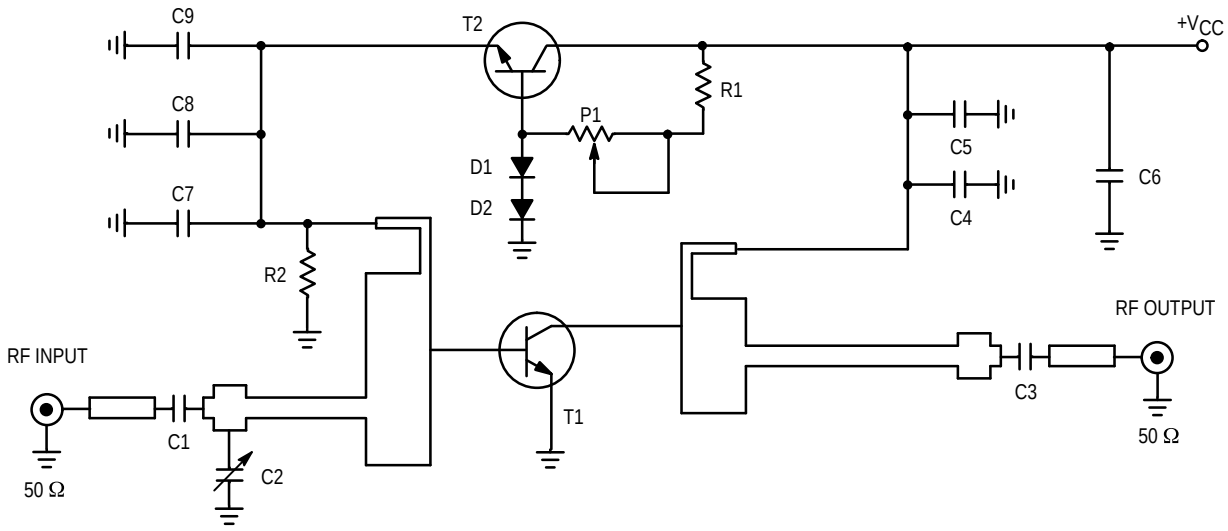
The RF Line

NPN Silicon RF Power Transistor

MRF6414 PHOTOMASTER



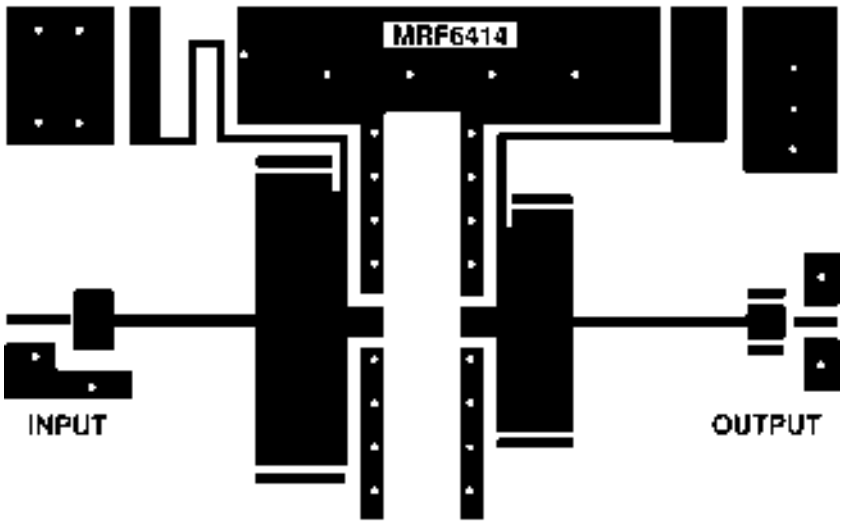
CASE 333A–02, STYLE 2



C1, C3	100 pF, Chip Capacitor, Hight Q	P1	1 kΩ, Trimmer
C2, C7	330 pF, Chip Capacitor, 0805	R1	1 kΩ, Resistor
C5, C8	10 nF, Chip Capacitor, 0805	R2	58 Ω, Resistor, 0805
C6	15 μF, Capacitor, 63 V	T1	MRF6414
C9	100 μF, Capacitor, 16 V	T2	Transistor NPN Type SD135
D1, D2	Diode 1N4007		

Figure 1. 960 MHz Test Circuit Schematic

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(SCALE 1:1)

TEST CIRCUIT @ f = 960 MHz
TEFLON® GLASS 1/50 INCH Er = 2.55

Figure 2. 960 MHz PC Board Photomaster

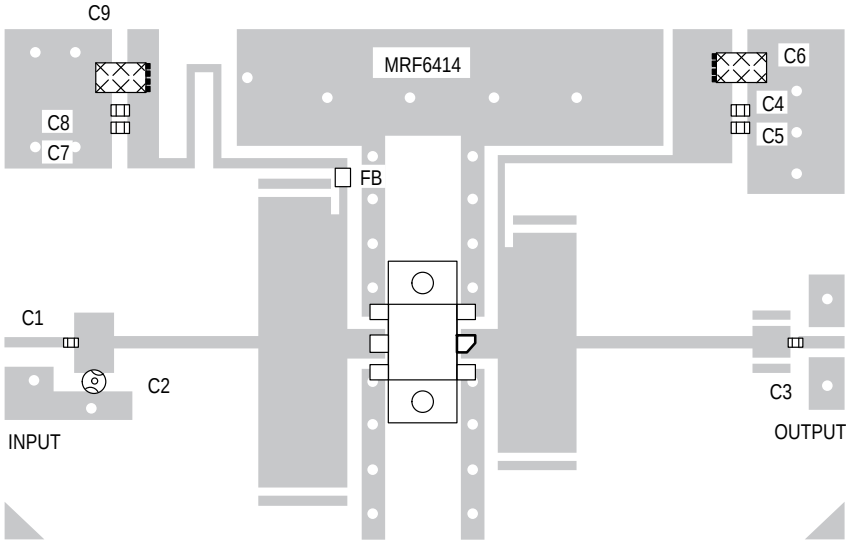
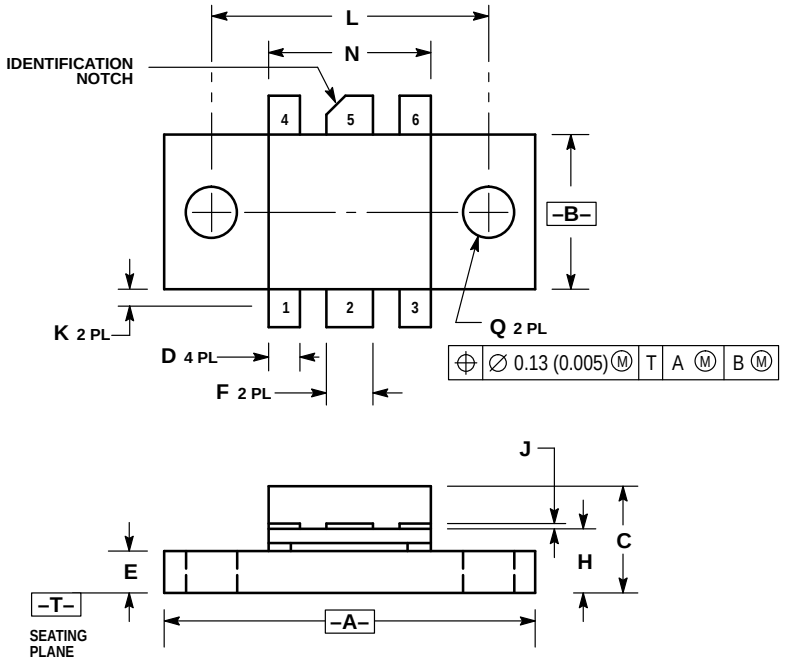


Figure 3. 960 MHz Test Circuit Components Layout

PACKAGE DIMENSIONS



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.965	0.985	24.52	25.01
B	0.390	0.410	9.91	10.41
C	0.250	0.290	6.35	7.36
D	0.075	0.090	1.91	2.28
E	0.095	0.115	2.42	2.92
F	0.110	0.130	2.80	3.30
H	0.155	0.175	3.94	4.44
J	0.004	0.006	0.11	0.15
K	0.090	0.116	2.29	2.94
L	0.725 BSC		18.41 BSC	
N	0.415	0.435	10.55	11.04
Q	0.120	0.135	3.05	3.42

STYLE 2:
PIN 1. EMITTER
2. BASE
3. EMITTER
4. EMITTER
5. COLLECTOR
6. EMITTER

CASE 333A–02
ISSUE C

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