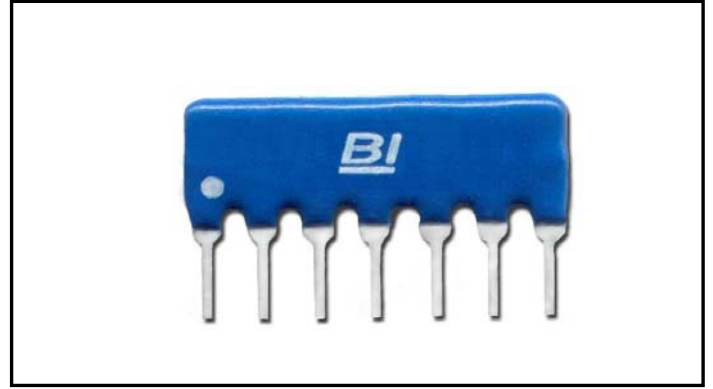


DIODE NETWORKS D SERIES

Single-In-Line

Conformal Coated

RoHS Compliant



FEATURES

- 4 to 14 Leads
- Multiple circuit configurations
- Space saving design

SPECIFICATION

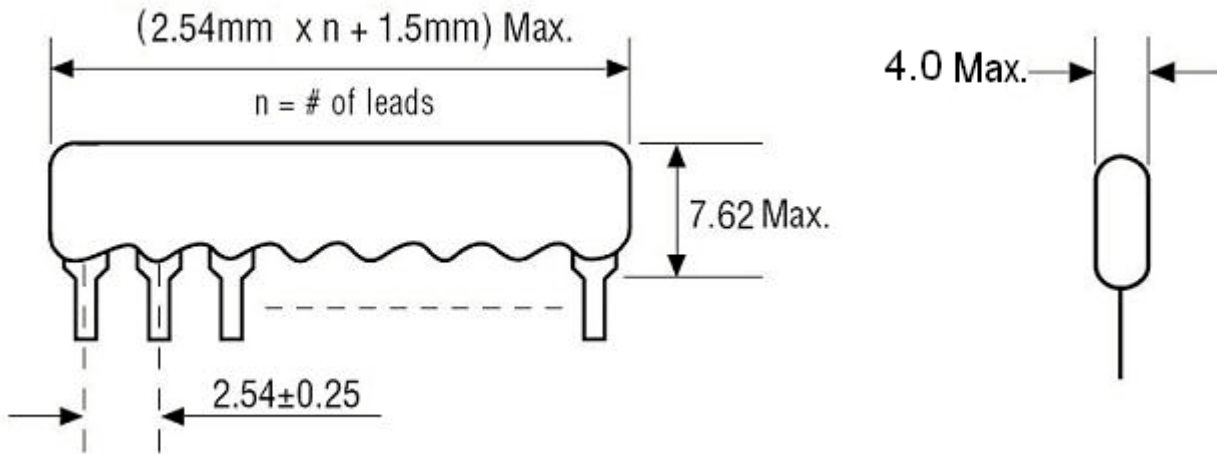
Reverse Voltage, V_R	80V
Reverse Current, I_R	1.0 μ A ($V_R = 70V$)
Forward Current, I_F	100mA Average, 300mA Surge (1 μ s Max.)
Forward Voltage, V_F	1.2V @ $I_F = 100mA$
Package Power, P_{PKG}	200mW @ 25°C
Reverse Recovery Time, t_{rr}	4ns ($V_R = 6V$, $I_F = 5mA$, $R_L = 50\Omega$)
Capacitance, C	5.5pF ($V_R = 6V$, $f = 1MHz$)
Storage Temperature Range	- 55°C to 125°C
Operating Temperature Range	- 25°C to 80°C

Specifications subject to change without notice.

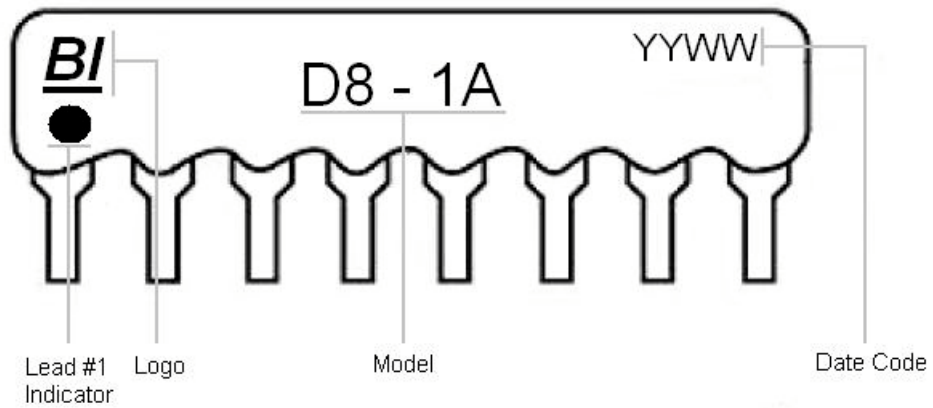
SCHEMATICS

Circuit type	Polarity "A"	Polarity "C"
<p>1</p> <p>N = 4 to 14</p>		
<p>2</p> <p>N = 3 to 13 (Odd #)</p>		
<p>3</p> <p>N = 4 to 14 (Even #)</p>		
<p>5</p> <p>N = 4 to 14</p>		

OUTLINE DIMENSIONS (mm)



TYPICAL PART MARKING



ORDERING INFORMATION

D 8 - 3 C
(1) (2) (3) (4)

- (1) Model Series: D
- (2) Number of pins: 4 to 14
- (3) Circuit type:
1 = Bussed with pin 1 in common.
2 = Bussed with centered common.
3 = Isolated.
5 = Dual termination.
- (4) Polarity (see schematic)
A = Common Anode (circuit 1, circuit 2)
 Annode is at pin number 1 (circuit 3, circuit 5)

C = Common Cathode (circuit 1, circuit 2)
 Cathode is at pin number 1 (circuit 3, circuit 5)

MATERIAL

Part	Material
Substrate	Alumina ceramic
Diode	Small mold package silicon epitaxial planar diode array
Termination	100% Ag
Lead Pins	100% Sn Plated steel
Junction and Lead Pin finish	96%Sn, 3.5%Ag, 0.5%Cu
Conformal Coat	Expoxy resin, UL-94 V-0 rated

PACKAGING

Number of pins	Qty. per plastic bag	Qty. per box
3 to 6	100	1000
7 to 12	100	500
13 to 14	50	250