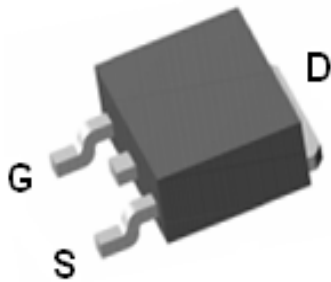


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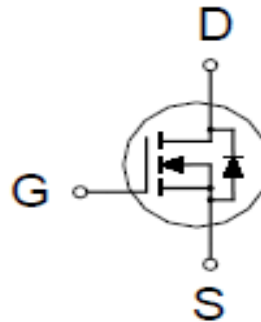
N-Channel High Voltage Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
600V	660mΩ @ $V_{GS} = 10V$	7A



TO-252



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	600	V
Gate-Source Voltage		V_{GS}	±30	
Continuous Drain Current ²	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	7	A
	$T_C = 100\text{ }^{\circ}\text{C}$		4.4	
Pulsed Drain Current ¹		I_{DM}	21	
Avalanche Current ³		I_{AS}	2	
Avalanche Energy ³		E_{AS}	80	mJ
Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	P_D	48	W
	$T_C = 100\text{ }^{\circ}\text{C}$		19	
Operating Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		62.5	$^{\circ}\text{C} / \text{W}$
Junction-to-Case	$R_{\theta JC}$		2.6	

¹Pulse width limited by maximum junction temperature.

²Ensure that the channel temperature does not exceed 150 $^{\circ}\text{C}$.

³ $V_{DS} = 50V$, $L = 40\text{mH}$, starting $T_J = 25^{\circ}\text{C}$

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N-Channel High Voltage Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	600			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	3.4	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 30V$			± 100	nA
Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V, T_C = 25\text{ }^{\circ}C$			1	μA
		$V_{DS} = 480V, V_{GS} = 0V, T_C = 100\text{ }^{\circ}C$			100	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 3.5A$		580	660	mΩ
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 3.5A$		5		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		555		pF
Output Capacitance	C_{oss}			435		
Reverse Transfer Capacitance	C_{rss}			9		
Effective Output Capacitance ⁴	$C_{o(er)}$	$V_{GS} = 0V, V_{DS} = 0 \text{ to } 480V$		33		
Total Gate Charge ²	Q_g	$V_{DD} = 480V, I_D = 7A, V_{GS} = 10V$		19		nC
Gate-Source Charge ²	Q_{gs}			3.2		
Gate-Drain Charge ²	Q_{gd}			9		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 300V$ $I_D = 7A, R_G = 10\Omega$		27		nS
Rise Time ²	t_r			52		
Turn-Off Delay Time ²	$t_{d(off)}$			72		
Fall Time ²	t_f			34		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current ³	I_S				7	A
Forward Voltage ¹	V_{SD}	$I_F = 7A, V_{GS} = 0V$			1.5	V
Reverse Recovery Time	t_{rr}	$I_F = 7A, di_F/dt = 100A / \mu S$		245		nS
Reverse Recovery Charge	Q_{rr}			2.4		uC

¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

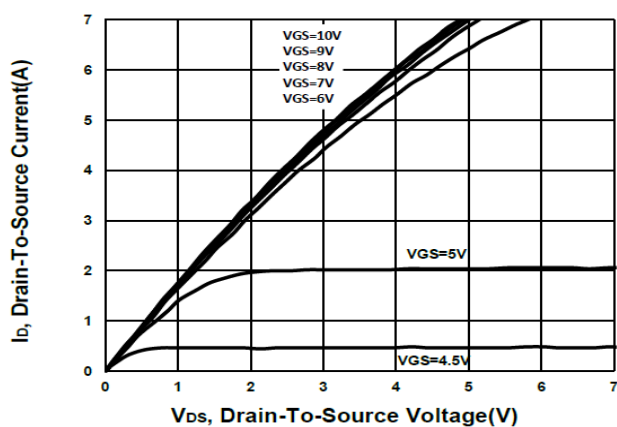
³Pulse width limited by maximum junction temperature.

⁴ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% $V_{(BR)DSS}$.

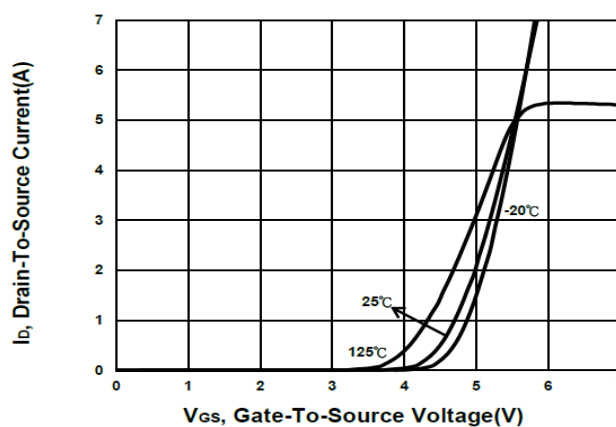
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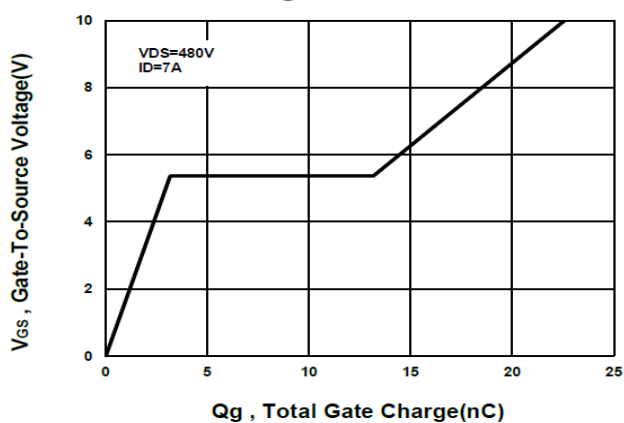
Output Characteristics



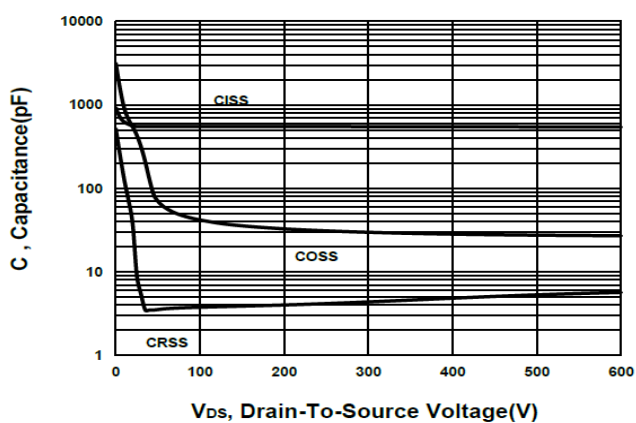
Transfer Characteristics



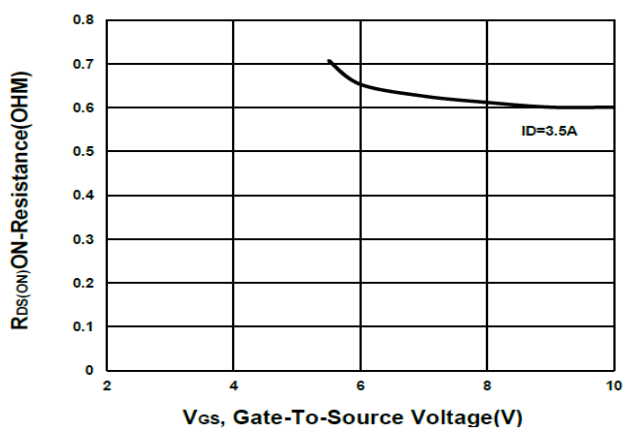
Gate charge Characteristics



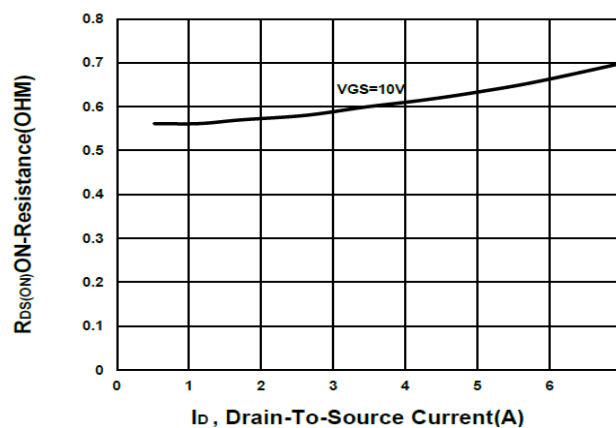
Capacitance Characteristic



On-Resistance VS Gate-To-Source



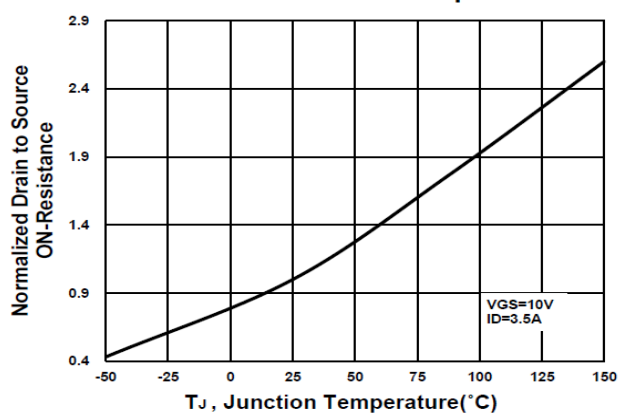
On-Resistance VS Drain Current



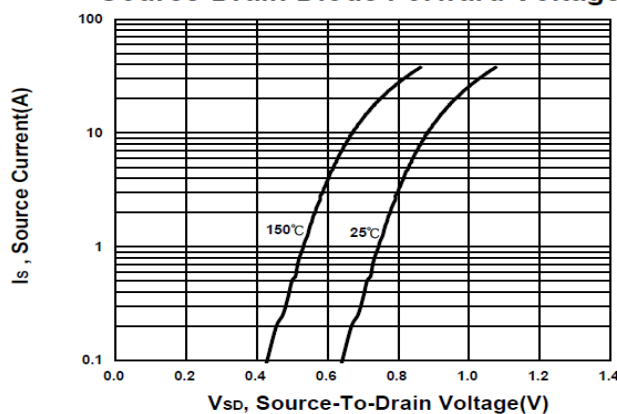
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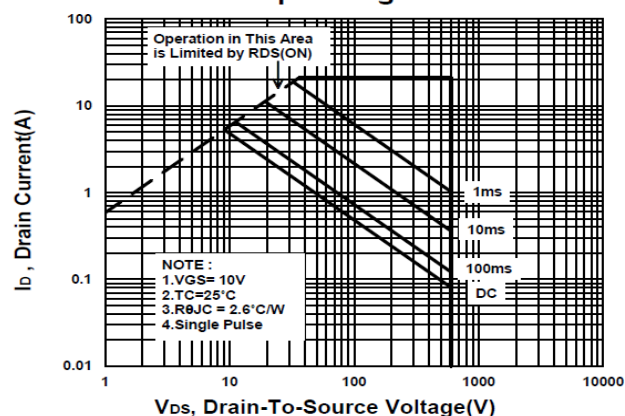
On-Resistance VS Temperature



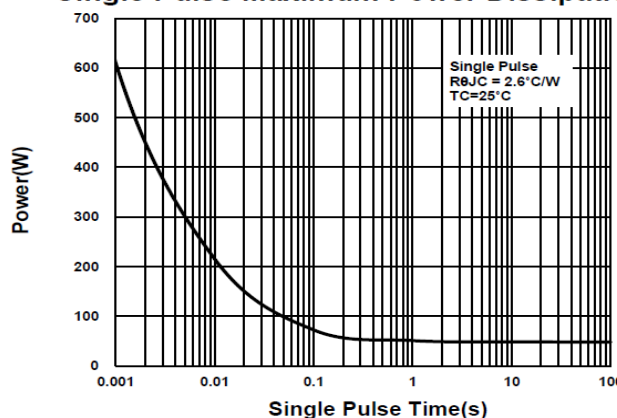
Source-Drain Diode Forward Voltage



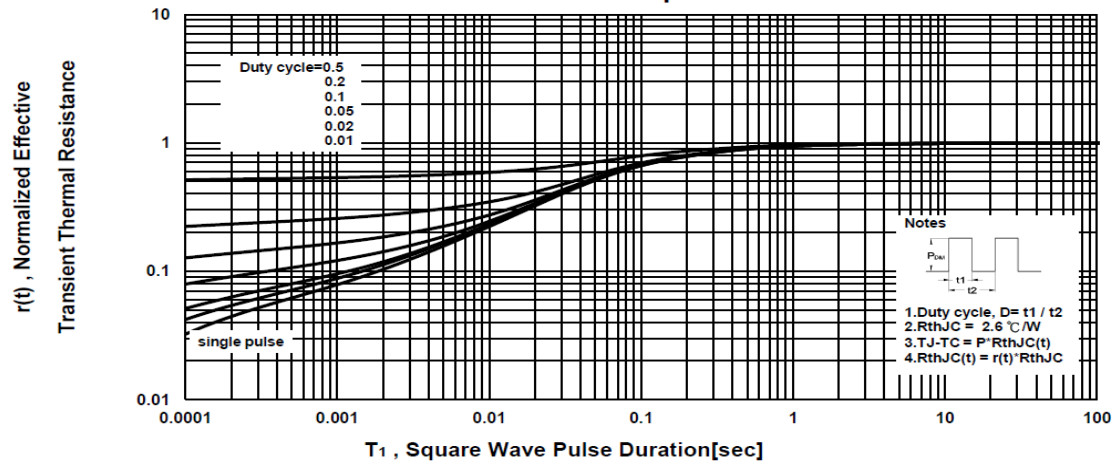
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



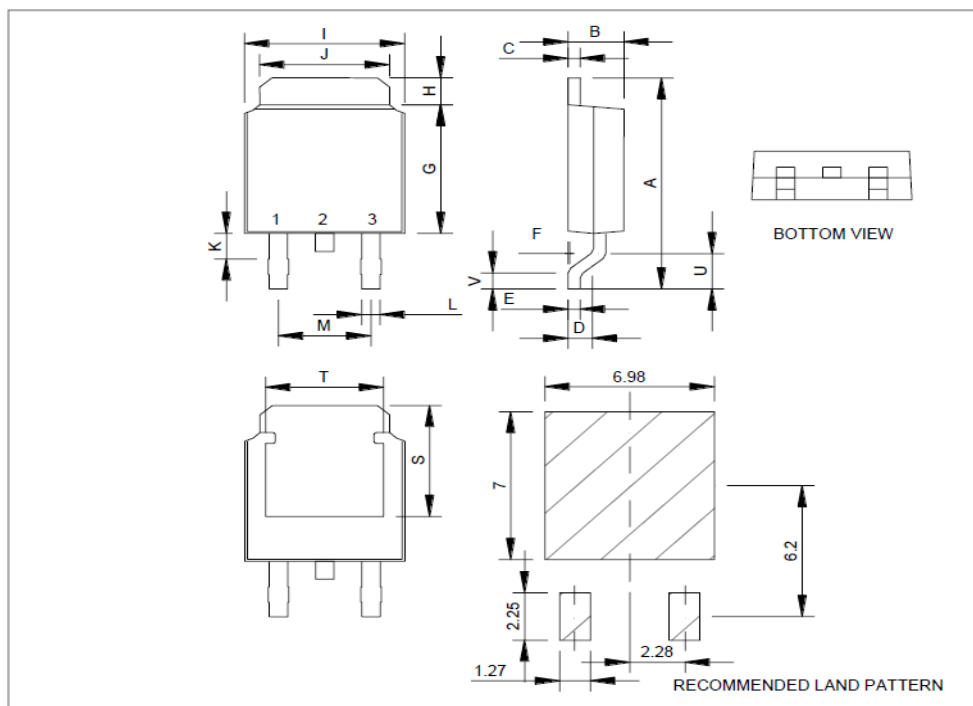
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N-Channel High Voltage Mode MOSFET

Package Dimension

TO-252 (DPAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	8.9	10	10.41	J	4.8		5.64
B	2.1	2.2	2.4	K	0.15		1.1
C	0.4	0.5	0.61	L	0.4	0.76	0.89
D	0.82	1.2	1.5	M	4.2	4.58	5
E	0.4	0.5	0.61	S	4.9	5.1	5.3
F	0		0.2	T	4.6	4.75	5.44
G	5.3	6.1	6.3	U	1.4		1.78
H	0.9		1.7	V	0.55	1.25	1.7
I	6.3	6.5	6.8				

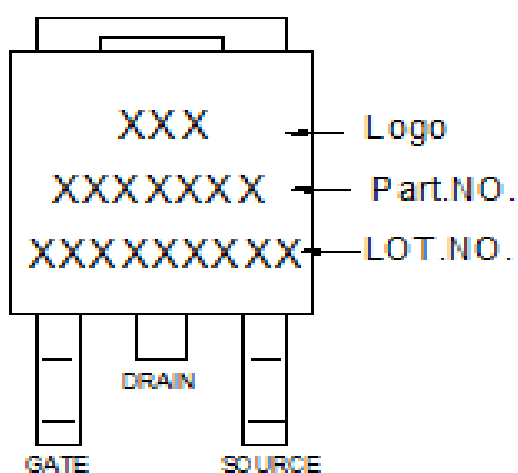


*因为各家封装厂模具不同而外观略有所差异，不影响电性及Layout。

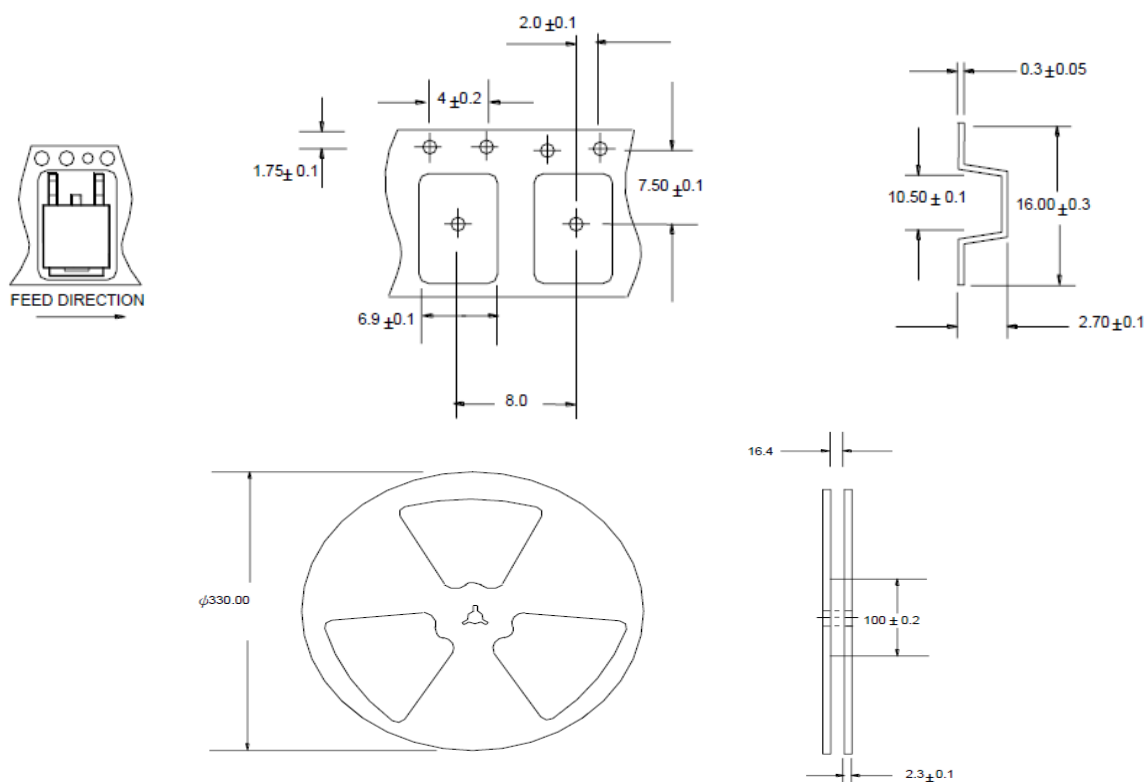
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N-Channel High Voltage Mode MOSFET

A. Marking Information



B. Tape&Reel Information:2500pcs/Reel

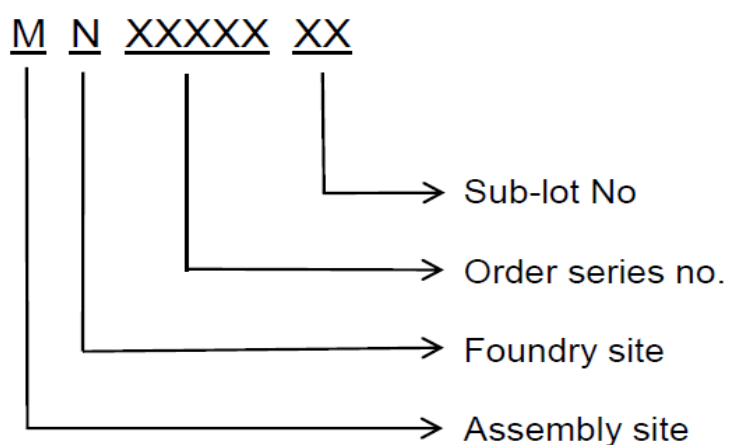


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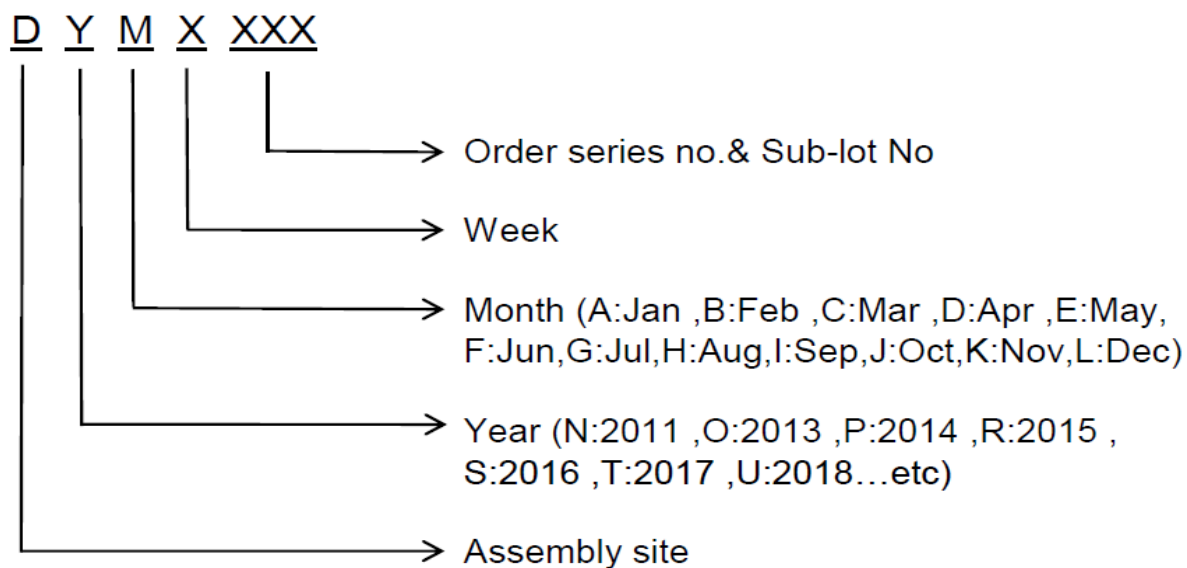
N-Channel High Voltage Mode MOSFET

C. Lot No.&Date Code rule

1.Lot No.



2.Date Code



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D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文"0"和数字"0", "G"和"Q"的字型即可)
3	U-NIKC	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	RoHS label	 long axis: 12 mm minor axis: 6 mm bottom color: White Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial
12	Scan information	Device / Lot / D/C / QTY, Insert "/" between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least