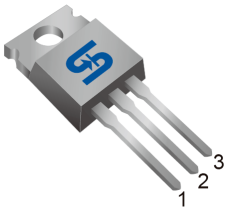
TAIWAN SEMICONDUCTOR

RoHS COMPLIANCE

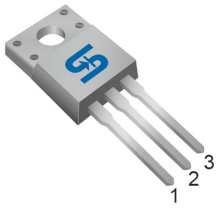
TSM7N90

900V N-Channel Power MOSFET

TO-220



ITO-220



Pin Definition:
1. Gate
2. Drain
3. Source

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$(Ω)	I_D (A)
900	1.9 @ V_{GS}=10V	7

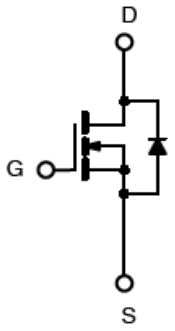
General Description

The TSM7N90 N-Channel enhancement mode Power MOSFET is produced by planar stripe DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply, electronic lamp ballast based on half bridge.

Features

- Low $R_{DS(ON)}$ 1.9Ω (Max.)
- Low gate charge typical @ 49nC (Typ.)
- Improve dv/dt capability

Block Diagram



N-Channel MOSFET

Ordering Information

Part No.	Package	Packing
TSM7N90CZ C0	TO-220	50pcs / Tube
TSM7N90CI C0	ITO-220	50pcs / Tube

Absolute Maximum Ratings (Ta = 25°C unless otherwise noted)

Parameter		Symbol	TO-220	ITO-220	Unit
Drain-Source Voltage		V_{DS}	900		V
Gate-Source Voltage		V_{GS}	±30		V
Continuous Drain Current	Tc = 25°C	I_D	7	7 *	A
	Tc = 100°C		4.31	4.31 *	
Pulsed Drain Current *		I_{DM}	28	28 *	A
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5		V
Single Pulse Avalanche Energy (Note 2)		E_{AS}	106		mJ
Avalanche Current (Repetitive) (Note 1)		I_{AR}	7		A
Repetitive Avalanche Energy (Note 1)		E_{AR}	25		mJ
Power Dissipation	Tc = 25°C	P_D	250	40.3	W
	Derate above 25℃		2	0.32	ºC/W
Operating Junction Temperature		T_J	150		ºC
Storage Temperature Range		T_{STG}	-55 to +150		°C

* Limited by maximum junction temperature

TSM7N90

900V N-Channel Power MOSFET

Thermal Performance

Parameter	Symbol	TO-220	ITO-220	Unit
Thermal Resistance - Junction to Case	$R\Theta_{JC}$	0.5	3.1	°C/W
Thermal Resistance - Junction to Ambient	$R\Theta_{JA}$	62.5		

Notes: Surface mounted on FR4 board t ≤ 10sec

Electrical Specifications (Tc = 25°C unless otherwise noted)

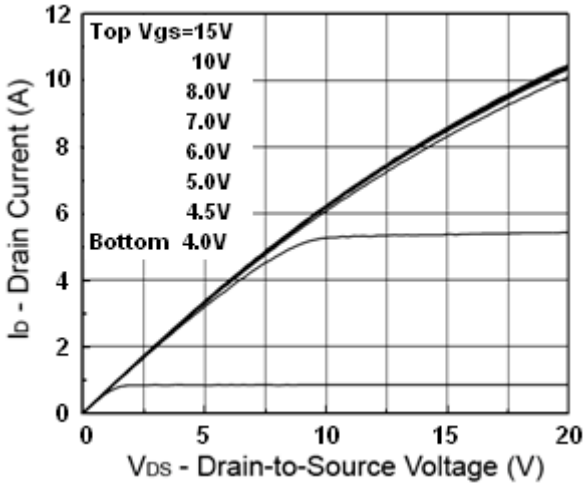
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250μA	BV_{DSS}	900	--	--	V
Drain-Source On-State Resistance	V_{GS} = 10V, I_D = 3.5A	$R_{DS(ON)}$	--	1.52	1.9	Ω
Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	$V_{GS(TH)}$	2.0	--	4.0	V
Zero Gate Voltage Drain Current	V_{DS} = 900V, V_{GS} = 0V	I_{DSS}	--	--	10	μA
Gate Body Leakage	V_{GS} = ±30V, V_{DS} = 0V	I_{GSS}	--	--	±100	nA
Forward Transconductance	V_{DS} = 30V, I_D = 3.5A	g_{fs}	--	7	--	S
Diode Forward Voltage	I_S = 7A, V_{GS} = 0V	V_{SD}	--	--	1.5	V
Dynamic [b]						
Total Gate Charge	V_{DS} = 720V, I_D = 7A, V_{GS} = 10V	Q_g	--	49	--	nC
Gate-Source Charge		Q_{gs}	--	7	--	
Gate-Drain Charge		Q_{gd}	--	20	--	
Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz	C_{iss}	--	1969	--	pF
Output Capacitance		C_{oss}	--	133	--	
Reverse Transfer Capacitance		C_{rss}	--	11	--	
Switching [c]						
Turn-On Delay Time	V_{GS} = 10V, I_D = 7A, V_{DD} = 450V, R_G = 25Ω	$t_{d(on)}$	--	39	--	ns
Turn-On Rise Time		t_r	--	38	--	
Turn-Off Delay Time		$t_{d(off)}$	--	155	--	
Turn-Off Fall Time		t_f	--	45	--	
Reverse Recovery Time	V_{GS} = 0V, I_S = 7A, dI_F/dt = 100A/μs	t_{fr}	--	464	--	ns
Reverse Recovery Charge		Q_{fr}	--	4.7	--	uC

Notes:

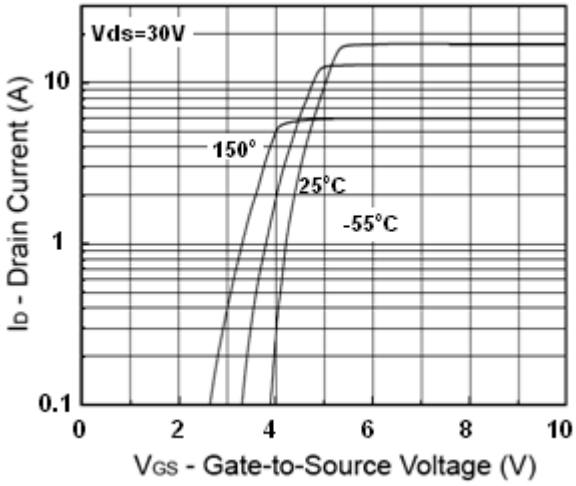
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. V_{DD} = 50V, I_{AS}=7A, L=4.1mH, R_G=25Ω, Starting T_J=25℃
   Guaranteed 100% E_{AS} Test Condition: V_{DD} = 50V, I_{AS}=7A, L=1mH, R_G =25Ω, Starting T_J=25ºC
3. I_{SD} ≤7A, di/dt ≤ 200A/≤μs, V_{DD} ≤ BV, Starting T_J=25℃
4. Pulse test: pulse width ≤300μs, duty cycle ≤2%
5. b For design reference only, not subject to production testing.
6. c Switching time is essentially independent of operating temperature.

Electrical Characteristics Curve (Tc = 25°C, unless otherwise noted)

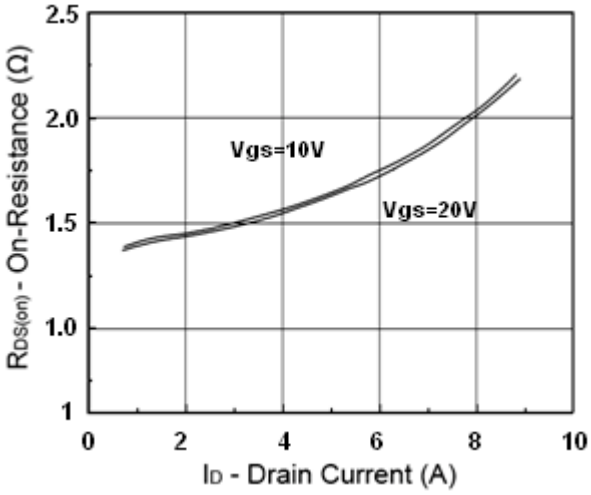
Output Characteristics



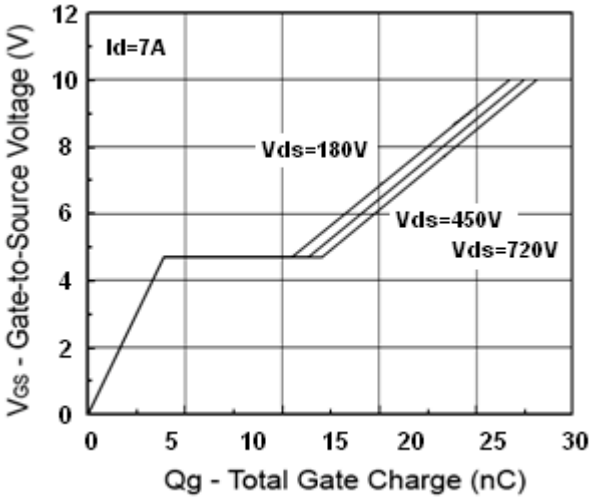
Transfer Characteristics



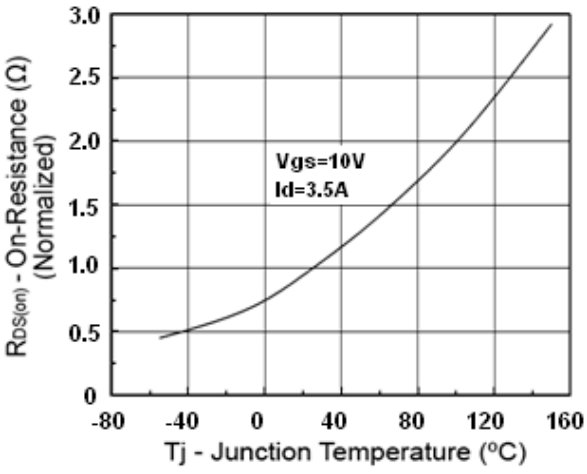
On-Resistance vs. Drain Current



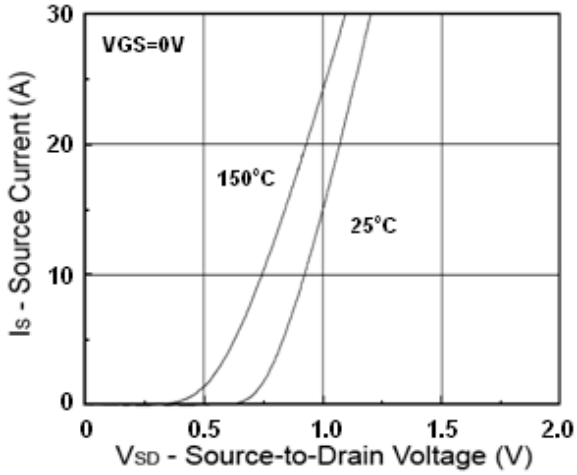
Gate Charge



On-Resistance vs. Junction Temperature

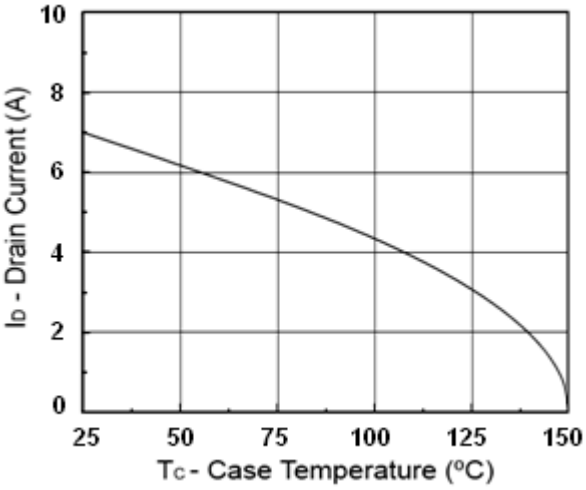


Source-Drain Diode Forward Voltage

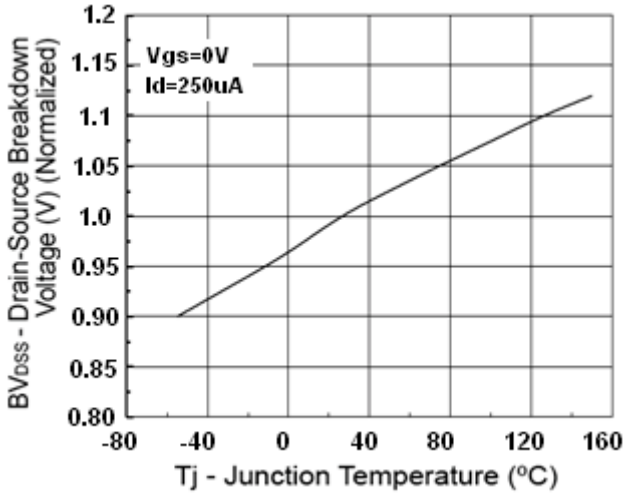


Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

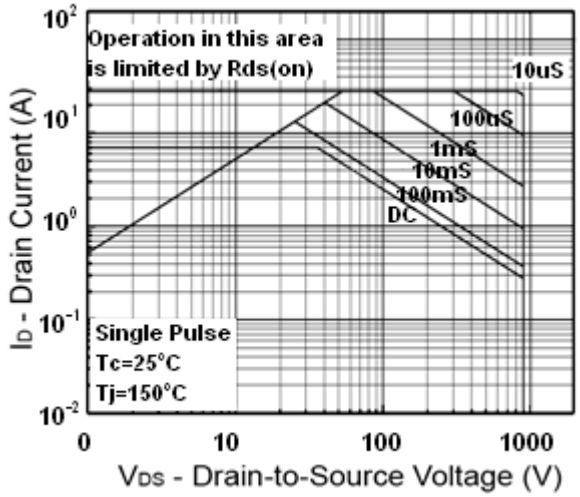
Drain Current vs. Case Temperature



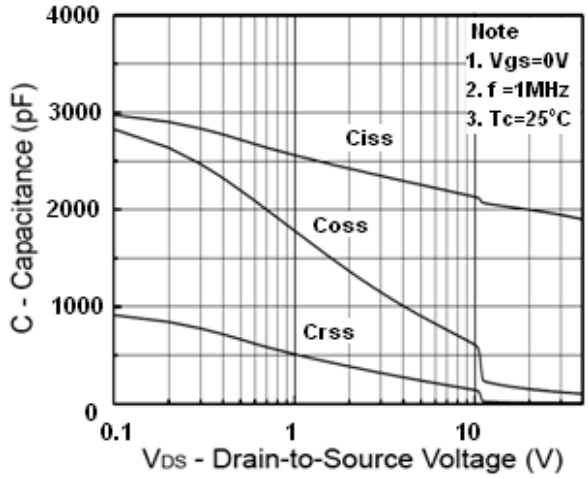
BV_{DSS} vs. Junction Temperature



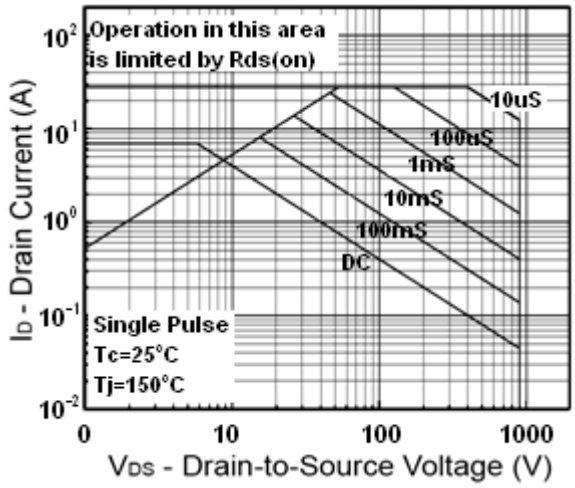
Maximum Safe Operating Area



Capacitance vs. Drain-Source Voltage

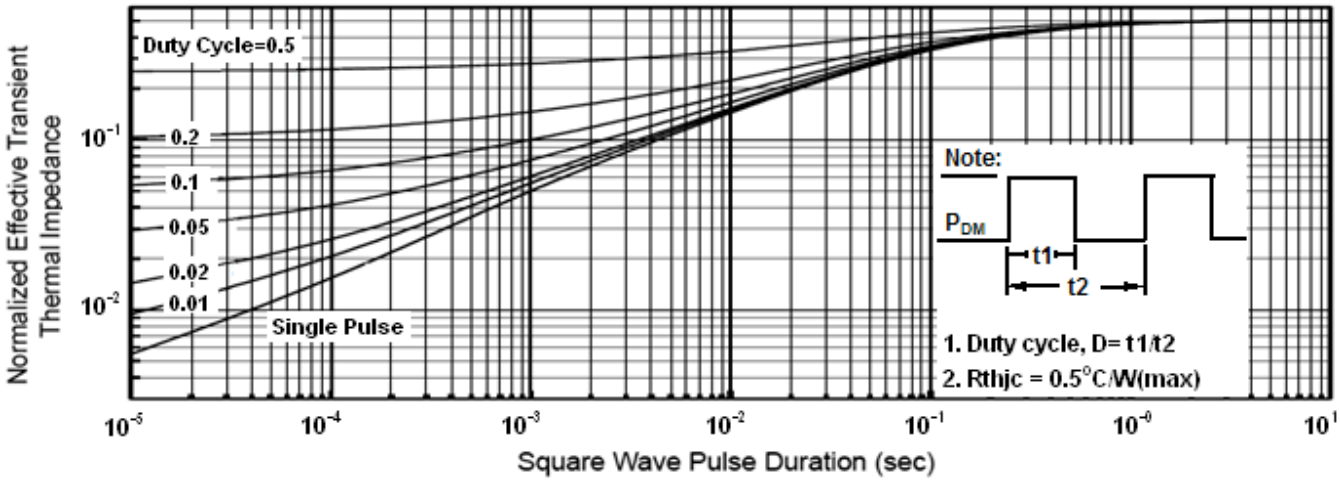


Maximum Safe Operating Area (ITO-220)

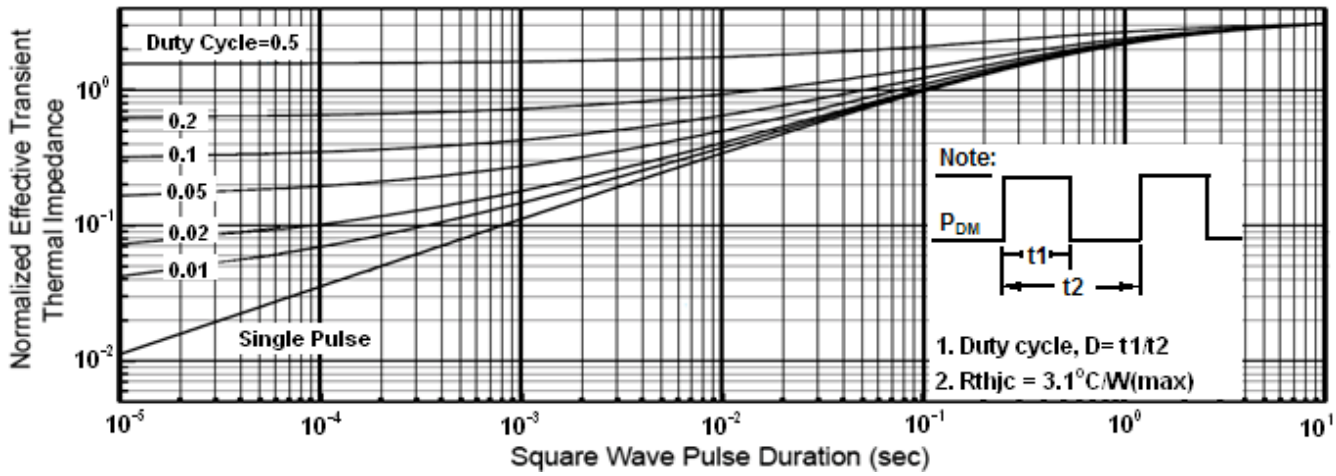


Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

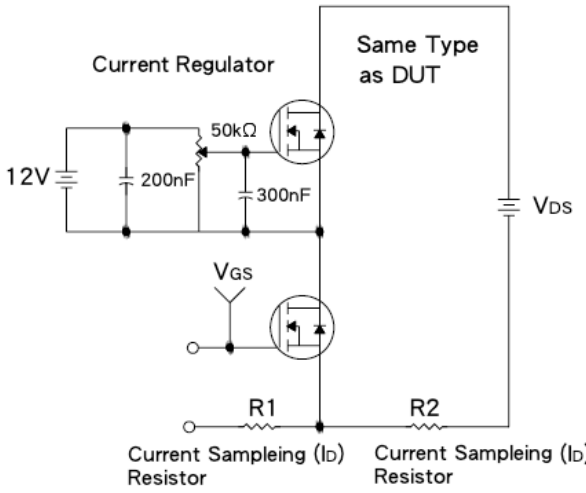
Normalized Thermal Transient Impedance, Junction-to-Ambient

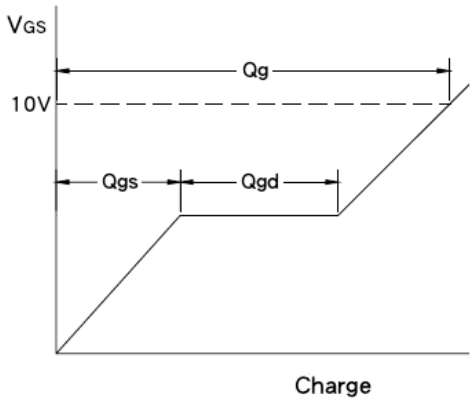


Normalized Thermal Transient Impedance, Junction-to-Ambient (ITO-220)

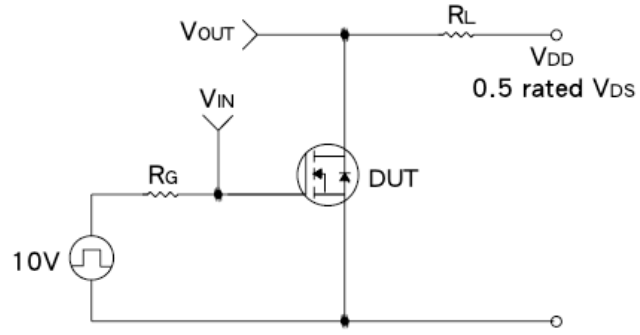


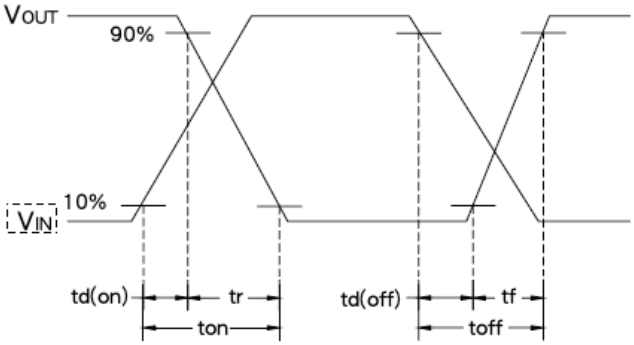
Gate Charge Test Circuit & Waveform



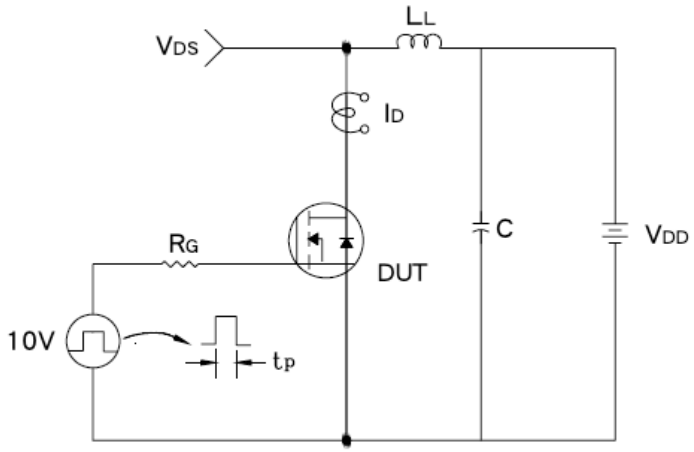


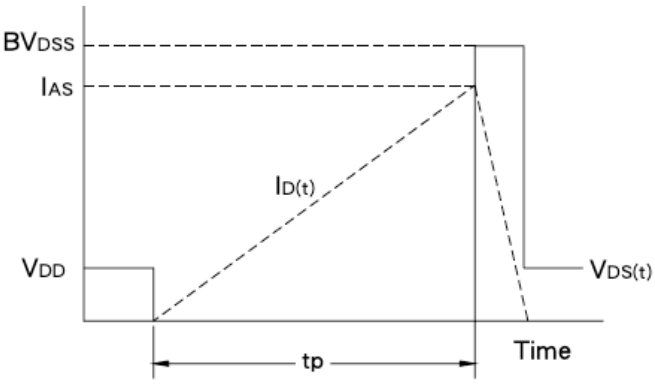
Resistive Switching Test Circuit & Waveform



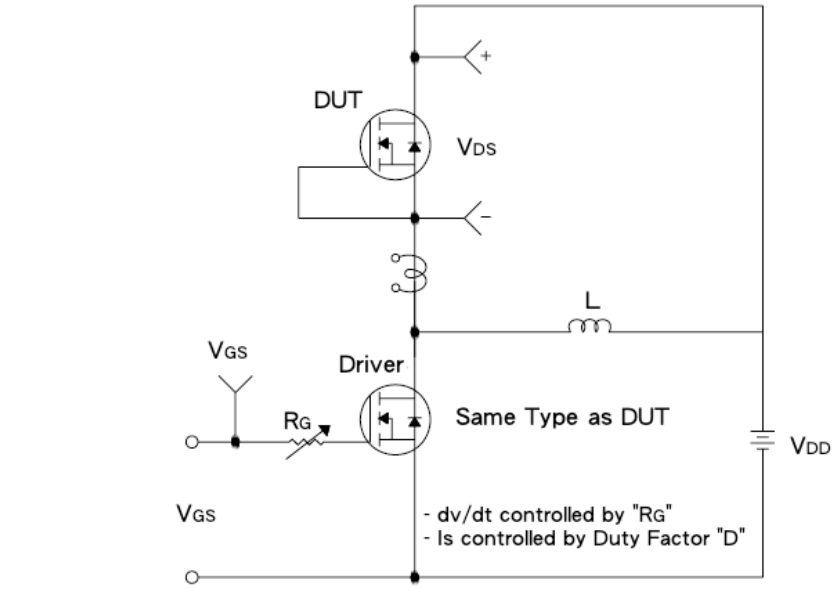


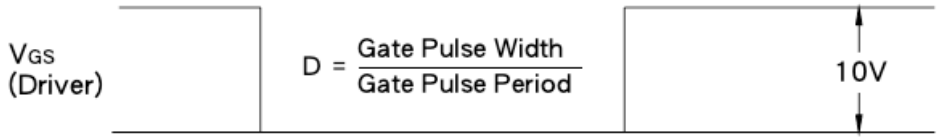
E_{AS} Test Circuit & Waveform

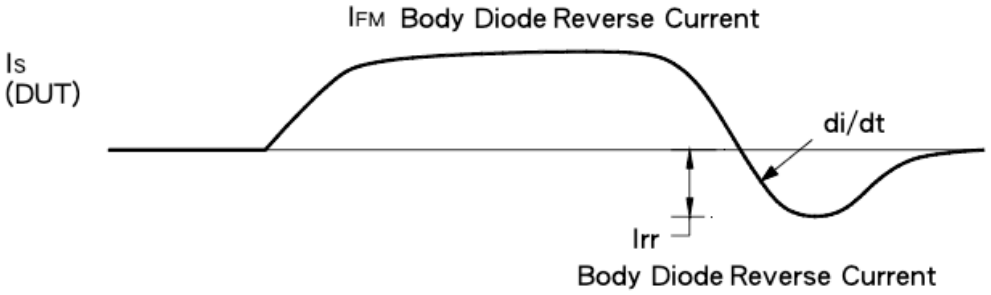


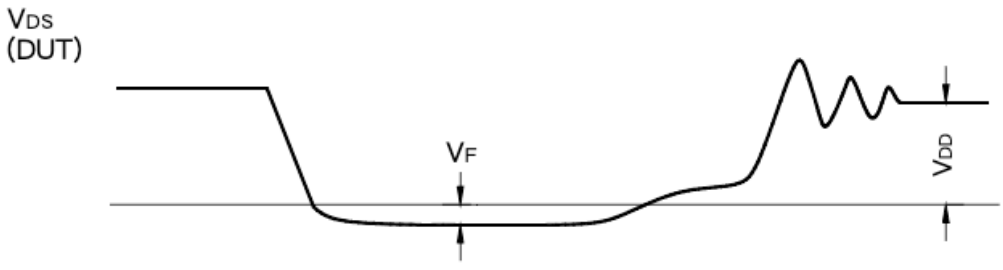


Diode Reverse Recovery Time Test Circuit & Waveform

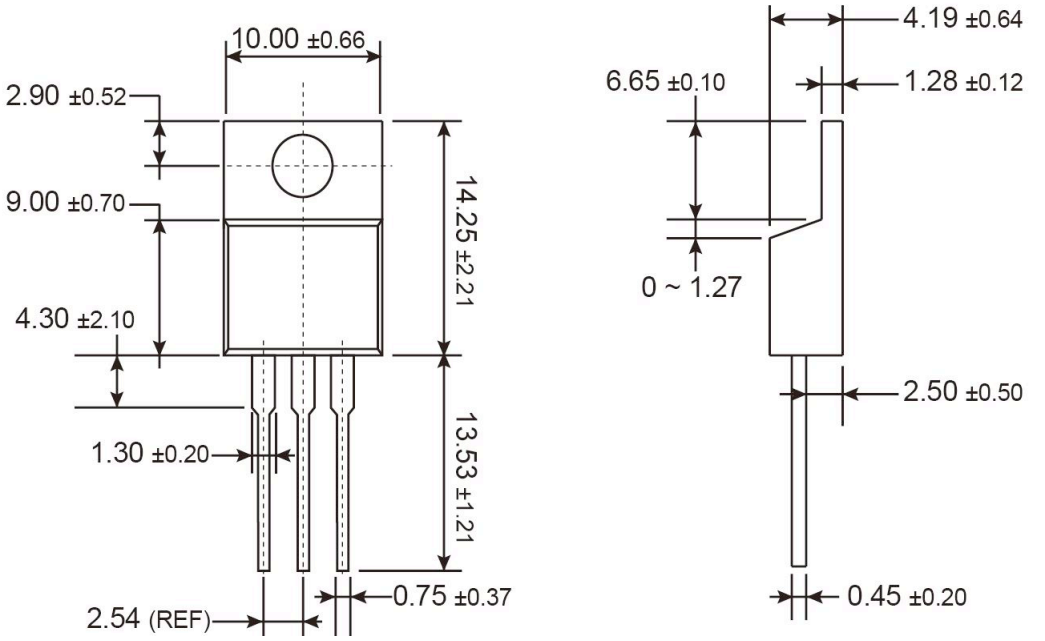






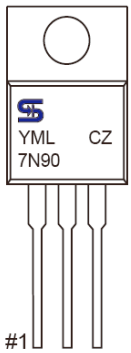


TO-220 Mechanical Drawing



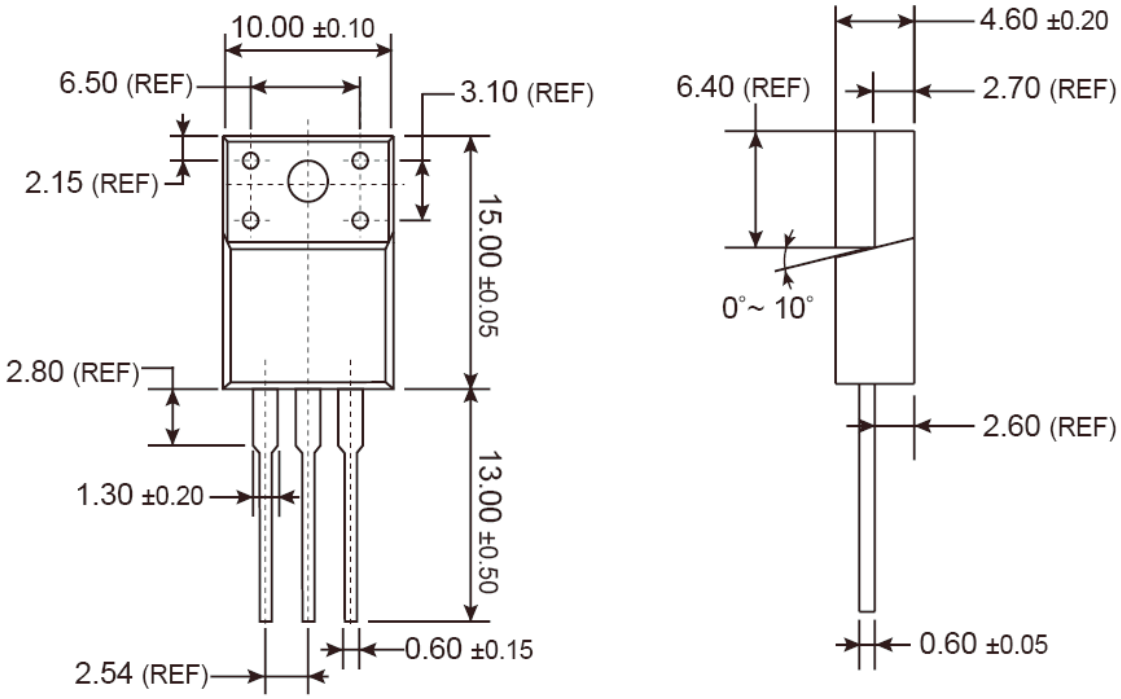
Unit: Millimeters

Marking Diagram



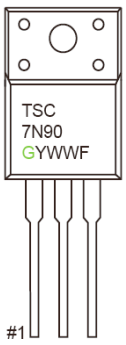
Y = Year Code
M = Month Code for Halogen Free Product
(**O**=Jan, **P**=Feb, **Q**=Mar, **R**=Apl, **S**=May, **T**=Jun, **U**=Jul, **V**=Aug, **W**=Sep, **X**=Oct, **Y**=Nov, **Z**=Dec)
L = Lot Code

ITO-220 Mechanical Drawing



Unit: Millimeters

Marking Diagram



G = Halogen Free
Y = Year Code
WW = Week Code by Calendar Year
F = Factory Code

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