HD74LV1GT86A

2–input Exclusive–OR Gate / CMOS Logic Level Shifter

REJ03D0122-1000
Rev.10.00
Mar 21, 2008

Description

The HD74LV1GT86A performs the Boolean functions $Y = A \oplus B$ or $Y = \overline{A}B + A\overline{B}$ in positive logic. A common application is as a true / complement element. If one of the inputs is low, the other input will be reproduced in true form at the output. If one of the inputs is high, the signal on the other input will be reproduced inverted form at the output. The input protection circuitry on this device allows over voltage tolerance on the input, allowing the device to be used as a logic–level translator from 3.0 V CMOS Logic to 5.0 V CMOS Logic or from 1.8 V CMOS logic to 3.0 V CMOS Logic while operating at the high-voltage power supply. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

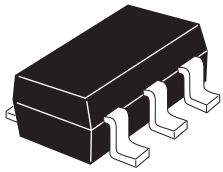
- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- TTL compatible input level.
  Supply voltage range : 3.0 to 5.5 V
  Operating temperature range : –40 to +85°C
- Logic-level translate function
  3.0 V CMOS logic → 5.0 V CMOS logic (@V_{CC} = 5.0 V)
  1.8 V or 2.5 V CMOS logic → 3.3 V CMOS logic (@V_{CC} = 3.3 V)
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V)
  All outputs V_O (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current ±6 mA (@V_{CC} = 3.0 V to 3.6 V), ±12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1GT86ACME	CMPAK–5 pin	PTSP0005ZC-A (CMPAK-5V)	CM	E (3000 pcs/reel)
HD74LV1GT86AVSE	VSON–5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

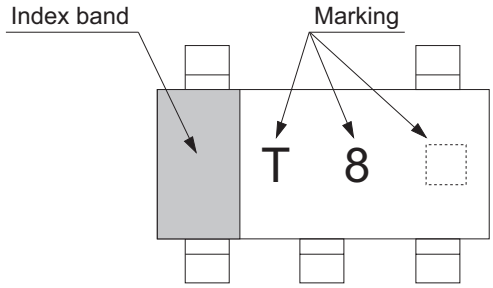
Note: Please consult the sales office for the above package availability.

Outline and Article Indication

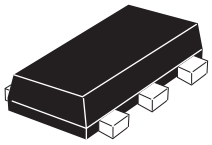
• HD74LV1GT86A



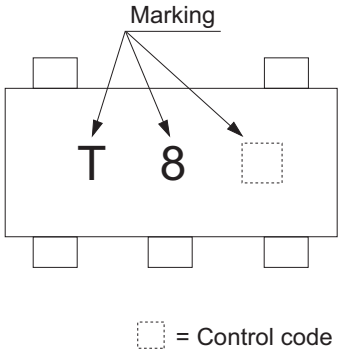
CMPAK–5



• HD74LV1GT86A



VSON–5



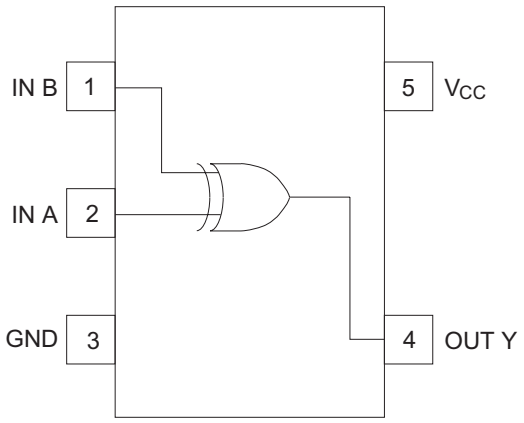
Function Table

Inputs		Output Y
A	**B**	
L	L	L
L	H	H
H	L	H
H	H	L

H : High level

L : Low level

Pin Arrangement



(Top view)

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V_{CC}	–0.5 to 7.0	V	
Input voltage range [*1]	V_I	–0.5 to 7.0	V	
Output voltage range [*1, 2]	V_O	–0.5 to V_{CC} + 0.5	V	Output : H or L
		–0.5 to 7.0		V_{CC} : OFF
Input clamp current	I_{IK}	–20	mA	V_I < 0
Output clamp current	I_{OK}	±50	mA	V_O < 0 or V_O > V_{CC}
Continuous output current	I_O	±25	mA	V_O = 0 to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) [*3]	P_T	200	mW	
Storage temperature	Tstg	–65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.
3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	3.0	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_O	0	V_{CC}	V	
Output current	I_{OL}	—	6	mA	V_{CC} = 3.0 to 3.6 V
		—	12		V_{CC} = 4.5 to 5.5 V
	I_{OH}	—	–6		V_{CC} = 3.0 to 3.6 V
		—	–12		V_{CC} = 4.5 to 5.5 V
Input transition rise or fall rate	Δt / Δv	0	100	ns / V	V_{CC} = 3.0 to 3.6 V
		0	20		V_{CC} = 4.5 to 5.5 V
Operating free-air temperature	T_a	–40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

- Ta = –40 to 85°C

Item	Symbol	V_{CC} (V) *	Min	Typ	Max	Unit	Test condition
Input voltage	V_{IH}	3.0 to 3.6	1.5	—	—	V	
		4.5 to 5.5	2.0	—	—		
	V_{IL}	3.0 to 3.6	—	—	0.6		
		4.5 to 5.5	—	—	0.8		
Hysteresis voltage	V_H	3.3	—	0.10	—	V	$V_T^+ - V_T^-$
		5.0	—	0.15	—		
Output voltage	V_{OH}	Min to Max	V_{CC}–0.1	—	—	V	I_{OH} = –50 µA
		3.0	2.48	—	—		I_{OH} = –6 mA
		4.5	3.8	—	—		I_{OH} = –12 mA
	V_{OL}	Min to Max	—	—	0.1		I_{OL} = 50 µA
		3.0	—	—	0.44		I_{OL} = 6 mA
		4.5	—	—	0.55		I_{OL} = 12 mA
Input current	I_{IN}	0 to 5.5	—	—	±1	µA	V_{IN} = 5.5 V or GND
Quiescent supply current	I_{CC}	5.5	—	—	10	µA	V_{IN} = V_{CC} or GND, I_O = 0
	ΔI_{CC}	5.5	—	—	1.5	mA	One input V_{IN} = 3.4 V, other input V_{CC} or GND
Output leakage current	I_{OFF}	0	—	—	5	µA	V_{IN} or V_O = 0 to 5.5 V
Input capacitance	C_{IN}	5.0	—	2.5	—	pF	V_{IN} = V_{CC} or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

- V_{CC} = 3.3 ± 0.3 V

Item	Symbol	Ta = 25°C			Ta = –40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	8.0	12.5	1.0	14.0	ns	C_L = 15 pF	A or B	Y
	t_{PHL}	—	9.5	17.0	1.0	19.0		C_L = 50 pF		

- V_{CC} = 5.0 ± 0.5 V

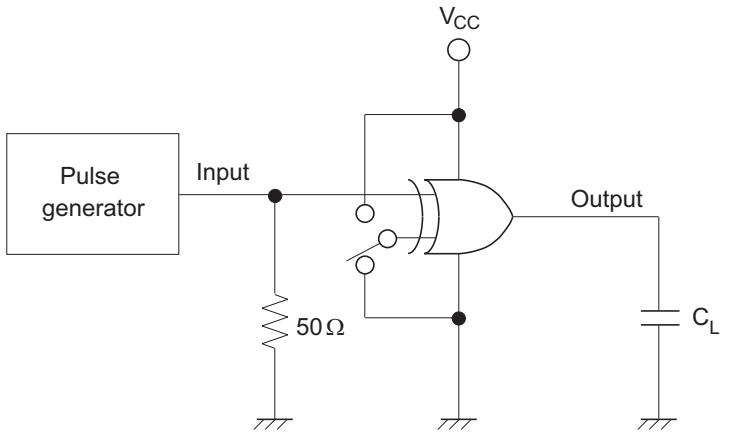
Item	Symbol	Ta = 25°C			Ta = –40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	5.5	7.5	1.0	8.5	ns	C_L = 15 pF	A or B	Y
	t_{PHL}	—	6.5	10.3	1.0	11.5		C_L = 50 pF		

Operating Characteristics

- C_L = 50 pF

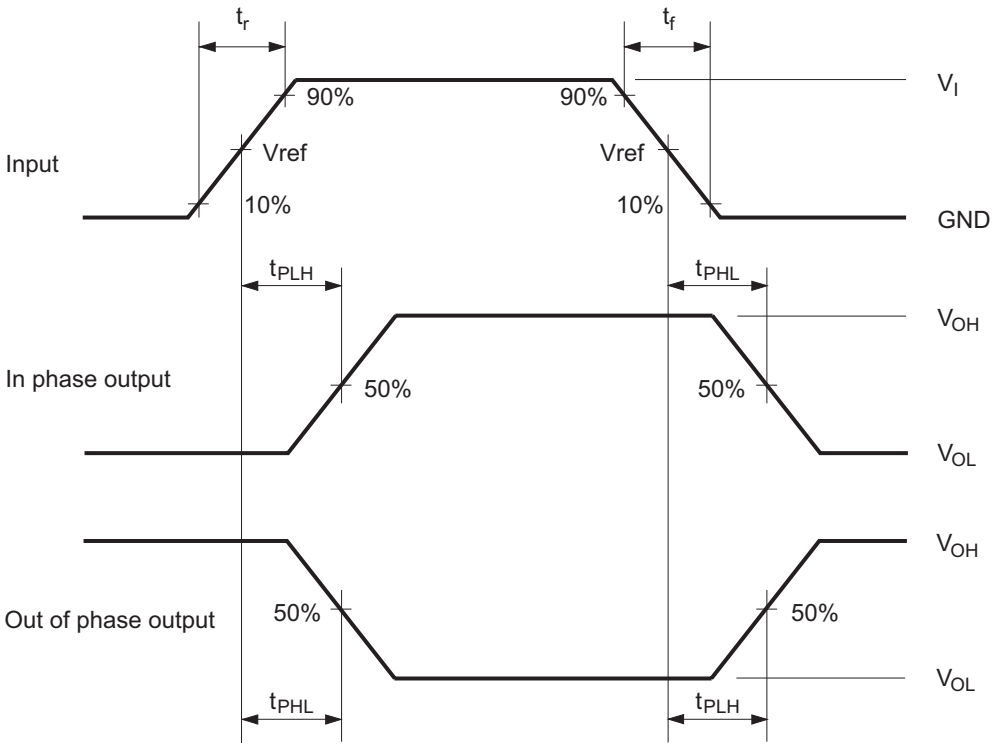
Item	Symbol	V_{CC} (V)	Ta = 25°C			Unit	Test Conditions
			Min	Typ	Max		
Power dissipation capacitance	C_{PD}	5.0	—	11.0	—	pF	f = 10 MHz

Test Circuit



Note: Operating current test time, output is open.

Waveforms

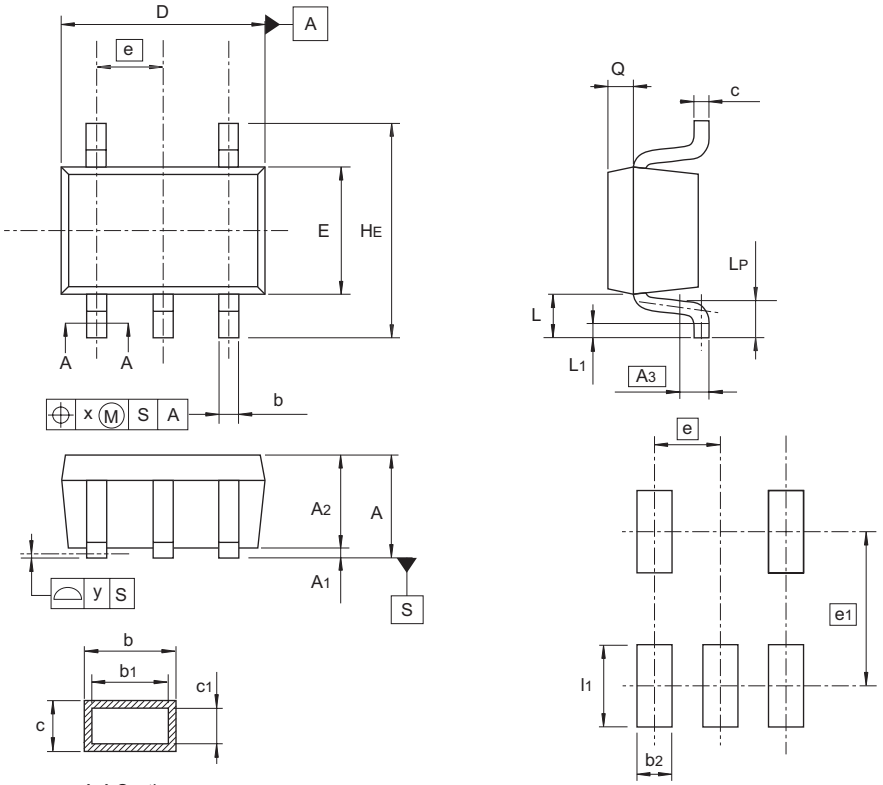


V_{CC} (V)	INPUTS		Vref
	V_I	t_r / t_f	
3.3±0.3	2.5 V	≤ 3.0 ns	50%
5.0±0.5	3 V	≤ 3.0 ns	1.5 V

Notes: 1. Input waveform : PRR ≤ 1 MHz, Zo = 50 Ω.
2. The output are measured one at a time with one transition per measurement.

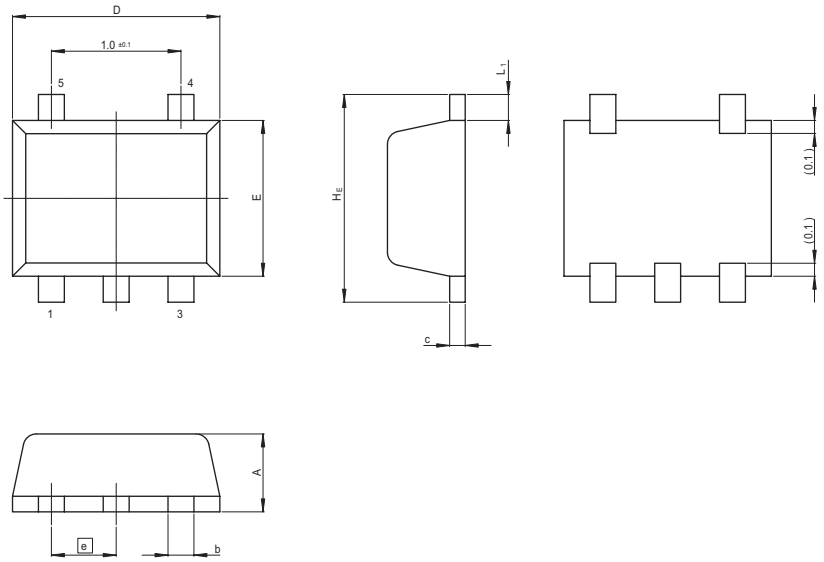
Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
SC-88A	PTSP0005ZC-A	CMPAK-5 / CMPAK-5V	0.006g



Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.8	—	1.1
A1	0	—	0.1
A2	0.8	0.9	1.0
A3	—	0.25	—
b	0.15	0.22	0.3
b1	—	0.2	—
c	0.1	0.13	0.15
c1	—	0.11	—
D	1.8	2.0	2.2
E	1.15	1.25	1.35
e	—	0.65	—
HE	1.8	2.1	2.4
L	0.3	—	0.7
L1	0.1	—	0.5
LP	0.2	—	0.6
x	—	—	0.05
y	—	—	0.05
b2	—	—	0.35
e1	—	1.5	—
l1	—	—	0.9
Q	—	0.25	—

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-USON5-1.2x1.6-0.50	PUSN0005KA-A	TNP-5D/TNP-5DV	0.002g



Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	1.55	1.6	1.65
E	1.1	1.2	1.3
A	—	—	0.6
A1	—	—	—
A2	—	—	—
b	0.15	0.2	0.3
b1	—	—	—
e	—	0.5	—
Lp	—	—	—
x	—	—	—
y	—	—	—
ZD	—	—	—
c	0.07	0.12	0.22
c1	—	—	—
HE	1.55	1.6	1.65
L1	—	0.2	—

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