

NPN GENERAL PURPOSE DUAL TRANSISTOR

DESCRIPTION

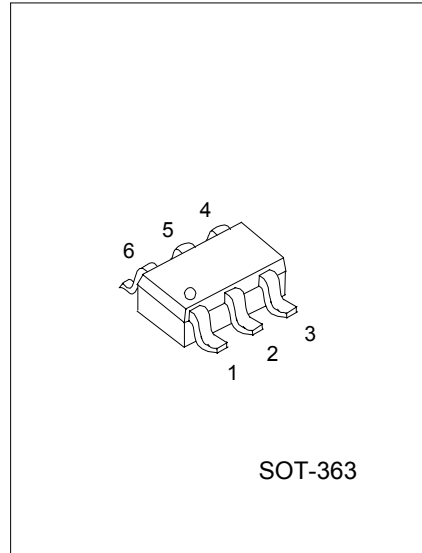
Two independently operating NPN transistors.

FEATURES

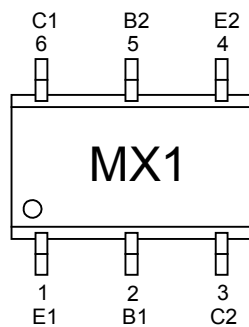
- *Low current (max.100mA)
- *Low voltage (max.40V)
- *Reduces number of components and board space.
- *Complement to PUMT1.

APPLICATIONS

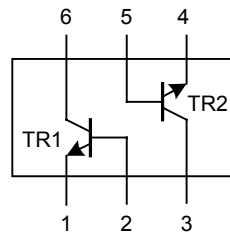
- *General purpose switching and amplification.



PIN CONFIGURE/MARKING CODE



EQUIVALENT CIRCUITS



The following characteristics apply to both Tr1 and Tr2

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	40	V
Emitter-base voltage	V _{EBO}	5	V
Collector current (DC)	I _C	100	mA
Peak collector current	I _{CM}	200	mA
Peak base current	I _{BM}	200	mA
Collector Power Dissipation (total)	P _C	300	mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-65~+150	°C

ELECTRICAL CHARACTERISTICS (Ta=25°C, unless otherwise specified)

PARAMETER	SYMBOL	CONDITION	MIN	MAX	UNIT
Collector cut-off current	I _{CBO}	I _E =0, V _{CB} =30V		100	nA
		I _E =0, V _{CB} =30V, T _j =150°C		10	μA
Emitter cut-off current	I _{EBO}	V _{EB} =4V, I _C =0		100	nA
DC current gain	h _{FE}	I _C =1mA, V _{CE} =6V	120		
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =50mA, I _B =5mA (note 1)		200	mV
Collector capacitance	C _C	I _E =I _{CE} =0, V _{CB} =12V, f=1MHz		1.5	pF
Transition frequency	f _T	I _C =2mA, V _{CE} =12V, f=100MHz	100		MHz

Note: 1. Pulse test: t_p ≤ 300μs, δ ≤ 0.02

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