

UNISONIC TECHNOLOGIES CO., LTD

UT8067 Preliminary Power MOSFET

9A, 30V, N-CHANNEL MOSFET

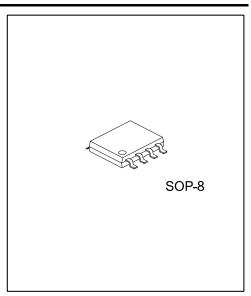
■ DESCRIPTION

The UTC **UT8067** is an N-channel MOSFET, it uses UTC's advanced technology to provide the customers with high switching speed and a minimum on-state resistance, etc.

The UTC **UT8067** is suitable for high-efficiency DC-DC converters, mobile handsets and notebook PCs.

■ FEATURES

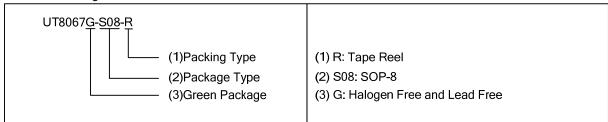
- * $R_{DS(ON)}$ < 33 m Ω @ V_{GS} =4.5V, I_{D} =4.5A
- * High switching speed



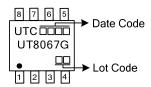
■ ORDERING INFORMATION

Ordering Number	Package	Pin Assignment							Dooking	
		1	2	3	4	5	6	7	8	Packing
UT8067G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: S: Source G: Gate D: Drain



MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous (Note 1)	I _D	9	Α
	Pulsed (Note 1)	I _{DM}	36	Α
Avalanche Current		I _{AR}	9	Α
Single Pulsed Avalanche Energy (Note 2)		E _{AS}	21	mJ
Power Dissipation (t=10s)		P_{D}	1.0	W
Junction Temperature		TJ	150	°C
Storage Temperature Range		T _{STG}	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 0.2mH, I_{AS} = 9A, V_{DD} = 24V, R_G = 1.2 Ω , Starting T_J = 25°C

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient (t=10s)	θ_{JA}	125	°C/W

Note: Surface Mounted on 25.4 mmx25.4 mmx0.8mm FR4 Board.

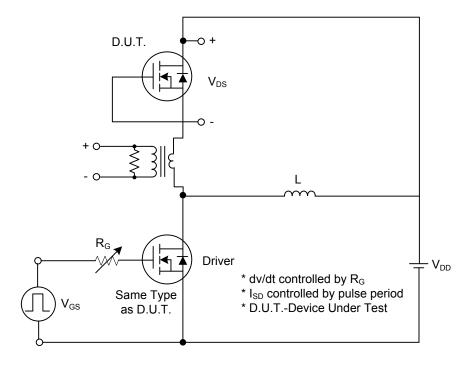
■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV_{DSS}	I _D =10mA, V _{GS} =0V	30			V	
		BV_{DSX}	I _D =10mA, V _{GS} =-20V	15			V	
Drain-Source Leakage Current		I_{DSS}	V_{DS} =30V, V_{GS} =0V			10	μΑ	
Gate-Source Leakage Current	Forward		V_{GS} =+20V, V_{DS} =0V			+0.1	μΑ	
	Reverse	I_{GSS}	V _{GS} =-20V, V _{DS} =0V			-0.1	μΑ	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS} =10V, I_{D} =0.1mA	1.3		2.3	V	
Static Drain-Source On-State Resistance	nointanaa	R _{DS(ON)}	V_{GS} =4.5V, I_{D} =4.5A		26	33	mΩ	
Static Drain-Source On-State Re	esistance		V _{GS} =10V, I _D =4.5A		20	25		
DYNAMIC PARAMETERS								
Input Capacitance	nput Capacitance				690		pF	
Output Capacitance Reverse Transfer Capacitance		Coss	V _{GS} =0V, V _{DS} =10V, f=1.0MHz		120		pF	
		C_{RSS}			28		pF	
SWITCHING PARAMETERS								
Total Gate Charge		0	V _{DD} ≈24V, V _{GS} =10V, I _D =9A		9.5		nC	
		Q_{G}	V _{DD} ≈24V, V _{GS} =5V, I _D =9A		4.7		nC	
Gate to Source Charge		Q_GS	\/ ~24\/ \/ =10\/ =0A		2.2		nC	
Gate to Drain Charge		Q_GD	V _{DD} ≈24V, V _{GS} =10V, I _D =9A		0.9		nC	
Gate Resistance		R_G	V _{GS} =0V, V _{DS} =10V, f=5MHz	, V _{DS} =10V, f=5MHz		5.1	Ω	
Turn-ON Delay Time		$t_{D(ON)}$			6.7		ns	
Rise Time		t_R			2.1		ns	
Turn-OFF Delay Time		t _{D(OFF)}			15		ns	
Fall-Time		t _F			2.1		ns	
SOURCE- DRAIN DIODE RATI	NGS AND	CHARACTERI	STICS					
Drain-Source Diode Forward Vo	Itage	V_{SD}	I _{SD} =9A, V _{GS} =0V			-1.2	V	
Continuous Drain-Source Current		I _{SD}				36	Α	

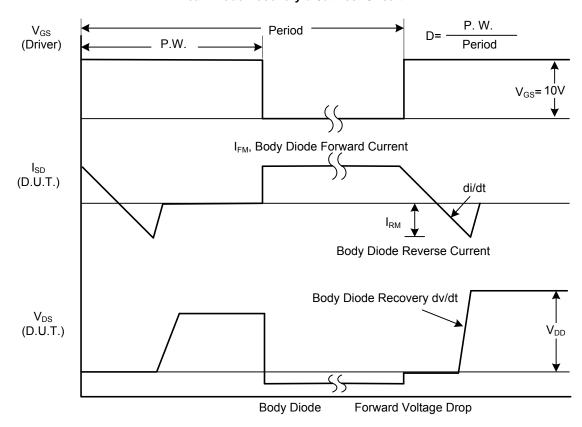
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

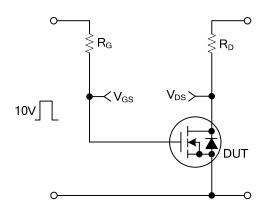


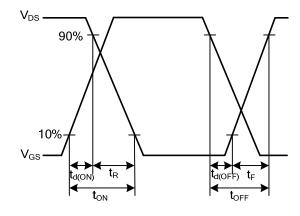
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

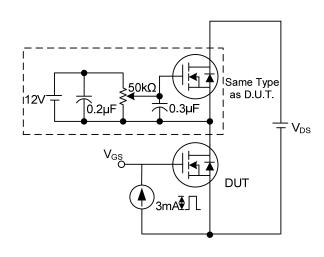
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

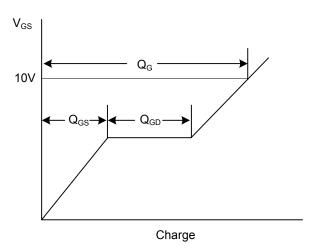




itching Test Circuit

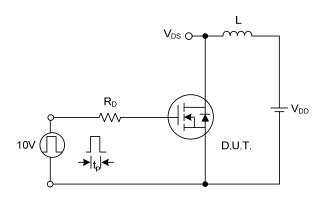
Switching Waveforms

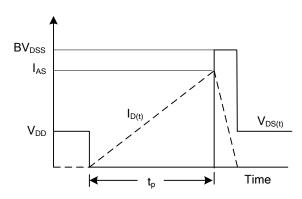




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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