

**40V PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR**

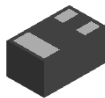
**Features**

- Complementary NPN Type Available (MMBT3904LP)
- Ultra-Small Leadless Surface Mount Package
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- ESD rating: 200V-MM, 4KV-HBM

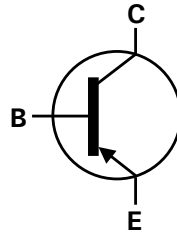
**Mechanical Data**

- Case: DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (approximate)

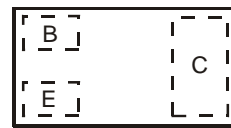
DFN1006-3



Bottom View



Device Symbol



Top View  
Device Schematic

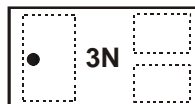
**Ordering Information**

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMBT3906LP-7	3N	7	8mm	3,000
MMBT3906LP-7B	3N	7	8mm	10,000

- Notes:
1. No purposefully added lead.
  2. Diodes Inc's "Green" policy can be found on our website at <http://www.diodes.com>.
  3. For packaging details, go to our website at <http://www.diodes.com>.

**Marking Information**

MMBT3906LP-7



Top View  
Dot Denotes  
Collector Side

MMBT3906LP-7B



Top View  
Bar Denotes Base  
and Emitter Side

3N = Product Type Marking Code

**Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-40	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-40	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	V
Collector Current - Continuous (Note 4)	I <sub>C</sub>	-200	mA

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P <sub>D</sub>	250	mW
Thermal Resistance, Junction to Ambient (Note 4)	R <sub>θJA</sub>	500	°C/W
Operating and Storage and Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes: 4. Device mounted on FR-4 PCB, pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com>

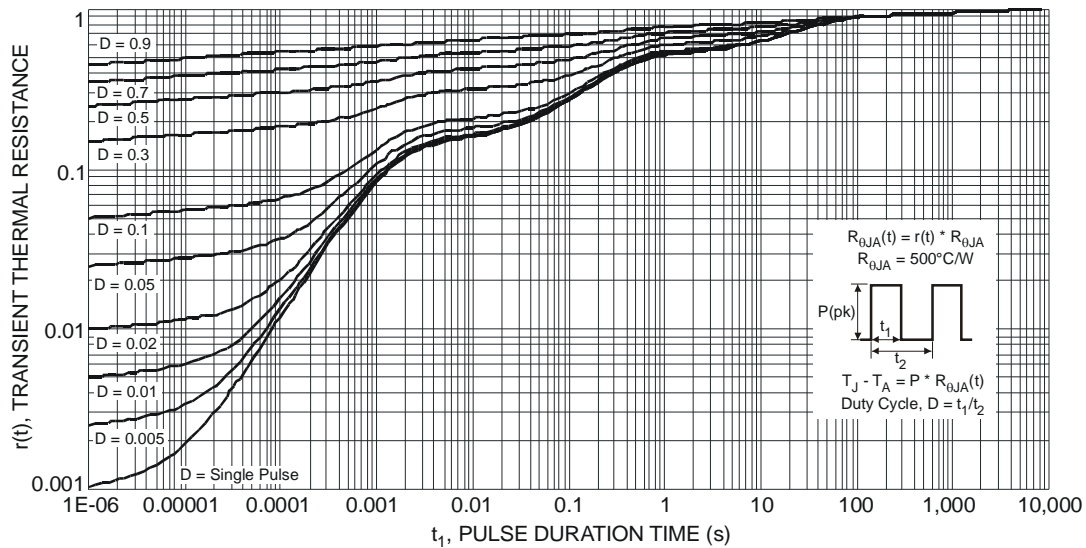


Fig. 1 Transient Thermal Response

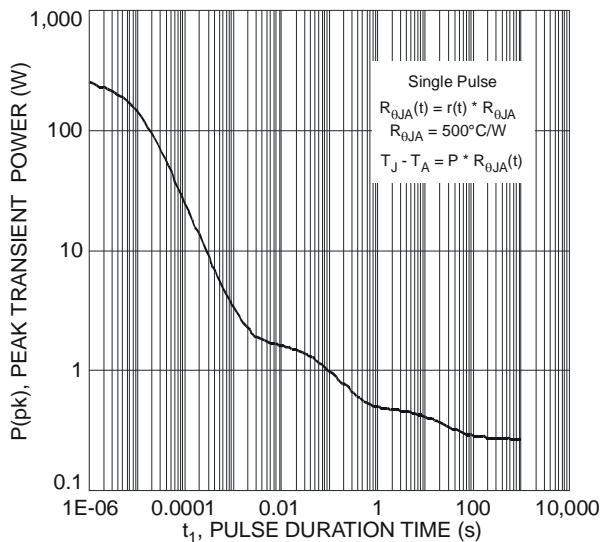


Fig. 2 Single Pulse Maximum Power Dissipation

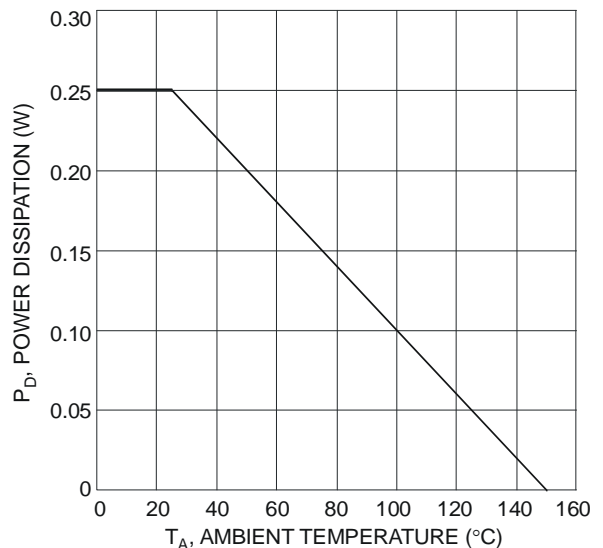


Fig. 3 Power Dissipation vs. Ambient Temperature

**Electrical Characteristics** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b>					
Collector-Base Breakdown Voltage	$BV_{CBO}$	-40	—	V	$I_C = -10\mu\text{A}, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 5)	$BV_{CEO}$	-40	—	V	$I_C = -1.0\text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$BV_{EBO}$	-5.0	—	V	$I_E = -10\mu\text{A}, I_C = 0$
Collector Cutoff Current	$I_{CEX}$	—	-50	nA	$V_{CE} = -30\text{V}, V_{EB(OFF)} = -3.0\text{V}$
	$I_{CBO}$	—	-50	nA	$V_{CB} = -30\text{V}, I_E = 0$
Base Cutoff Current	$I_{BL}$	—	-50	nA	$V_{CE} = -30\text{V}, V_{EB(OFF)} = -3.0\text{V}$
<b>ON CHARACTERISTICS (Note 5)</b>					
DC Current Gain	$h_{FE}$	60	—	—	$I_C = -100\mu\text{A}, V_{CE} = -1.0\text{V}$ $I_C = -1.0\text{mA}, V_{CE} = -1.0\text{V}$ $I_C = -10\text{mA}, V_{CE} = -1.0\text{V}$ $I_C = -50\text{mA}, V_{CE} = -1.0\text{V}$ $I_C = -100\text{mA}, V_{CE} = -1.0\text{V}$
		80	—		
		100	300		
		60	—		
		30	—		
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	-0.25 -0.40	V	$I_C = -10\text{mA}, I_B = -1.0\text{mA}$ $I_C = -50\text{mA}, I_B = -5.0\text{mA}$
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	-0.65	-0.85 -0.95	V	$I_C = -10\text{mA}, I_B = -1.0\text{mA}$ $I_C = -50\text{mA}, I_B = -5.0\text{mA}$
<b>SMALL SIGNAL CHARACTERISTICS</b>					
Output Capacitance	$C_{obo}$	—	4.5	pF	$V_{CB} = -5.0\text{V}, f = 1.0\text{MHz}, I_E = 0$
Input Capacitance	$C_{ibo}$	—	10	pF	$V_{EB} = -0.5\text{V}, f = 1.0\text{MHz}, I_C = 0$
Input Impedance	$h_{ie}$	2.0	12	$k\Omega$	$V_{CE} = 10\text{V}, I_C = 1.0\text{mA}, f = 1.0\text{kHz}$
Voltage Feedback Ratio	$h_{re}$	0.1	10	$\times 10^{-4}$	
Small Signal Current Gain	$h_{fe}$	100	400	—	
Output Admittance	$h_{oe}$	3.0	60	$\mu\text{S}$	
Current Gain-Bandwidth Product	$f_T$	300	—	MHz	
<b>SWITCHING CHARACTERISTICS</b>					
Delay Time	$t_d$	—	35	ns	$V_{CC} = -3.0\text{V}, I_C = -10\text{mA}, V_{BE(off)} = 0.5\text{V}, I_{B1} = -1.0\text{mA}$
Rise Time	$t_r$	—	35	ns	
Storage Time	$t_s$	—	225	ns	$V_{CC} = -3.0\text{V}, I_C = -10\text{mA}, I_{B1} = I_{B2} = -1.0\text{mA}$
Fall Time	$t_f$	—	75	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effect.

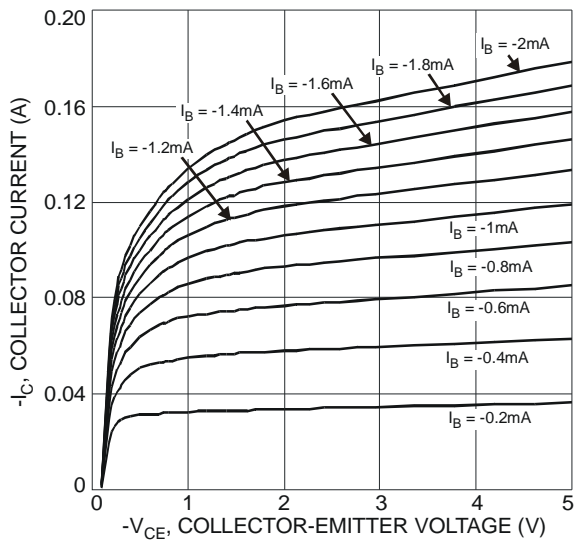


Fig. 4 Typical Collector Current vs. Collector-Emitter Voltage

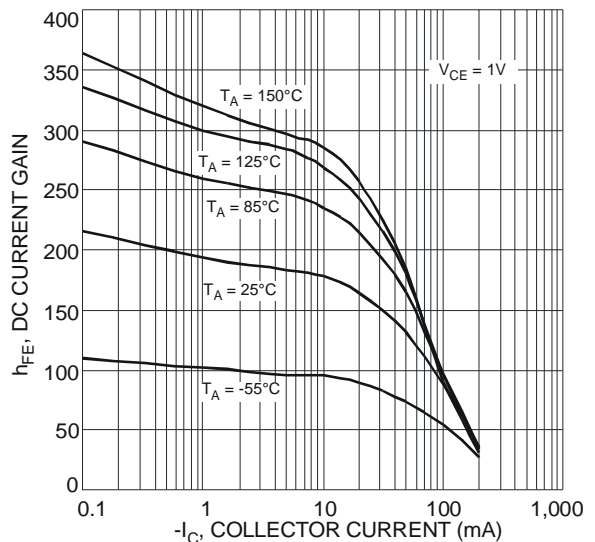


Fig. 5 Typical DC Current Gain vs. Collector Current

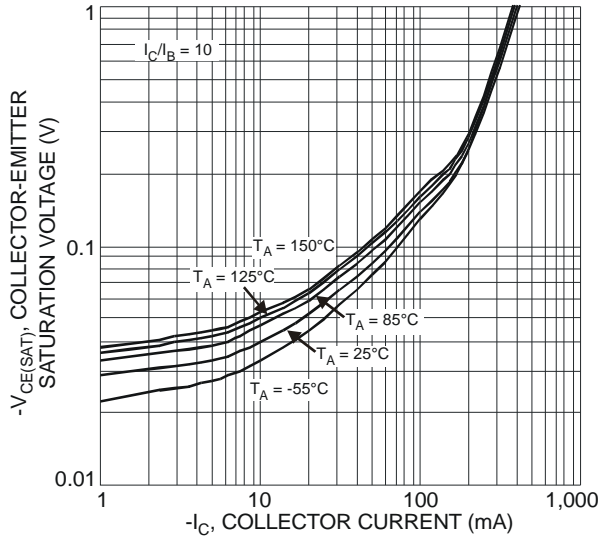


Fig. 6 Typical Collector-Emitter Saturation Voltage vs. Collector Current

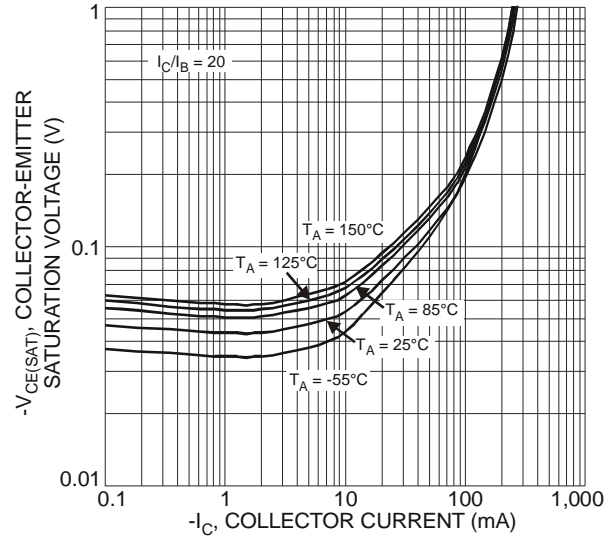


Fig. 7 Typical Collector-Emitter Saturation Voltage vs. Collector Current

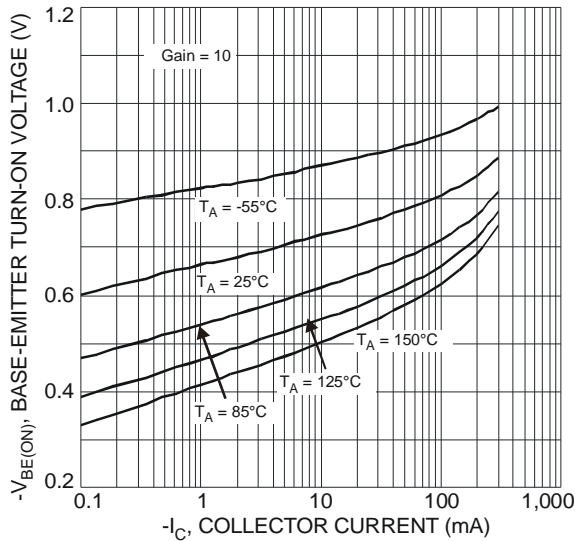


Fig. 8 Typical Base-Emitter Saturation Voltage vs. Collector Current

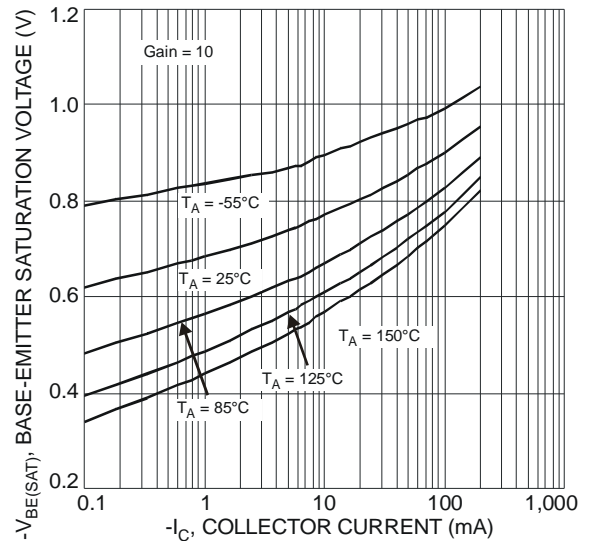
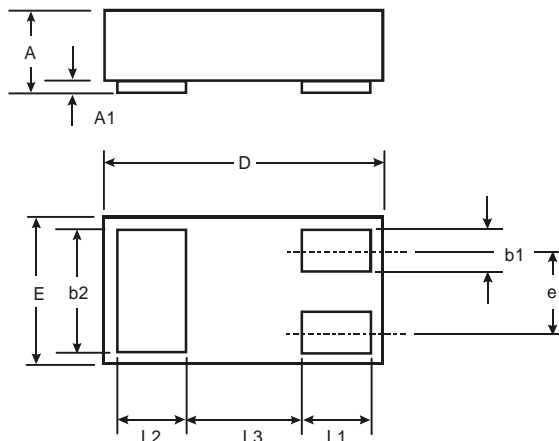


Fig. 9 Typical Base-Emitter Saturation Voltage vs. Collector Current

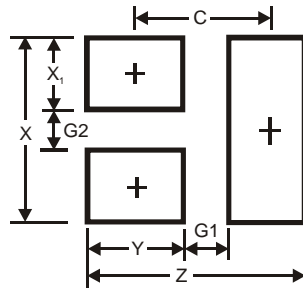
**Package Outline Dimensions**



DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b1	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	—	—	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	—	—	0.40

All Dimensions in mm

## Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
X	0.7
X1	0.25
Y	0.4
C	0.7

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