



US104S/N

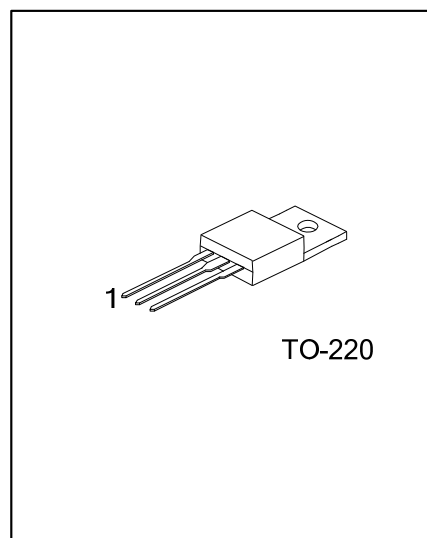
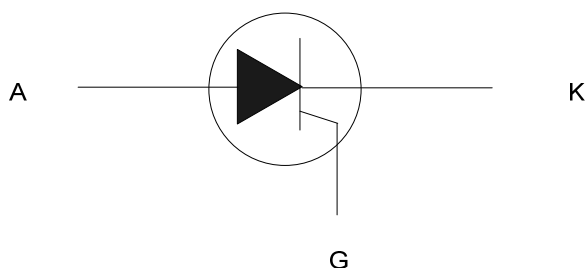
SCR

SCRS

DESCRIPTION

Thanks to highly sensitive triggering levels, the UTC **US104S** is suitable for all applications where the available gate current is limited, such as motor control for hand tools, kitchen aids, overvoltage crowbar protection for low power supplies, ... Available in through-hole or surface-mount packages, they provide an optimized performance in a limited space area.

SYMBOL



ORDERING INFORMATION

Order Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
US104SL-4-TA3-T	US104SG-4-TA3-T	TO-220	K	A	G	Tube
US104SL-6-TA3-T	US104SG-6-TA3-T	TO-220	K	A	G	Tube
US104SL-8-TA3-T	US104SG-8-TA3-T	TO-220	K	A	G	Tube
US104NL-4-TA3-T	US104NG-4-TA3-T	TO-220	K	A	G	Tube
US104NL-6-TA3-T	US104NG-6-TA3-T	TO-220	K	A	G	Tube
US104NL-8-TA3-T	US104NG-8-TA3-T	TO-220	K	A	G	Tube

Note: Pin Assignment: K: Cathode G: Gate A: Anode

US104SL-4-TA3-T	(1)Packing Type	(1) T: Tube
	(2)Package Type	(2) TA3: TO-220
	(3)Lead Free	(3) G: Halogen Free, L: Lead Free

■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Repetitive Peak Off-State Voltages And Repetitive Peak Reverse Voltage	US104S/N-4	400	V
	US104S/N-6	600	
	US104S/N-8	800	
RMS On-State Current (180° Conduction Angle) ($T_C = 115^\circ\text{C}$)	$I_{T(RMS)}$	4	A
Average On-State Current (180° Conduction Angle) ($T_C = 115^\circ\text{C}$)	$I_{T(AV)}$	2.5	A
Non Repetitive Surge Peak On-State Current ($T_J = 25^\circ\text{C}$)	$t_p = 8.3\text{ms}$	33	A
	$t_p = 10\text{ms}$	30	
I^2t Value For Fusing ($t_p = 10\text{ms}$, $T_J = 25^\circ\text{C}$)	I^2t	4.5	A^2S
Critical Rate Of Rise Of On-State Current ($I_G = 2 \times I_{GT}$, $t_r \leq 100\text{ns}$, $F = 60\text{Hz}$, $T_J = 125^\circ\text{C}$)	di/dt	50	$\text{A}/\mu\text{s}$
Peak Gate Current ($t_p = 20\mu\text{s}$, $T_J = 125^\circ\text{C}$)	I_{GM}	1.2	A
Average Gate Power Dissipation ($T_J = 125^\circ\text{C}$)	$P_{G(AV)}$	0.2	W
Storage Temperature	T_{STG}	$-40 \sim +150$	$^\circ\text{C}$
Junction Temperature	T_J	$-40 \sim +125$	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. The device is guaranteed to meet performance specification within $0^\circ\text{C} \sim 70^\circ\text{C}$ operating temperature range and assured by design from $-20^\circ\text{C} \sim 85^\circ\text{C}$.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JA}	60	K/W
Junction to Ambient	θ_{JC}	3.0	K/W

■ UTC US104S(SENSITIVE) ELECTRICAL CHARACTERISTICS

($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Trigger Current	I_{GT}	$V_D = 12\text{V}$, $R_L = 33\Omega$			200	mA
Gate Trigger Voltage	V_{GT}	$V_D = 12\text{V}$, $R_L = 33\Omega$			0.8	V
Gate Non-Trigger Voltage	V_{GD}	$V_D = V_{DRM}$, $R_L = 3.3\text{k}\Omega$ $R_{GK} = 220\Omega$, $T_J = 125^\circ\text{C}$	0.1			V
Reverse Gate Voltage	V_{RG}	$I_{RG} = 10\mu\text{A}$	8			V
Holding Current	I_H	$I_T = 50\text{mA}$, $R_{GK} = 1\text{k}\Omega$			5	mA
Latching Current	I_L	$I_G = 1\text{mA}$, $R_{GK} = 1\text{k}\Omega$			6	mA
Circuit Rate Of Change Of off-State Voltage	dV/dt	$V_D = 67\% V_{DRM}$, $R_{GK} = 220\Omega$, $T_J = 125^\circ\text{C}$	5			$\text{V}/\mu\text{s}$
On-State Voltage	V_{TM}	$I_{TM} = 8\text{A}$, $t_p = 380\mu\text{s}$, $T_J = 25^\circ\text{C}$			1.6	V
Threshold Voltage	V_{t0}	$T_J = 125^\circ\text{C}$			0.85	V
Dynamic Resistance	R_d	$T_J = 125^\circ\text{C}$			90	$\text{m}\Omega$
Off-State Leakage Current	I_{DRM}	$V_{DRM} = V_{RRM}$, $R_{GK} = 220\Omega$, $T_J = 25^\circ\text{C}$			5	μA
	I_{RRM}	$V_{DRM} = V_{RRM}$, $R_{GK} = 220\Omega$, $T_J = 125^\circ\text{C}$			1	mA

■ UTC US104N(SENSITIVE) ELECTRICAL CHARACTERISTICS

(T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Trigger Current	I _{GT}	V _D = 12 V, R _L = 33Ω	2		15	μA
Gate Trigger Voltage	V _{GT}	V _D = 12 V, R _L = 33Ω			1.3	V
Gate Non-Trigger Voltage	V _{GD}	V _D = V _{DRM} , R _L = 3.3 kΩ, T _J = 125°C	0.2			V
Holding Current	I _H	I _T = 100 mA Gate open			30	mA
Latching Current	I _L	I _G = 1.2 I _{GT}			60	mA
Circuit Rate Of Change Of off-State Voltage	dV/dt	V _D = 67 % V _{DRM} Gate open, T _J = 125°C	100			V/μs
On-State Voltage	V _{TM}	I _{TM} = 8A, t _p = 380 μs, T _J = 25°C			1.6	V
Threshold Voltage	V _{t0}	T _J = 125°C			0.85	V
Dynamic Resistance	R _d	T _J = 125°C			62	mΩ
Off-State Leakage Current	I _{DRM}	V _{DRM} = V _{RRM} , T _J = 25°C			5	μA
	I _{RRM}	V _{DRM} = V _{RRM} , T _J = 125°C			2	mA

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