



SIOV metal oxide varistors

Housed (ThermoFuse) varistors, AdvancedD series

Series/Type: **ETFV25**
Date: February 2010

Construction

- Round varistor element, leaded
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Terminals: tinned copper wire, metal compound wire
- Housing: thermoplastic, flame-retardant to UL 94 V-0

Features

- Wide operating voltage range 115 ... 420 V_{RMS}
- Self-protected under abnormal overvoltage conditions
- Very high surge current ratings of 20 kA

Approvals

- UL

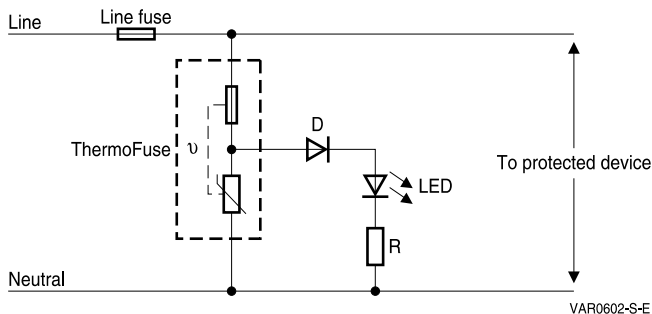
Applications

- Air conditioner, refrigerator, TV, etc.
- Power meter, inverter, telecom equipment, etc.
- Transient voltage surge suppressors (TVSS)

Delivery mode

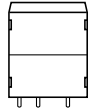
- Bulk (standard)

Typical applications



General technical data

Climatic category	to IEC 60068-1	40/85/56	
Operating temperature	to CECC 42 000	-40 ... + 85	°C
Storage temperature		-40 ... +125	°C
Electric strength	to CECC 42 000	≥ 2.5	kV _{RMS}
Response time		< 25	ns



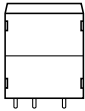
Electrical specifications and ordering codes

Maximum ratings ($T_A = 85\text{ °C}$)

Ordering code	Type (untaped) SIOV-	V_{RMS} V	V_{DC} V	i_{max} (8/20 μ s) A	W_{max} (2 ms) J	P_{max} W
B72225T4111K101	ETFV25K115E4	115	150	20000	170	1.0
B72225T4131K101	ETFV25K130E4	130	170	20000	185	1.0
B72225T4141K101	ETFV25K140E4	140	180	20000	195	1.0
B72225T4151K101	ETFV25K150E4	150	200	20000	215	1.0
B72225T4171K101	ETFV25K175E4	175	225	20000	245	1.0
B72225T4211K101	ETFV25K210E4	210	270	20000	290	1.0
B72225T4231K101	ETFV25K230E4	230	300	20000	315	1.0
B72225T4251K101	ETFV25K250E4	250	320	20000	345	1.0
B72225T4271K101	ETFV25K275E4	275	350	20000	375	1.0
B72225T4301K101	ETFV25K300E4	300	385	20000	410	1.0
B72225T4321K101	ETFV25K320E4	320	420	20000	445	1.0
B72225T4351K101	ETFV25K350E4	350	460	20000	495	1.0
B72225T4381K101	ETFV25K385E4	385	505	20000	600	1.0
B72225T4421K101	ETFV25K420E4	420	560	20000	700	1.0

Characteristics ($T_A = 25\text{ °C}$)

Ordering code	Type (untaped) SIOV-	V_v (1 mA) V	ΔV_v (1 mA) %	$V_{c,max}$ (i_c) V	i_c A	C_{typ} (1 kHz) pF
B72225T4111K101	ETFV25K115E4	180	± 10	300	150	2280
B72225T4131K101	ETFV25K130E4	205	± 10	340	150	2010
B72225T4141K101	ETFV25K140E4	220	± 10	360	150	1860
B72225T4151K101	ETFV25K150E4	240	± 10	395	150	1740
B72225T4171K101	ETFV25K175E4	270	± 10	455	150	1500
B72225T4211K101	ETFV25K210E4	330	± 10	545	150	1245
B72225T4231K101	ETFV25K230E4	360	± 10	595	150	1140
B72225T4251K101	ETFV25K250E4	390	± 10	650	150	1050
B72225T4271K101	ETFV25K275E4	430	± 10	710	150	945
B72225T4301K101	ETFV25K300E4	470	± 10	775	150	870
B72225T4321K101	ETFV25K320E4	510	± 10	840	150	810
B72225T4351K101	ETFV25K350E4	560	± 10	910	150	750
B72225T4381K101	ETFV25K385E4	620	± 10	1025	150	675
B72225T4421K101	ETFV25K420E4	680	± 10	1120	150	630

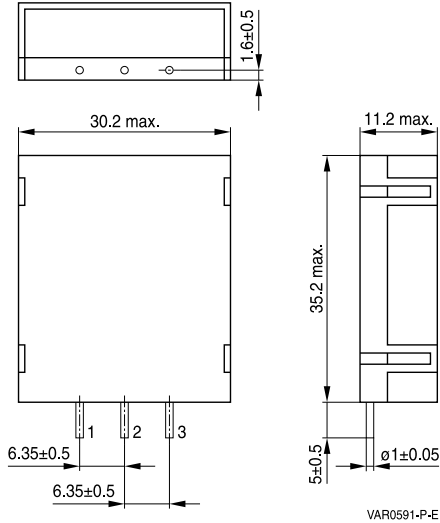


Housed varistors

ETFV25

ThermoFuse varistors, ETFV25 series

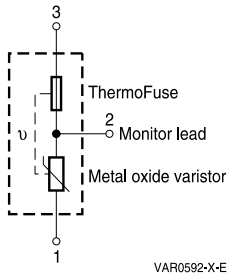
Dimensional drawings

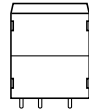


Weight

Nominal diameter mm	V _{RMS} V	Weight g
25	115 ... 420	13.3 ... 16.3

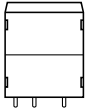
Lead configuration



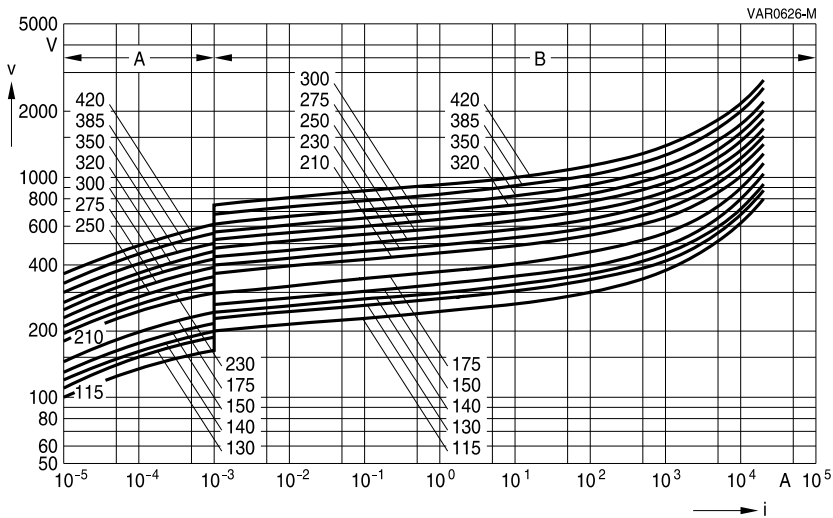


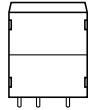
Reliability data

Test	Test methods/conditions	Requirement
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called V_V (1 mA _{DC} @ 0.2 ... 2 s).	To meet the specified value
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value
Surge current derating, 8/20 µs	CECC 42 000, test C 2.1 100 surge currents (8/20 µs), unipolar, interval 30 s, amplitude corresponding to derating curve for 100 impulses at 20 µs	$ \Delta V/V $ (1 mA) $\leq 10\%$ (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	CECC 42 000, test C 2.1 100 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 100 impulses at 2 ms	$ \Delta V/V $ (1 mA) $\leq 10\%$ (measured in direction of surge current) No visible damage
Abnormal overvoltage test	UL1449, limited current abnormal overvoltage test. Apply a high AC voltage to ThermoFuse varistor, the amplitude of overvoltage and current limit will be adopted from UL1449 general instruction in section 37. The specimen will be tested on a softwood surface covered with a double layer of white tissue paper. The specimen is to be loosely draped with a double layer of cheesecloth. The cheesecloth shall cover openings where flame, molten or other particles may be expelled as a result of the test. The test result will be visually inspected.	Any of these phenomena shall not be observed, or this specimen will be judged as failed part. 1. Emission of flame, molten metal, glowing or flaming particles through any openings (pre-existed or created as a result of the test) in the product. 2. Charring, glowing, or flaming of the supporting surface, tissue paper, or cheesecloth. 3. Ignition of the enclosure. 4. Creation of any openings in the enclosure that result in accessibility of live parts, when judged in accordance with accessibility of live parts, UL1449 section 13.


v/i characteristics

$v = f(i)$ for explanation of the characteristics refer to "General technical information", chapter 1.6.3
A = Leakage current, B = Protection level } for worst-case varistor tolerances

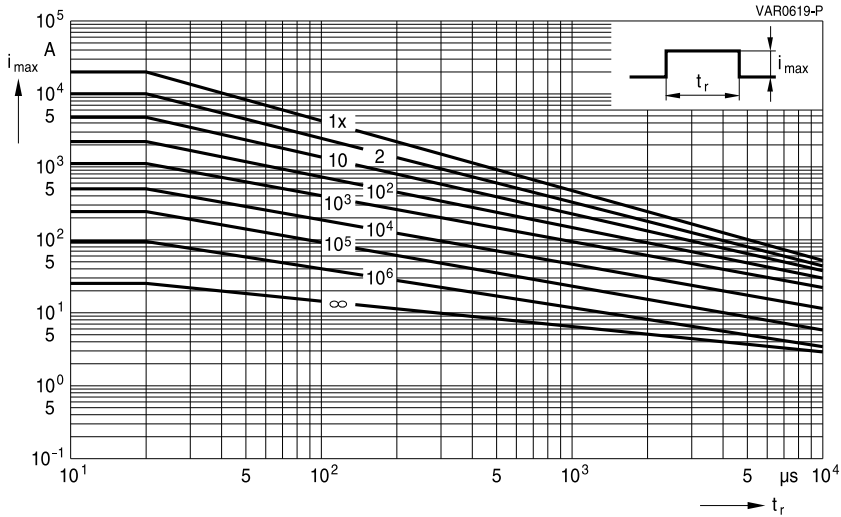

SIOV-ETFV25 ... E4



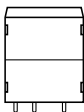
Derating curves

Maximum surge current $i_{\max} = f(t_r, \text{pulse train})$

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-ETFV25 ... E4



Cautions and warnings

General

1. EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

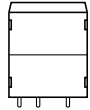
1. Store SIOVs only in original packaging. Do not open the package before storage.
2. Storage conditions in original packaging:
Storage temperature: $-25\text{ }^{\circ}\text{C} \dots +45\text{ }^{\circ}\text{C}$,
Relative humidity: $<75\%$ annual average,
 $<95\%$ on maximum 30 days a year.
Dew precipitation: Is to be avoided.
3. Avoid contamination of an SIOV's surface during storage, handling and processing.
4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
5. The SIOV type series should be soldered within the time specified:
SIOV-S, -Q, -LS 24 months
ETFV and SFS types 12 months.

Handling

1. SIOVs must not be dropped.
2. Components must not be touched with bare hands. Gloves are recommended.
3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

1. Use rosin-type flux or non-activated flux.
2. Insufficient preheating may cause ceramic cracks.
3. Rapid cooling by dipping in solvent is not recommended.
4. Complete removal of flux is recommended.



Mounting

1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

Operation

1. Use SIOVs only within the specified temperature operating range.
2. Use SIOVs only within the specified voltage and current ranges.
3. Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in the presence of deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Avoid contact with any liquids and solvents.

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